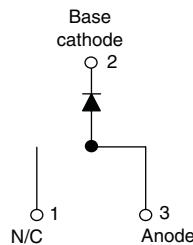


Schottky Rectifier, 15 A


 D²PAK


FEATURES

- 150 °C T_J operation
- Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for Q101 level

DESCRIPTION

The 15TQ060SPbF Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

PRODUCT SUMMARY

I _{F(AV)}	15 A
V _R	60 V

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
I _{F(AV)}	Rectangular waveform	15	A
V _{RRM}		60	V
I _{FSM}	t _p = 5 µs sine	1000	A
V _F	15 Apk, T _J = 125 °C	0.56	V
T _J	Range	- 55 to 150	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	15TQ060S	UNITS
Maximum DC reverse voltage	V _R		
Maximum working peak reverse voltage	V _{RWM}	60	V

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 104 °C, rectangular waveform	15	A
Maximum peak one cycle non-repetitive surge current See fig. 7	I _{FSM}	5 µs sine or 3 µs rect. pulse	1000	A
		10 ms sine or 6 ms rect. pulse	260	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.5 A, L = 11.5 mH	6	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 µs Frequency limited by T _J maximum V _A = 1.5 x V _R typical	1.50	A

ELECTRICAL SPECIFICATIONS

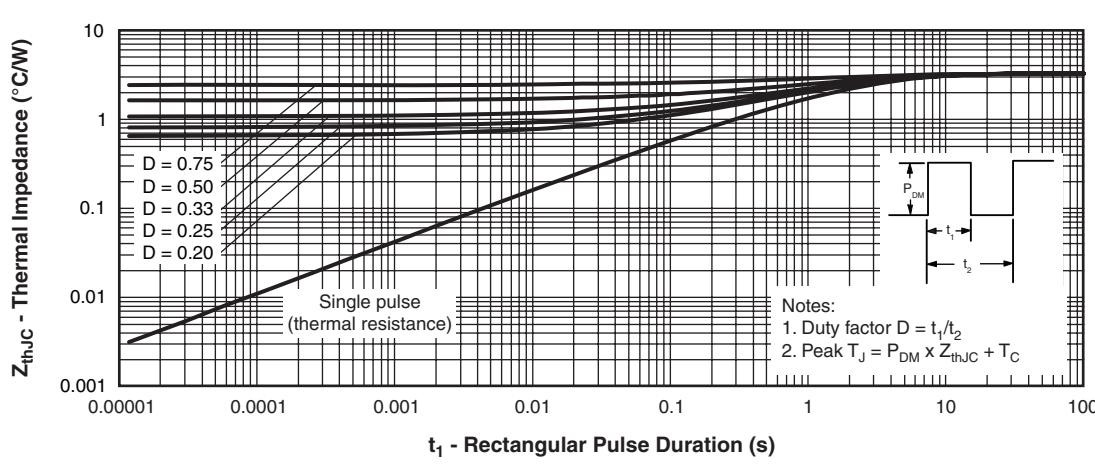
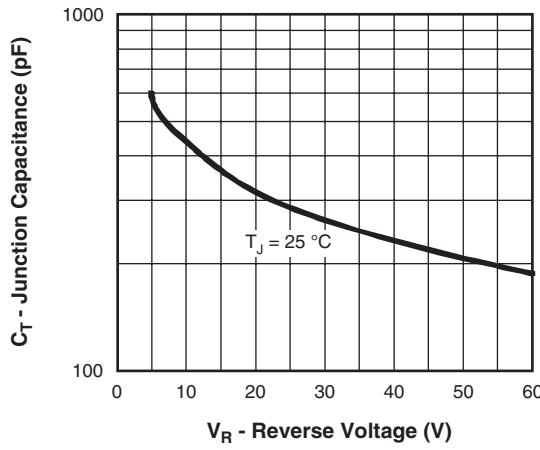
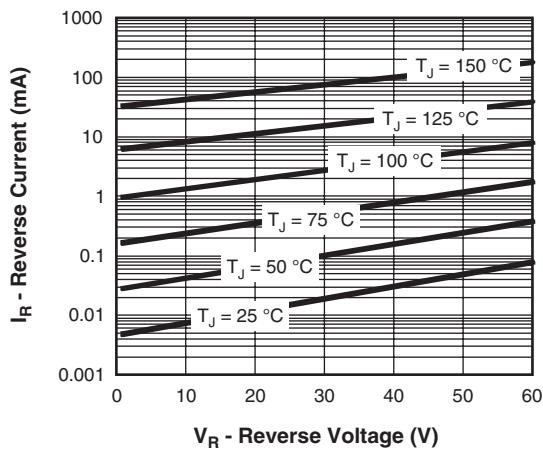
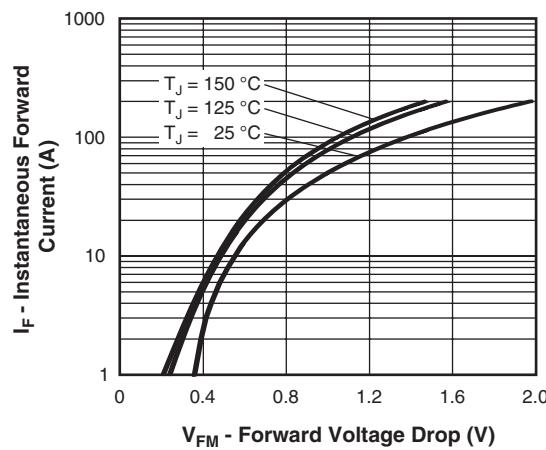
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	15 A	T _J = 25 °C	0.62	V	
		30 A		0.82		
		15 A	T _J = 125 °C	0.56		
		30 A		0.71		
Maximum reverse leakage current See fig. 2	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.80	mA	
		T _J = 125 °C		45		
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		720	pF	
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/μs	

Note

(1) Pulse width < 300 μs, duty cycle < 2 %

 THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	3.25	°C/W
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.50	
Approximate weight			2	g
			0.07	oz.
Mounting torque	minimum maximum		6 (5)	kgf · cm (lbf · in)
			12 (10)	
Marking device		Case style D ² PAK	15TQ060S	



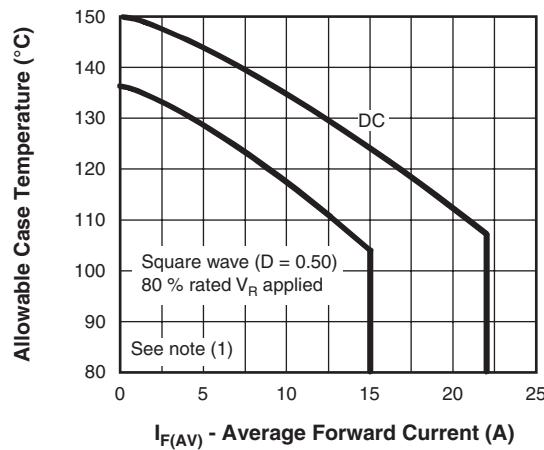


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

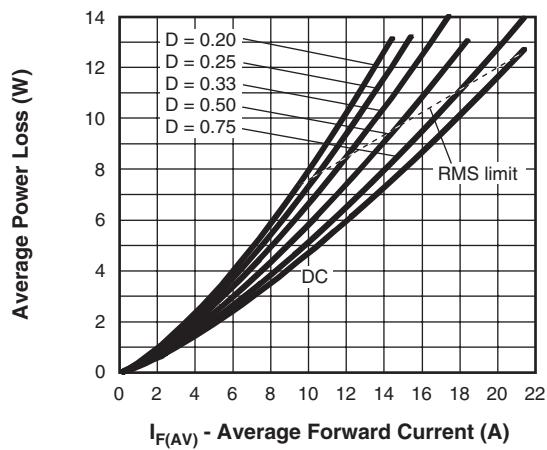


Fig. 6 - Forward Power Loss Characteristics

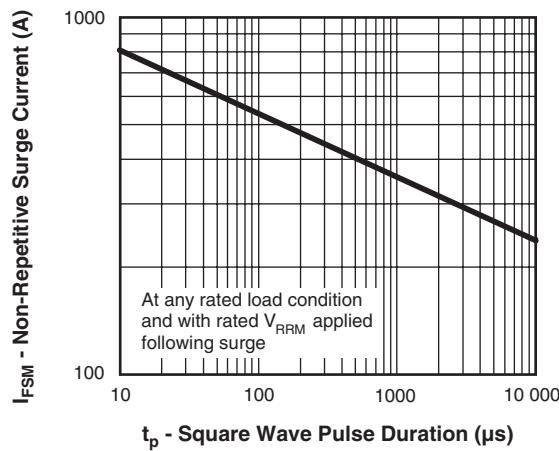


Fig. 7 - Maximum Non-Repetitive Surge Current

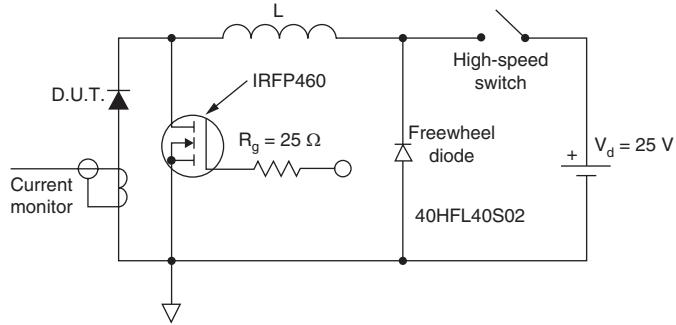


Fig. 8 - Unclamped Inductive Test Circuit

Note

(1) Formula used: $T_C = T_J - (P_d + P_{d,REV}) \times R_{thJC}$
 $P_d = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6);}$
 $P_{d,REV} = \text{Inverse power loss} = V_{R1} \times I_R (1 - D); I_R \text{ at } V_{R1} = 80\% \text{ rated } V_R$

ORDERING INFORMATION TABLE

Device code	15	T	Q	060	S	TRL	-
	(1)	(2)	(3)	(4)	(5)	(6)	(7)

- 1** - Current rating (15 A)
- 2** - Circuit configuration:
T = TO-220
- 3** - Schottky "Q" series
- 4** - Voltage rating (060 = 60 V)
- 5** - • S = D²PAK
- 6** - • None = Tube (50 pieces)
• TRL = Tape and reel (left oriented)
• TRR = Tape and reel (right oriented)
- 7** - • None = Standard production
• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS	
Dimensions	http://www.vishay.com/doc?95014
Part marking information	http://www.vishay.com/doc?95008
Packaging information	http://www.vishay.com/doc?95032

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