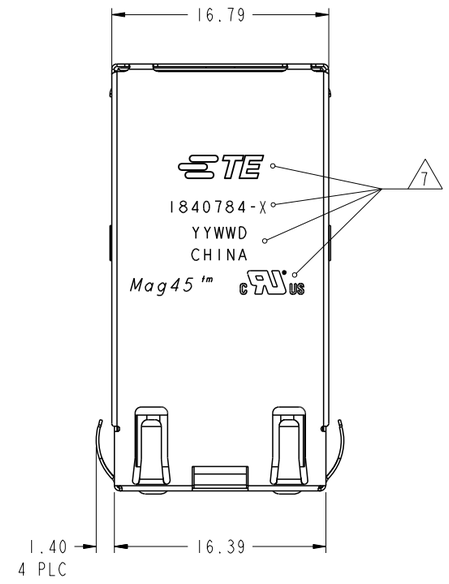
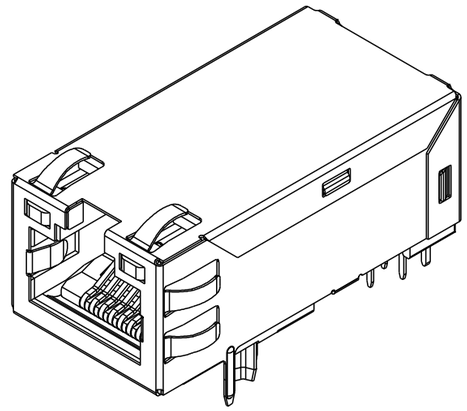
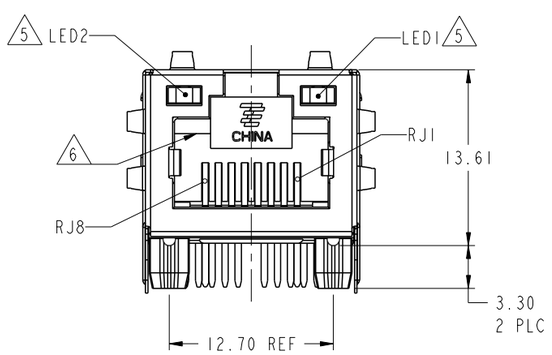
LOC	DIST
AA	00

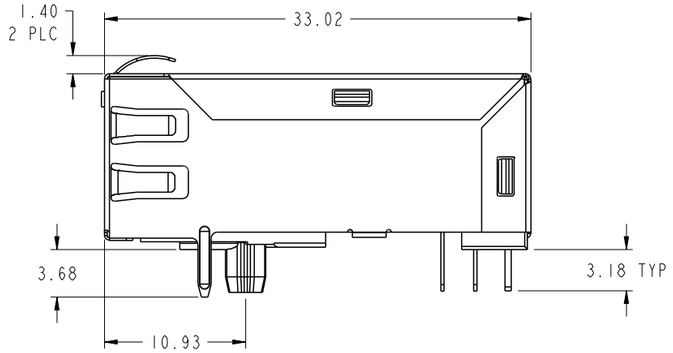
REVISIONS

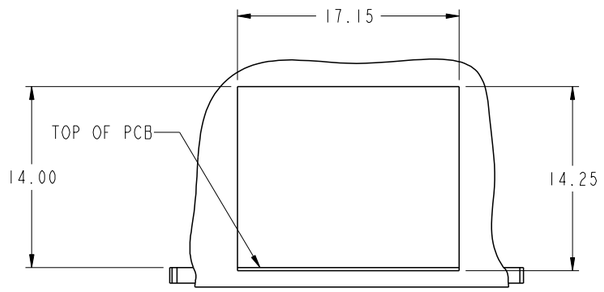
P	LTR	DESCRIPTION	DATE	DWN	APVD
	7	ECO-11-006110	08MAR2011	PP	KZ
	8	ECO-11-019412	05MAR2012	PP	LJ



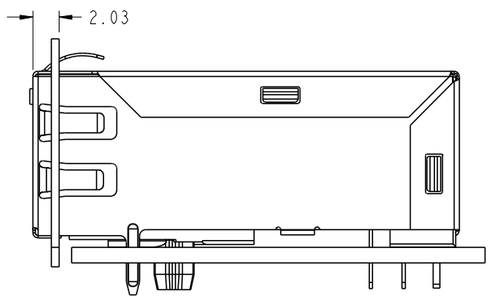








SUGGESTED PANEL CUTOUT
VS CUSTOMER BOARD



CONNECTOR ASSEMBLED
TO PANEL AND PCB

1 MATERIALS
HOUSING: THERMOPLASTIC, BLACK, FLAMMABILITY RATING UL 94V-0
SHIELD: BRASS, 0.20mm THICK,PREPLATED WITH MIN 0.76um SEMI-BRIGHT NICKEL
POST DIPPED WITH 2.54um MIN SAC SOLDER AT GROUND PINS.
CONTACT: PHOSPHOR BRONZE, 0.46mm X0.30mm
WITH 1.27um MIN OVERALL NICKEL UNDERPLATE AND
SELECTIVE 1.27um MIN GOLD PLATING AT MATING INTERFACE
SOLDER TAIL: 0.25mm THICK, PHOSPHOR BRONZE, 1.27µm MIN OVERALL NICKEL
UNDERPLATE, 3µm MIN TIN PLATE
LIGHT PIPE: POLYCARBON, TRANSPARENT, RATING UL 94V-0
LED: DIFFUSED EPOXY LENS, CARBON STEEL WIREFRAME LEADS
PREPLATED WITH 2.03um MIN SILVER OVER 1.02um MIN NICKEL UNDERPLATE
OVER 1.02um MIN COPPER UNDERPLATE;POST PLATED WITH 2.54um MIN MATTE TIN.

2 MAGNETICS
-APPLICATION: 10/100/1000 BASE-T
-IMPEDANCE: 100 OHMS
-TURNS RATIO (CHIP:CABLE): 1:1 ALL FOUR PAIRS
-OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.1VRMS,
8mA DC BIAS FROM 0°C TO 70°C, ALL FOUR PAIRS
-ALL FOUR PAIRS BI-DIRECTIONAL
-PERFORMANCE @ 25°C:
INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz
12-20LOG(f/80)dB MIN FROM 40.1MHz TO 100MHz
CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
-ISOLATION VOLTAGE: 2250VDC (MAX) FOR 60 SECONDS WITH A RISE TIME OF 500V/SEC

3 THE MAGNETIC CIRCUIT ON ALL FOUR CHANNELS IS SYMMETRICAL AND SUPPORTS AUTO-MDIX OPERATION.

4. OPERATING TEMPERATURE: 0°C ~ 70°C

5 LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX 20mA.
LED COLOR: DOMINANT WAVELENGTH (λD): GREEN 568 nm TYP. at IF=20mA
FORWARD VOLTAGE (VF): GREEN 2.2V TYP. at IF=20mA
DOMINANT WAVELENGTH (λD): ORANGE 605 nm TYP. at IF=20mA
FORWARD VOLTAGE (VF): ORANGE 2.1V TYP. at IF=20mA

6 RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.

7 TE CONNECTIVITY LOGO, PART NUMBER, DATE CODE, COUNTRY OF ORIGIN, AGENCY APPROVAL MARKING IN APPROPRIATE LOCATION SHOWN.
DATE CODE: YYWWD "YY" IS YEAR, "WW" IS WORK WEEK, "D" IS DAY OF WEEK, WITH SUNDAY=1.

8. THESE PART ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK WAVE SOLDERING TEMPERATURE IS 260°C, 10sec MAX.

THIS PRODUCT HAS NOT COMPLETED
VALIDATION/QUALIFICATION TESTING

∅0.90±0.05	GREEN	GREEN/ORANGE	1-1840784-1
∅1.14±0.05	GREEN	GREEN/ORANGE	1840784-1
DIM A	LED1	LED2	PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT.

DIMENSIONS: mm

TOLERANCES UNLESS OTHERWISE SPECIFIED:
0 PLC ±-
1 PLC ±-
2 PLC ±0.25
3 PLC ±-
4 PLC ±-
ANGLES ±-

MATERIAL 1 -

FINISH 1 -

DWN ALEX WANG 23-FEB-10
CHK TONG YUAN 23-FEB-10
APVD KEITH ZHU 23-FEB-10
PRODUCT SPEC -
APPLICATION SPEC -
WEIGHT -

TE Connectivity

NAME 1X1 MAG45(TM) MODULAR JACK 7G41 GIGABIT CIRCUIT SHIELDED WITH LEDS

SIZE	CAGE CODE	DRAWING NO	RESTRICTED TO
A2	00779	C-1840784	-

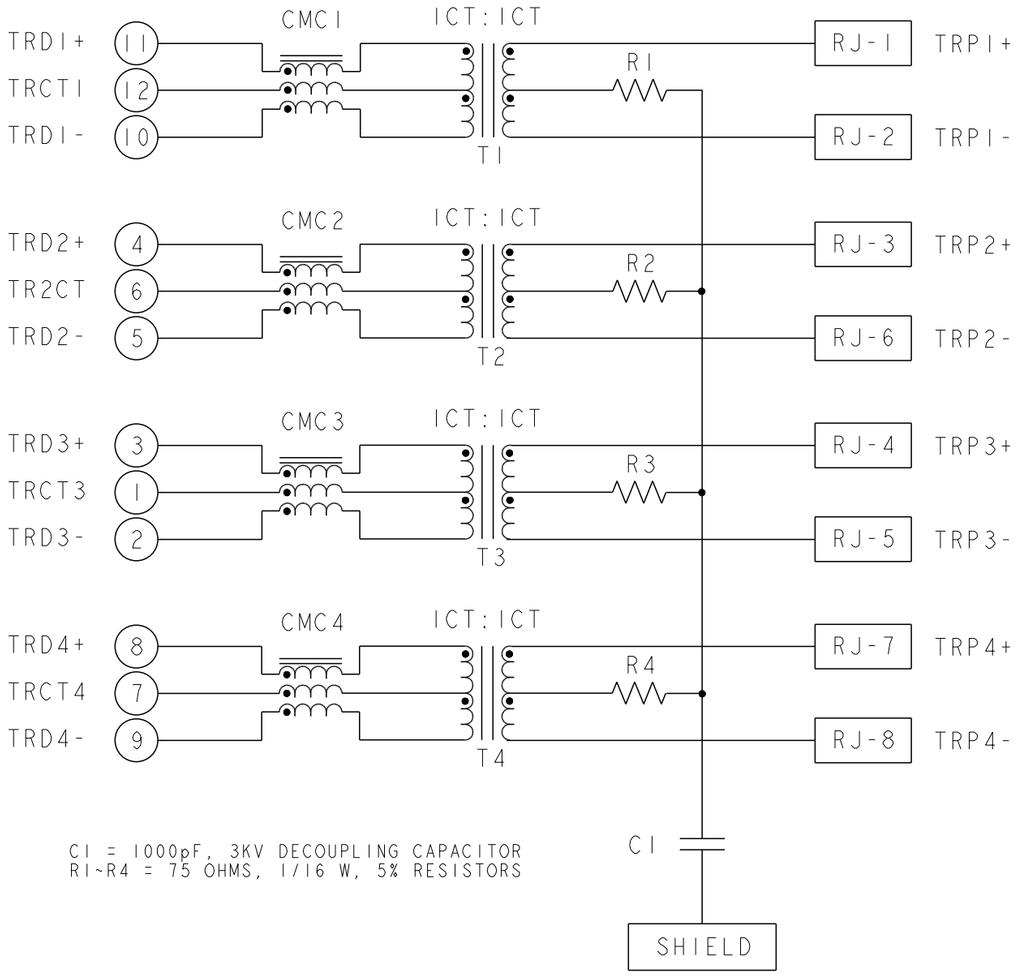
CUSTOMER DRAWING | SCALE NTS | SHEET 1 OF 3 | REV 8

LOC	DIST
AA	00

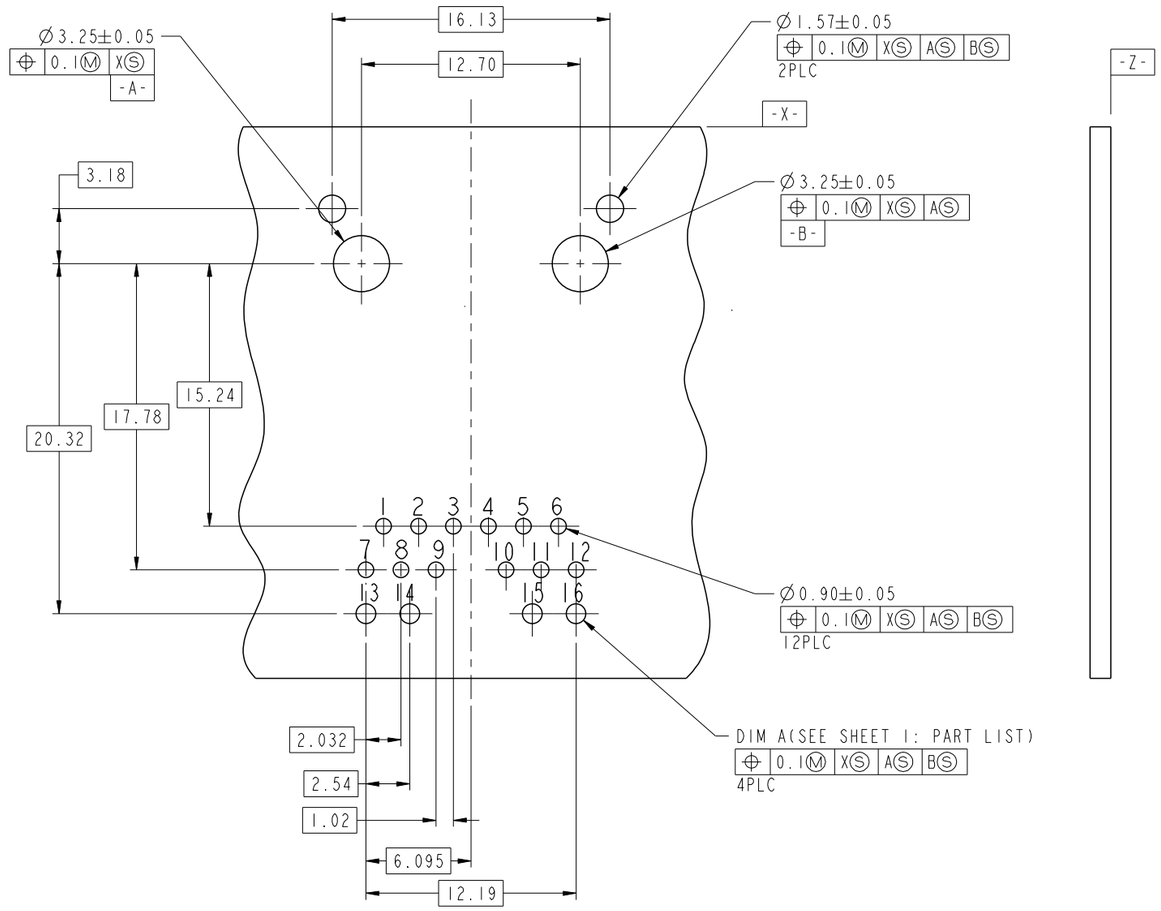
REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1	-	-	-

7G41 GIGABIT CIRCUIT △2 △3

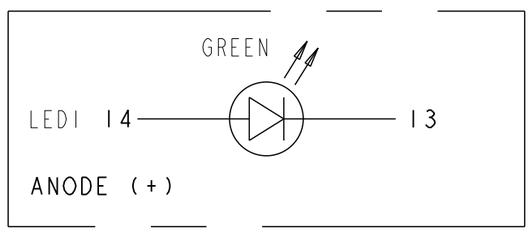


C1 = 1000pF, 3KV DECOUPLING CAPACITOR
R1~R4 = 75 OHMS, 1/16 W, 5% RESISTORS

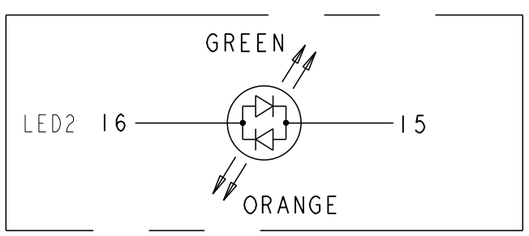


SUGGESTED CUSTOMER PCB
COMPONENT SIDE
SCALE 4:1

SINGLE COLOR LED



BI-COLOR LED



LED CONFIGURATION

THIS PRODUCT HAS NOT COMPLETED
VALIDATION/QUALIFICATION TESTING

THIS DRAWING IS A CONTROLLED DOCUMENT.

DIMENSIONS: mm

TOLERANCES UNLESS OTHERWISE SPECIFIED:
0 PLC ±-
1 PLC ±-
2 PLC ±0.25
3 PLC ±-
4 PLC ±-
ANGLES ±-

MATERIAL -
FINISH -

DWN ALEX WANG 23-FEB-10
CHK TONG YUAN 23-FEB-10
APVD KEITH ZHU 23-FEB-10
PRODUCT SPEC -
APPLICATION SPEC -
WEIGHT -

TE Connectivity

NAME
1X1 MAG45(TM) MODULAR JACK
7G41 GIGABIT CIRCUIT
SHIELDED WITH LEDS

SIZE	CAGE CODE	DRAWING NO	RESTRICTED TO
A2	00779	C-1840784	-

CUSTOMER DRAWING | SCALE NTS | SHEET 2 OF 3 | REV 8

LOC	DIST
AA	00

REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1	-	-	-

1840784

1X1 MAG(45) MODULAR JACK, 7G41 GIGABIT CIRCUIT, SHIELDED WITH LEDS

REVISION HISTORY

STOCK DISPOSITION

A: CONTINUE AS IS
B: REWORK
C: SCRAP
D: USE OLD FIRST
E: PARTS TO CONFORM
F: OTHER

DATE	REV	CHANGES	REASON	APPROVAL	INVENTORY (A,B,C,D,E,F)	WORK IN PROCESS (A,B,C,F)	SUPPLIER ORDERS (A,E,F)	OPEN CUST ORDERS (A,E,F)
23FEB2010	1	INITIAL RELEASE	INITIAL RELEASE	KZ	-	-	-	-
25FEB2010	2	SHEET 1 (D2): CHANGE THE STAMP SHEET 2 (D2): CHANGE THE STAMP	DRAFT ERROR	KZ	-	-	-	-
02MAR2010	3	SHEET 2 (B2): CHANGE THE DIMENSION FROM 6.118 TO 6.095 ADD THE SHEET 3	COREECT DRAFT ERRORS	KZ	-	-	-	-
05MAR2010	4	SHEET 2:CHANGE THE CIRCUIT	CUSTOMER REQUIREMENT	KZ	-	-	-	-
12MAR2010	5	SHEET 2 (D3,D4,C3, C4,B3,B4) CHANGE TEH CIRCUIT FROM "1G27 GIGABIT" TO "7G41 GIGABIT"	CUSOTMER REQUIREMENT	KZ	-	-	-	-
26OCT2010	6	SHEET 1, D4: CHANGE DATE CODE TO "YYWWD". SHEET 1, NOTE 7, ADD DATE CODE: YYWWD "YY" IS YEAR, "WW" IS WORK WEEK, "D" IS DAY OF WEEK, WITH SUNDAY=1.	TO TRACK THE PRODUCT MORE ACCURATE	KZ	A	A	A	A
08MAR2011	7	SHEET 1,A1: ADD DASH NUMBER 1-1840784-1 FOR LED HOLE SIZE Ø0.9mm SHEET 2,B1:Ø1.14mm CHANGE TO OPTIONAL HOLE SIZE. SHEET 2,A4 & A3: UPDATED LED CONFIGURATION ACCORDING TO TE STANDARD. ALL SHEETS: TITLE BLOCK USE NEW LOGO FOR TE CONNECTIVITY.	RTS(4269191) REQUIREMENT AND TYCO ELECTRONICS CHANGE NAME	KZ	-	-	-	-

THIS PRODUCT HAS NOT COMPLETED VALIDATION/QUALIFICATION TESTING

THIS DRAWING IS A CONTROLLED DOCUMENT.

DIMENSIONS: mm

TOLERANCES UNLESS OTHERWISE SPECIFIED:
0 PLC ±-
1 PLC ±-
2 PLC ±0.25
3 PLC ±-
4 PLC ±-
ANGLES ±-

MATERIAL -
FINISH -

DWN ALEX WANG 23-FEB-10
CHK TONG YUAN 23-FEB-10
APVD KEITH ZHU 23-FEB-10
PRODUCT SPEC -
APPLICATION SPEC -
WEIGHT -

TE Connectivity

NAME 1X1 MAG45(TM) MODULAR JACK 7G41 GIGABIT CIRCUIT SHIELDED WITH LEDS

SIZE	CAGE CODE	DRAWING NO	RESTRICTED TO
A2	00779	C-1840784	-

CUSTOMER DRAWING | SCALE NTS | SHEET 3 OF 3 | REV 8