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### Vishay Semiconductors

# "Full Bridge" IGBT MTP (Warp Speed IGBT), 50 A



PRODUCT SUMMARY				
V <sub>CES</sub>	600 V			
I <sub>C</sub> DC	69 A			
$V_{CE(on)}$	2.22 V			
Speed	8 kHz to 30 kHz			
Package	MTP			
Circuit	Full bridge			

#### **FEATURES**

- Gen 4 warp speed IGBT technology
- HEXFRED® antiparallel diodes with ultrasoft reverse recovery



COMPLIANT

- Very low conduction and switching losses
- Optional SMT thermistor
- Al<sub>2</sub>O<sub>3</sub> DBC
- Very low stray inductance design for high speed operation
- Speed 8 kHz to 30 kHz > 20 kHz hard switching, > 200 kHz resonant mode
- UL approved file E78996
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **BENEFITS**

- · Optimized for welding, UPS and SMPS applications
- · Low EMI, requires less snubbing
- Direct mounting to heatsink
- PCB solderable terminals
- · Very low junction to case thermal resistance

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V <sub>CES</sub>		600	V	
Continuous collector current	,	T <sub>C</sub> = 25 °C	69		
Continuous collector current	I <sub>C</sub>	T <sub>C</sub> = 80 °C	46		
Pulsed collector current	I <sub>CM</sub>		200	A	
Peak switching current	I <sub>LM</sub>		200	A	
Diode continuous forward current	I <sub>F</sub>	T <sub>C</sub> = 100 °C	25		
Peak diode forward current	I <sub>FM</sub>		200		
Gate to emitter voltage	$V_{GE}$		± 20	V	
RMS isolation voltage	V <sub>ISOL</sub>	Any terminal to case, t = 1 minute	2500	V	
Maximum power dissipation	P <sub>D</sub>	T <sub>C</sub> = 25 °C	195	W	
per single İGBT		T <sub>C</sub> = 100 °C	78	VV	



<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V <sub>(BR)CES</sub>	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 250 μA	600	-	-	V
Temperature coefficient of breakdown voltage	$\Delta V_{(BR)CES}/\Delta T_{J}$	$V_{GE} = 0 \text{ V}, I_{C} = 4 \text{ mA} (25 ^{\circ}\text{C to } 125 ^{\circ}\text{C})$	-	+0.6	-	V/°C
Collector to emitter saturation voltage	V <sub>CE(on)</sub>	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 25 A	-	2.22	3.14	V
		V <sub>GE</sub> = 15 V, I <sub>C</sub> = 50 A	-	2.43	3.25	
		V <sub>GE</sub> = 15 V, I <sub>C</sub> = 25 A, T <sub>J</sub> = 150 °C	-	1.65	1.93	
		V <sub>GE</sub> = 15 V, I <sub>C</sub> = 50 A, T <sub>J</sub> = 150 °C	-	2.08	2.45	
Gate threshold voltage	V <sub>GE(th)</sub>	$V_{CE} = V_{GE}, I_{C} = 250 \mu A$	3	-	6	
Temperature coefficient of threshold voltage	$\Delta V_{GE(th)}/\Delta T_{J}$	V <sub>CE</sub> = V <sub>GE</sub> , I <sub>C</sub> = 250 μA (25 °C to 125 °C)	-	- 17	-	mV/°C
Transconductance	9 <sub>fe</sub>	$V_{CE} = 100 \text{ V}, I_{C} = 25 \text{ A}, PW = 80 \mu\text{s}$	-	43	-	S
Zero gate voltage collector current	I <sub>CES</sub> (1)	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 600 V, T <sub>J</sub> = 25 °C	-	-	250	μΑ
		V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 600 V, T <sub>J</sub> = 150 °C	-	-	10	mA
Gate to emitter leakage current	I <sub>GES</sub>	V <sub>GE</sub> = ± 20 V	-	-	± 250	nA
	V <sub>FM</sub>	I <sub>C</sub> = 25 A	-	1.36	1.64	V
		I <sub>C</sub> = 50 A	ı	1.57	1.93	
Diode forward voltage drop		I <sub>C</sub> = 25 A; T <sub>J</sub> = 150 °C	-	1.19	1.42	
		I <sub>C</sub> = 50 A; T <sub>J</sub> = 150 °C	-	1.48	1.80	

#### Note

<sup>(1)</sup> I<sub>CES</sub> includes also opposite leg overall leakage

<b>SWITCHING CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Total gate charge (turn-on)	Qg	I <sub>C</sub> = 25 A	-	175	263	
Gate to emitter charge (turn-on)	Q <sub>ge</sub>	V <sub>CC</sub> = 480 V	-	27	41	nC
Gate to collector charge (turn-on)	Q <sub>gc</sub>	V <sub>GE</sub> = 15 V	-	71	107	
Turn-on switching loss	E <sub>on</sub>	$R_a = 5 \Omega, I_C = 25 A$	-	0.13	0.20	· mJ
Turn-off switching loss	E <sub>off</sub>	V <sub>CC</sub> = 480 V	-	0.42	0.62	
Total switching loss	E <sub>tot</sub>	$V_{GE} = \pm 15 \text{ V}, T_J = 25 ^{\circ}\text{C}$	-	0.55	0.82	
Turn-on switching loss	E <sub>on</sub>	$R_g = 5 \Omega$ , $I_C = 25 A$ $V_{CC} = 480 V$ $V_{GE} = \pm 15 V$ , $T_J = 125 °C$	-	0.39	0.59	
Turn-off switching loss	E <sub>off</sub>		-	0.49	0.74	
Total switching loss	E <sub>tot</sub>		-	0.88	1.32	
Input capacitance	C <sub>ies</sub>	V <sub>GE</sub> = 0 V V <sub>CC</sub> = 30 V f = 1.0 MHz	-	3610	5415	
Output capacitance	C <sub>oes</sub>		-	714	1071	pF
Reverse transfer capacitance	C <sub>res</sub>		-	58	87	
Diode reverse recovery time	t <sub>rr</sub>	$V_R = 200 \text{ V};$ $I_C = 25 \text{ A};$ $dI/dt = 200 \text{ A/}\mu\text{s}$	-	50	-	ns
Diode peak reverse current	I <sub>rr</sub>		-	4.5	-	Α
Diode Recovery charge	Q <sub>rr</sub>		-	112	-	nC
Diode peak rate of fall of recovery during t <sub>b</sub>	dI <sub>(rec)M</sub> /dt		-	250	-	A/µs



THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature range	$T_J$		-40	-	150	°C
Storage temperature range	T <sub>Stg</sub>		-40	-	125	
Junction to case	R <sub>thJC</sub>		-	-	0.64	
Diode			-	-	0.9	°C/W
Case to sink per module	R <sub>thCS</sub>	Heatsink compound thermal conductivity = 1 W/mK	-	0.06	-	
Clearance (1)		Externel shortest distance in air between 2 terminals	5.5	-	-	
Creepage (1)		Shortest distance along external surface of the insulating material between 2 terminals	8	-	-	mm
Weight				66		g

#### Note

<sup>(1)</sup> Standard version only i.e. without optional thermistor

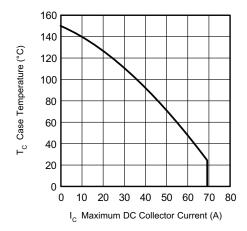


Fig. 1 - Maximum Collector Current vs. Case Temperature

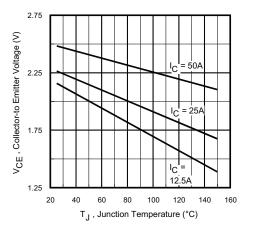


Fig. 2 - Typical Collector to Emitter Voltage vs. Junction Temperature

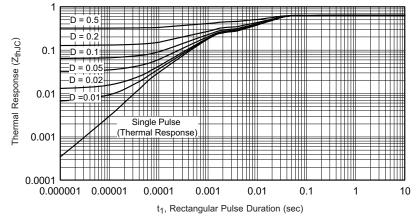


Fig. 3 - Maximum Transient Thermal Impedance, Junction to Case (IGBT)

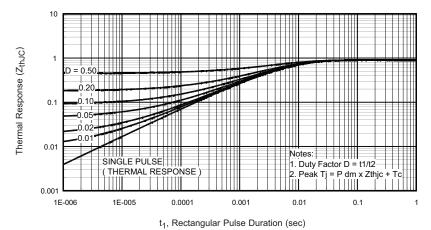


Fig. 4 - Maximum Transient Thermal Impedance, Junction to Case (Diode)

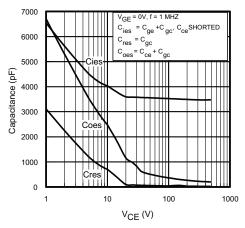


Fig. 5 - Typical Capacitance vs. Collector to Emitter Voltage

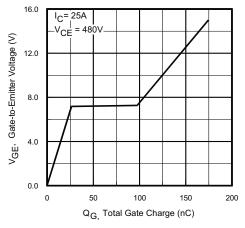


Fig. 6 - Typical Gate Charge vs. Gate to Emitter Voltage

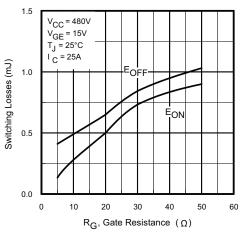


Fig. 7 - Typical Switching Losses vs. Gate Resistance

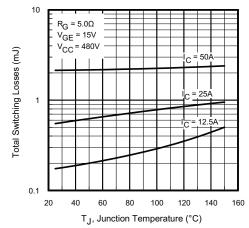


Fig. 8 - Typical Switching Losses vs. Junction Temperature



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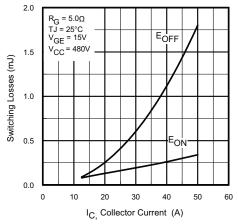
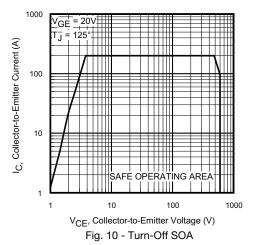


Fig. 9 - Typical Switching Losses vs. Collector to Emitter Current



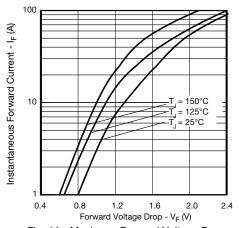


Fig. 11 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current

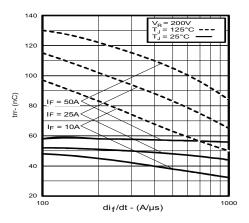


Fig. 12 - Typical Reverse Recovery Time vs. dI<sub>F</sub>/dt

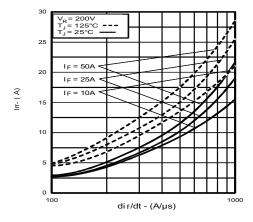


Fig. 13 - Typical Reverse Recovery Current vs. dI<sub>F</sub>/dt

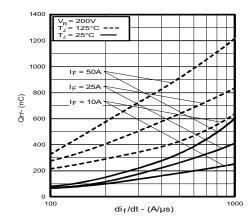


Fig. 14 - Typical Stored Charge vs. dl<sub>F</sub>/dt

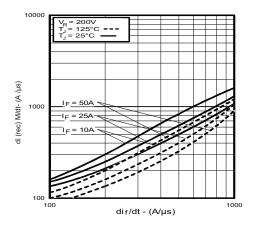


Fig. 15 - Typical  $dI_{(rec)M}/dt$  vs.  $dI_F/dt$ 

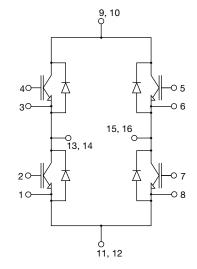
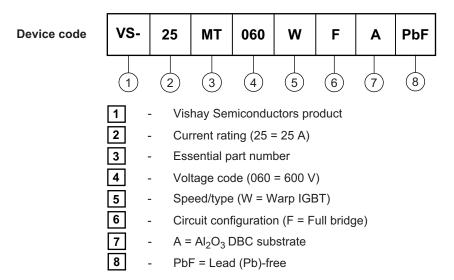


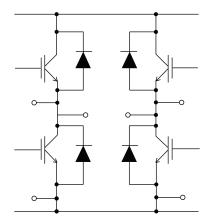
Fig. 16 - Electrical diagram

### **ORDERING INFORMATION TABLE**





### **CIRCUIT CONFIGURATION**

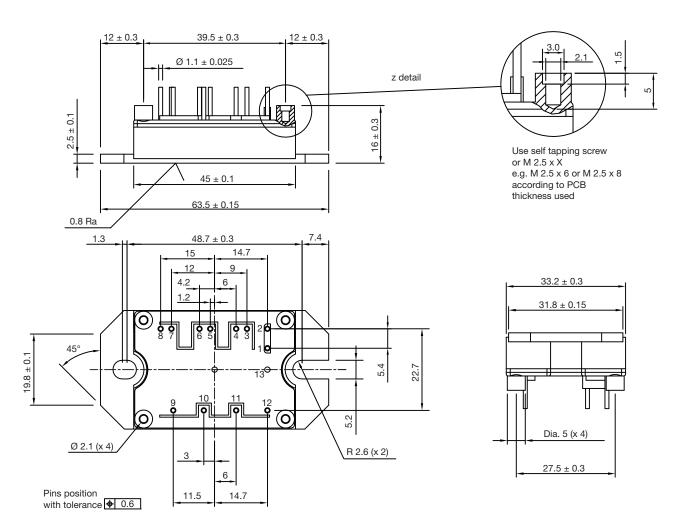


LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95245			



### **MTP**

#### **DIMENSIONS** in millimeters



### Note

• Unused terminals are not assembled in the package



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