

PHYSICAL DIMENSIONS:

A 3.05 [.120] $\pm$ 0.08 [.003]

B 2.54 [.100] $\pm$ 0.08 [.003]

B₁ 3.05 [.120] $\pm$ MAX

C 4.06 [.160] $\pm$ 0.15 [.006]

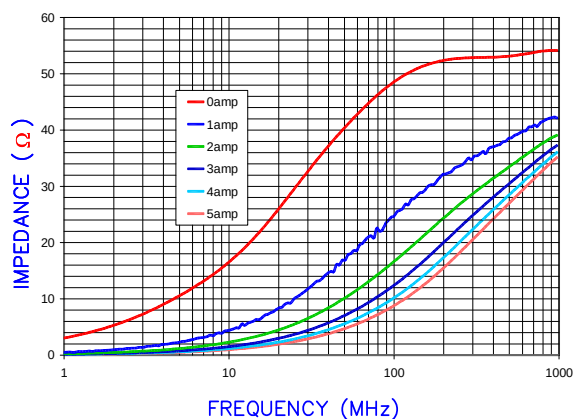
C₁ 5.08 [.200] $\pm$ MAX

WIRE DIMENSIONS:

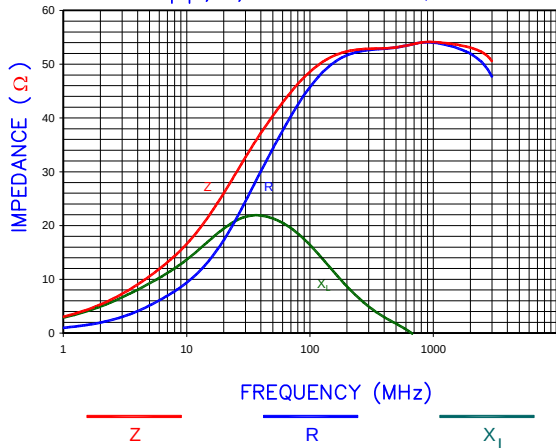
T₁ 1.27 [.050] $\pm$ 0.05 [.002]

T₂ 1.52 [.060] $\pm$ 0.51 [.020]

Z vs. FREQUENCY
IMPEDANCE UNDER DC BIAS

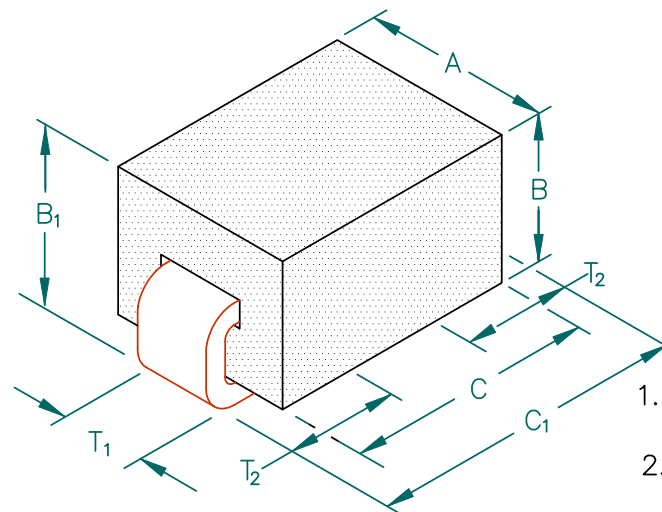


|Z|, R, AND X vs. FREQUENCY



AGILENT E4991A RF Impedance/Material Analyzer
HP 16194A Test Fixture. TEST REF. 3609

28F0121-0SR-10



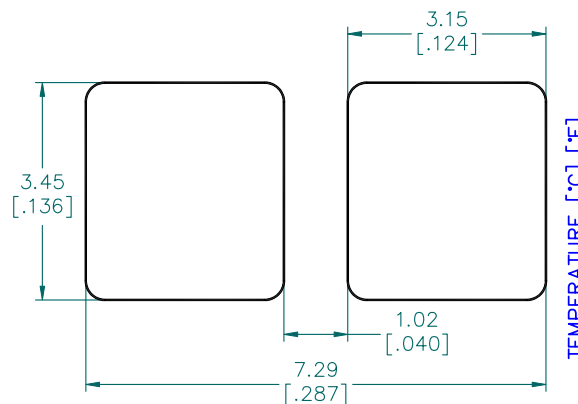
ELECTRICAL CHARACTERISTICS:

Z @ 100MHz (Ω)	DCR (m Ω)	Rated Current
Nominal	48	
Minimum	36	
Maximum	60	10,000 mA

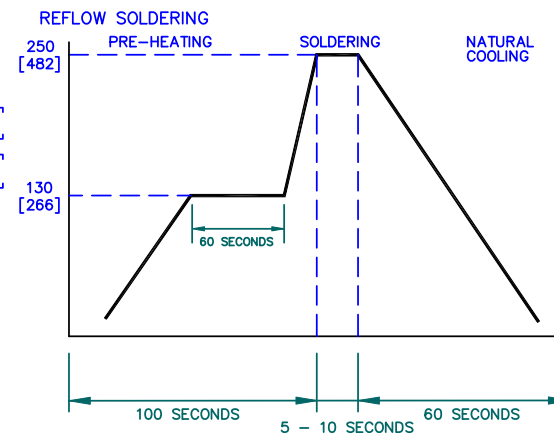
NOTES: UNLESS OTHERWISE SPECIFIED

1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 13" REELS, 2500 PCS/REEL.
2. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
3. REF. CARRIER TAPE SPECIFICATION # CART121-03.

LAND PATTERNS FOR REFLOW SOLDERING



RECOMMENDED SOLDERING CONDITIONS



UNCONTROLLED
DOCUMENT

DIMENSIONS ARE IN mm (INCHES).				THIS DOCUMENT CONTAINS INFORMATION WHICH IS PROPRIETARY AND CONFIDENTIAL TO STEWARD. NO PRODUCTION OR PUBLICATION OF THIS DOCUMENT IN WHOLE OR IN PART SHALL BE MADE WITHOUT WRITTEN AUTHORIZATION FROM STEWARD.			
REV	DESCRIPTION	DATE	INT	Steward			
D	CHANGE RATED CURRENT FROM 5000mA	02/17/04	JRK	PROJECT/PART NUMBER: 28F0121-0SR-10			
C	ADD DC BIAS CURVE UPDATE ZRX CURVE & DCR VALUE	08/29/03	JRK	DATE: 11/13/02	SCALE: NTS	SHEET: 2 of 3	
B	UPDATE REFLOW SOLDERING TEMPERATURE UPDATE NOTE # 5 ON SHT3	03/14/03	JRK	CAD #	TOOL #		
A	ORIGINAL DRAFT	11/13/02	JRK				