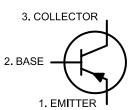


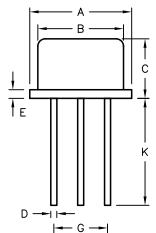
		REVISIONS	DOC. NO. SPC-F004 * Effective: 7/8/02 * DCP No: 1398							
DCP #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE		
1885	В	UPDATED TO ROHS COMPLIANCE	EO	02/03/06	но	2/6/06	НО	2/6/06		
1975	С	Temperature added	DCM	8/17/07	JWM	8/17/07	JWM	8/17/07		







Dimensions	A	В	C	D	E	F	G	H	J	K	L
Min.	8.50	7.74	6.09	0.40	_	2.41	4.82	0.71	0.73	12.70	42°
Max.	9.39	8.50	6.60	0.53	0.88	2.66	5.33	0.86	1.02	_	48°



1. EMITTER 2. BASE

3. COLLECTOR

Absolute Maximum Ratings:

- Collector-Emitter Voltage, $V_{CEO} = 75V$
- Collector-Base Voltage, V_{CBO} = 100V Emitter-Base Voltage, V_{EBO} = 7V Continuous Collector Current, I_{C} = 2A

- Base Current, I_B = 1A

- Base current, IB= IA

 Total Power Dissipation (T_C = +25°C), P_D= 10W

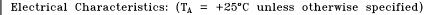
 Derate Above 25°C= 0.057nW/°C

 Operating Junction Temperature Range, T_J= -65° to +200°C

 Storage Temperature Range, T_{stg}= -65° to +200°C

 Thermal Resistance, Junction-to-Case, R_{thJC}= 17.5°C/W

- Solder Temp Rating: 235°C



Parameter	Symbol	Test Conditions	Min	Max	Unit
OFF Characteristics					
Collector—Emitter Breakdown Voltage	V _{(BR)CEO}	$I_{\rm C}=$ 100mA, $I_{\rm B}=$ 0	75	_	٧
Collector Cutoff Current	I _{CEX}	$V_{CE} = 100V, V_{BE} = 1.5V$	-	0.1	mΑ
		$V_{CE} = 70V, V_{BE} = 1.5V, T_{C} = +150^{\circ}C$	_	5	mΑ
Emitter Cutoff Current	I _{EBO}	$V_{BE} = 7V, I_{C} = 0$	-	0.1	mΑ
ON Characteristics (Note 1)					
DC Current Gain	h _{FE}	$I_{\rm C}$ = 500mA, $V_{\rm CE}$ = 4V	30	130	_
		I_{C} = 1A, V_{CE} = 2V	10	_	
Collector—Emitter Saturation Voltage	V _{CE(sat)}	$I_{\rm C}=$ 500mA, $I_{\rm B}=$ 50mA	-	0.7	٧
Base—Emitter ON Voltage	V _{BE(on)}	$I_{\rm C}$ = 500mA, $V_{\rm CE}$ = 4V	-	1.1	٧
Small-Signal Characteristics					
Small—Signal Current Gain	h _{fe}	$I_{\rm C}=$ 50mA, $V_{\rm CE}=$ 4V, f = 10MHz	5	_	_
Switching Characteristics	•		•		
Turn-On Time	ton	V_{CC} = 30V, I_{C} = 500mA, I_{B1} = 50mA	-	100	ns
Turn-Off Time	t _{off}	$V_{CC}=30V,\;I_{C}=500$ mA, $I_{B1}=I_{B2}=50$ mA	-	1000	ns

Note 1. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.

SPC-F004.DWG

TOLERANCES:	DRAWN BY:	DATE:		ING TITLE:				
UNLESS OTHERWISE	HISHAM ODISH	3/9/04		Transistor, G	eneral Purpose, F	PNP,	TO-39, Meta	l
SPECIFIED,	CHECKED BY:	DATE:	SIZE	DWG. NO.		ELEC	TRONIC FILE	REV
DIMENSIONS ARE FOR REFERENCE	JEFF MCVICKER	4/27/04	A	21	N5322	35	5C0722.DWG	C
PURPOSES ONLY.	APPROVED BY:	DATE:				<u> </u>		
	JOHN COLE	4/27/04	SCALI	E: NTS	U.O.M.: Millimeters		SHEET: 1 OF	- 1

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