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August 2016

2N7000 / 2N7002 / NDS7002A N-Channel Enhancement Mode Field Effect Transistor

Features

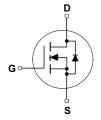
- High Density Cell Design for Low R_{DS(ON)}
- · Voltage Controlled Small Signal Switch
- · Rugged and Reliable
- High Saturation Current Capability

Description

These N-channel enhancement mode field effect transistors are produced using Fairchild's proprietary, high cell density, DMOS technology. These products have been designed to minimize on-state resistance while providing rugged, reliable, and fast switching performance. They can be used in most applications requiring up to 400 mA DC and can deliver pulsed currents up to 2 A. These products are particularly suited for low-voltage, low-current applications, such as small servo motor control, power MOSFET gate drivers, and other switching applications.







Ordering Information

Part Number	Marking	Package	Packing Method	Min Order Qty / Immediate Pack Qty
2N7000	2N7000	TO-92 3L	Bulk	10000 / 1000
2N7000_D74Z	2N7000	TO-92 3L	Ammo	2000 / 2000
2N7000_D75Z	2N7000	TO-92 3L	Tape and Reel	2000 / 2000
2N7000_D26Z	2N7000	TO-92 3L	Tape and Reel	2000 / 2000
2N7002	702	SOT-23 3L	Tape and Reel	3000 / 3000
NDS7002A	712	SOT-23 3L	Tape and Reel	3000 / 3000

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter		Linit		
Symbol	Parameter	2N7000	2N7002	NDS7002A	Unit
V_{DSS}	Drain-to-Source Voltage		60		V
V_{DGR}	Drain-Gate Voltage ($R_{GS} \le 1 \text{ M}\Omega$)		V		
V _{GSS}	Gate-Source Voltage - Continuous		V		
	Gate-Source Voltage - Non Repetitive (tp < 50 μS)				
I _D	Maximum Drain Current - Continuous	200	115	280	mA
	Maximum Drain Current - Pulsed	500	800	1500	
P _D	Maximum Power Dissipation Derated above 25°C	400	200	300	mW
		3.2	1.6	2.4	mW/°C
$T_{J,}T_{STG}$	Operating and Storage Temperature Range	-55 to 150 -65 to 150			°C
T _L	Maximum Lead Temperature for Soldering Purposes, 1/16-inch from Case for 10 Seconds	300			°C

Thermal Characteristics

Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter		Value		Unit
Symbol	Faiailletei	2N7000	2N7002	NDS7002A	Offic
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	312.5	625	417	°C/W

Electrical Characteristics

Values are at T_C = 25°C unless otherwise noted.

Symbol	Parameter	Conditions	Туре	Min.	Тур.	Max.	Unit	
Off Characteristics								
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 10 \mu A$	All	60			V	
I _{DSS}	Zero Gate Voltage Drain	V _{DS} = 48 V, V _{GS} = 0 V	2N7000			1	μА	
Current	Current	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}, $ $T_C = 125^{\circ}\text{C}$				1	mA	
		V _{DS} = 60 V, V _{GS} = 0 V	2N7002			1	μΑ	
		V _{DS} = 60 V, V _{GS} = 0 V, T _C = 125°C	NDS7002A			0.5	mA	
I _{GSSF}	Gate - Body Leakage, Forward	V _{GS} = 15 V, V _{DS} = 0 V	2N7000			10	nA	
		V _{GS} = 20 V, V _{DS} = 0 V	2N7002 NDS7002A			100	nA	
I _{GSSR}	Gate - Body Leakage, Reverse	$V_{GS} = -15 \text{ V}, V_{DS} = 0 \text{ V}$	2N7000			-10	nA	
		V _{GS} = -20 V, V _{DS} = 0 V	2N7002 NDS7002A			-100	nA	

Electrical Characteristics (Continued)

Symbol		Conditions	Туре	Min.	Тур.	Max.	Unit
	acteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 1 \text{ mA}$	2N7000	8.0	2.1	3	V
		$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2N7002 NDS7002A	1	2.1	2.5	
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 500 mA	2N7000		1.2	5	Ω
		V _{GS} = 10 V, I _D = 500 mA, T _C = 125°C			1.9	9	
		$V_{GS} = 4.5 \text{ V}, I_D = 75 \text{ mA}$			1.8	5.3	
		V _{GS} = 10 V, I _D = 500 mA	2N7002		1.2	7.5	
		$V_{GS} = 10 \text{ V},$ $I_D = 500 \text{ mA}, T_C = 100^{\circ}\text{C}$			1.7	13.5	
		$V_{GS} = 5 \text{ V},$ $I_D = 50 \text{ mA}$			1.7	7.5	
		$V_{GS} = 5 \text{ V},$ $I_D = 50 \text{ mA}, T_C = 100^{\circ}\text{C}$			2.4	13.5	
		V _{GS} = 10 V, I _D = 500 mA	NDS7002A		1.2	2	
		V _{GS} = 10 V, I _D = 500 mA, T _C = 125°C			2	3.5	
		$V_{GS} = 5 V$, $I_D = 50 \text{ mA}$			1.7	3	
		$V_{GS} = 5 \text{ V},$ $I_D = 50 \text{ mA}, T_C = 125^{\circ}\text{C}$			2.8	5	
V _{DS(ON)}	Drain-Source On-Voltage	V _{GS} = 10 V, I _D = 500 mA	2N7000		0.6	2.5	V
		$V_{GS} = 4.5 \text{ V},$ $I_D = 75 \text{ mA}$			0.14	0.4	
		$V_{GS} = 10 \text{ V},$ $I_D = 500 \text{ mA}$	2N7002 NDS7002A		0.6	3.75	
		V _{GS} = 5.0 V, I _D = 50 mA			0.09	1.5	
		$V_{GS} = 10 \text{ V},$ $I_{D} = 500 \text{ mA}$			0.6	1	
		V _{GS} = 5.0 V, I _D = 50 mA			0.09	0.15	
I _{D(ON)}	On-State Drain Current	$V_{GS} = 4.5 \text{ V},$ $V_{DS} = 10 \text{ V}$	2N7000	75	600		mA
		V_{GS} = 10 V, $V_{DS} \ge 2 V_{DS(on)}$	2N7002	500	2700		\prec
		V_{GS} = 10 V, $V_{DS} \ge 2 V_{DS(on)}$	NDS7002A	500	2700		
9 _{FS}	Forward Transconductance	V _{DS} = 10 V, I _D = 200 mA	2N7000	100	320		mS
		$\begin{aligned} &V_{DS} \geq 2V_{DS(ON)}, \\ &I_D = 200 \text{ mA} \end{aligned}$	2N7002	80	320		
		$V_{DS} \ge 2V_{DS(ON)},$ $I_D = 200 \text{ mA}$	NDS7002A	80	320		

Electrical Characteristics (Continued)

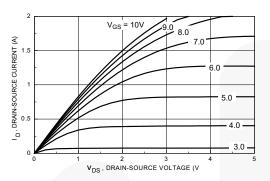
Symbol	Parameter	Conditions	Туре	Min.	Тур.	Max.	Unit
Dynamic	Characteristics		•	•			
C _{iss}	Input Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$	All		20	50	pF
C _{oss}	Output Capacitance	f = 1.0 MHz	All		11	25	
C _{rss}	Reverse Transfer Capacitance		All		4	5	
t _{on}	Turn-On Time	V_{DD} = 15 V, R _L = 25 Ω , I _D = 500 mA, V _{GS} = 10 V, R _{GEN} = 25 Ω	2N7000			10	ns
		V_{DD} = 30 V, R_{L} = 150 Ω , I_{D} = 200 mA, V_{GS} = 10 V, R_{GEN} = 25 Ω				20	
t _{off}	Turn-Off Time	V_{DD} = 15 V, R_{L} = 25 Ω , I_{D} = 500 mA, V_{GS} = 10 V, R_{GEN} = 25 Ω	2N7000			10	ns
		V_{DD} = 30 V, R_{L} = 150 Ω , I_{D} = 200 mA, V_{GS} = 10 V, R_{GEN} = 25 Ω				20	
Drain-Sc	ource Diode Characterist	ics and Maximum Rati	ngs				
I _S	Maximum Continuous Drain-	-Source Diode Forward	2N7002			115	mA
	Current		NDS7002A			280	
I _{SM}	Maximum Pulsed Drain-Sou	rce Diode Forward	2N7002		7	0.8	Α
	Current		NDS7002A			1.5	
V _{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V},$ $I_S = 115 \text{ mA}^{(1)}$	2N7002		0.88	1.5	V
		V _{GS} = 0 V, I _S = 400 mA ⁽¹⁾	NDS7002A		0.88	1.2	

Note:

1. Pulse test : Pulse Width ≤ 300 µs, Duty Cycel ≤ 2 %.

Typical Performance Characteristics

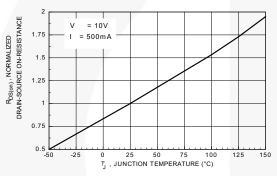
2N7000 / 2N7002 / NDS7002A



V =4.0V 4.5 5.0 6.0 7.0 WARNINGED 1.5 0.5 0.4 0.8 1.2 1.6 2 1.6 2 1.5 0.5 DRAIN CURRENT (A)

Figure 1. On-Region Characteristics

Figure 2. On-Resistance Variation with Gate Voltage and Drain Current



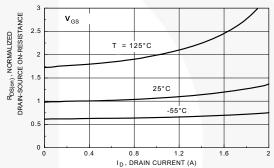
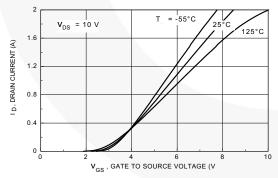


Figure 3. On-Resistance Variation with Temperature

Figure 4. On-Resistance Variation with Drain Current and Temperature



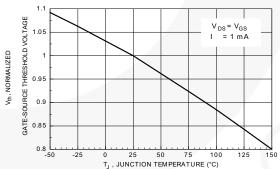
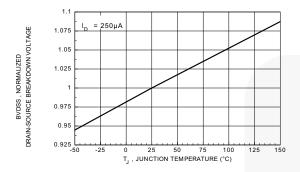


Figure 5. Transfer Characteristics

Figure 6. Gate Threshold Variation with Temperature

Typical Performance Characteristics (Continued)

2N7000 / 2N7002 / NDS7002A



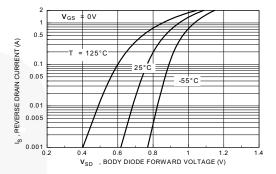


Figure 7. Breakdown Voltage Variation with Temperature

Figure 8. Body Diode Forward Voltage Variation with

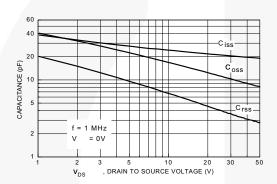


Figure 9. Capacitance Characteristics

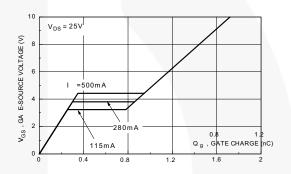


Figure 10. Gate Charge Characteristics

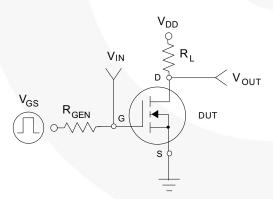


Figure 11. Switching Test Circuit

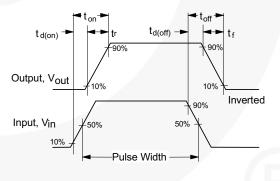


Figure 12. Switching Waveforms

Typical Performance Characteristics (Continued)

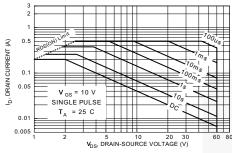


Figure 13. 2N7000 Maximum Safe Operating Area

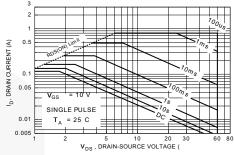


Figure 14. 2N7002 Maximum Safe Operating Area

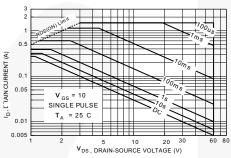
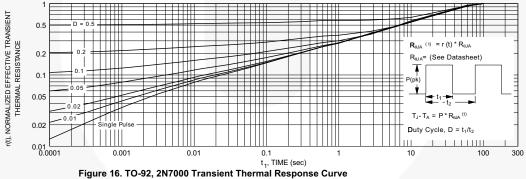


Figure 15. NDS7000A Maximum Safe Operating Area



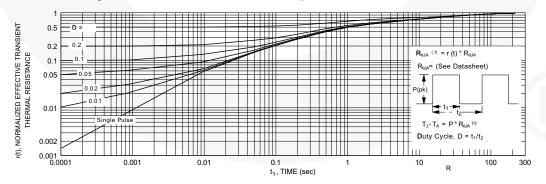
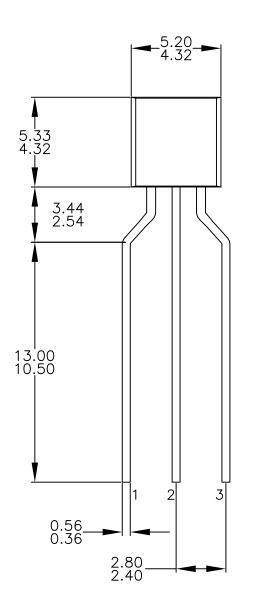
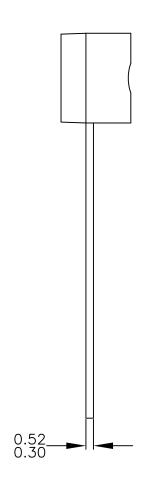
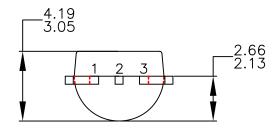


Figure 17. SOT-23, 2N7002 / NDS7002A Transient Thermal Response Curve

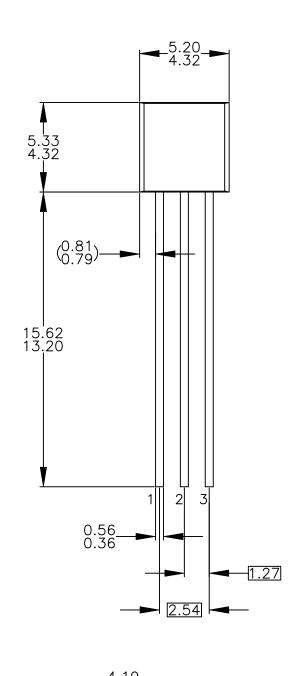


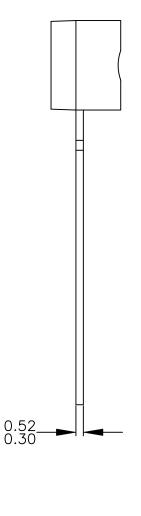




NOTES: UNLESS OTHERWISE SPECIFIED

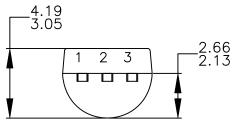
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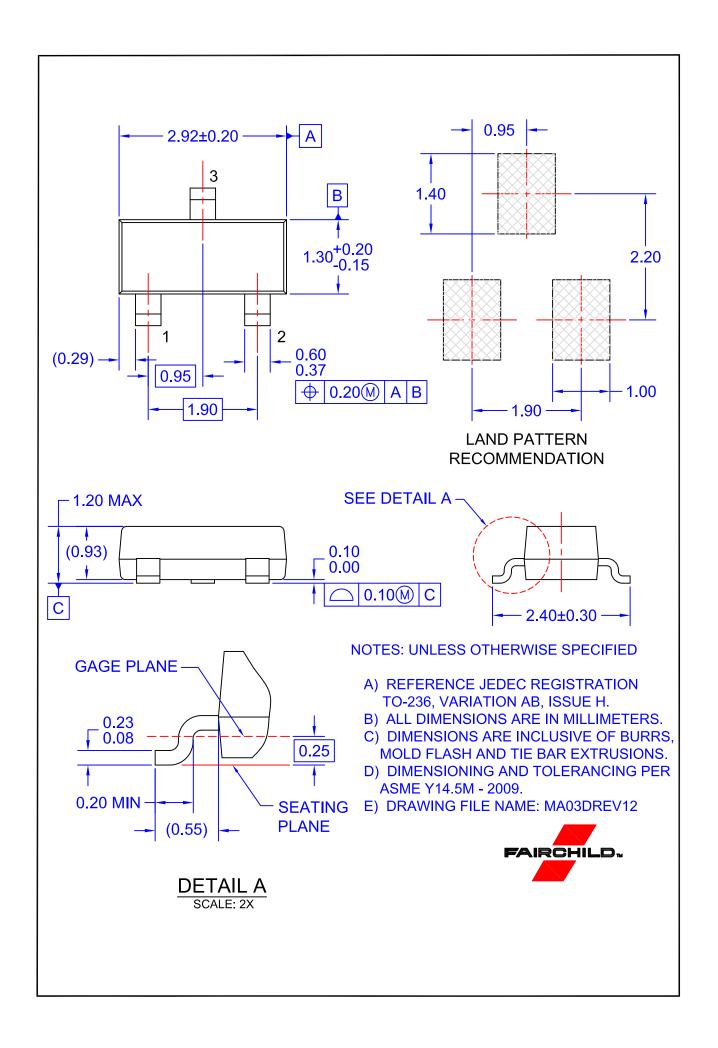


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