



2N7002T

#### N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

### **Features**

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- Lead Free/RoHS Compliant (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability
- "Green" Device (Notes 3 and 4)

### **Mechanical Data**

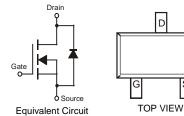
- Case: SOT-523
- Case Material: Molded Plastic. "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).

D

- Terminal Connections: See Diagram
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.002 grams (approximate)



TOP VIEW



### Maximum Ratings @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic		Symbol	Value	Units	
Drain-Source Voltage		$V_{DSS}$	60	V	
Drain-Gate Voltage R <sub>GS</sub> ≤ 1.0MΩ		$V_{DGR}$	60	V	
Gate-Source Voltage	Continuous Pulsed	$V_{GSS}$	±20 ±40	V	
Drain Current (Note 1)	Continuous Continuous @ 100°C Pulsed	I <sub>D</sub>	115 73 800	mA	

SOT-523

### Thermal Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 1)	$P_d$	150	mW
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	833	°C/W
Operating and Storage Temperature Range	T <sub>j</sub> , T <sub>STG</sub>	-55 to +150	°C

Notes:

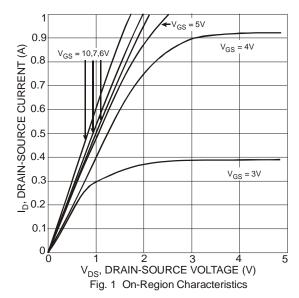
- 1. Device mounted on FR-4 PCB 1.0 x 0.75 x 0.062 inch pad layout as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 2. No purposefully added lead.
- Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.
- Product manufactured with Date Code UO (week 40, 2007) and newer are built with Green Molding Compound. Product manufactured prior to Date Code UO are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.



# Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 5)		-					
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	60	_	_	V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current	@ T <sub>C</sub> = 25°C @ T <sub>C</sub> = 125°C	I <sub>DSS</sub>		_	1.0 500	μA	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Body Leakage		I <sub>GSS</sub>	_	_	±10	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage		V <sub>GS(th)</sub>	1.0	_	2.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance	@ T <sub>j</sub> = 25°C @ T <sub>j</sub> = 125°C	R <sub>DS (ON)</sub>		2.0 4.4	7.5 13.5	Ω	$V_{GS} = 5.0V, I_D = 0.05A$ $V_{GS} = 10V, I_D = 0.5A$
On-State Drain Current		I <sub>D(ON)</sub>	0.5	1.0	_	Α	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 7.5V
Forward Transconductance		g <sub>FS</sub>	80	_	_	mS	V <sub>DS</sub> =10V, I <sub>D</sub> = 0.2A
DYNAMIC CHARACTERISTICS							
Input Capacitance		C <sub>iss</sub>		22	50	pF	
Output Capacitance		Coss		11	25	pF	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$
Reverse Transfer Capacitance		C <sub>rss</sub>	_	2.0	5.0	pF	
SWITCHING CHARACTERISTICS		•					
Turn-On Delay Time		t <sub>D(ON)</sub>		7.0	20	ns	$V_{DD} = 30V, I_D = 0.2A,$
Turn-Off Delay Time		t <sub>D(OFF)</sub>	_	11	20	ns	$R_L = 150\Omega$ , $V_{GEN} = 10V$ , $R_{GEN} = 25\Omega$

Notes: 5. Short duration pulse test used to minimize self-heating effect.



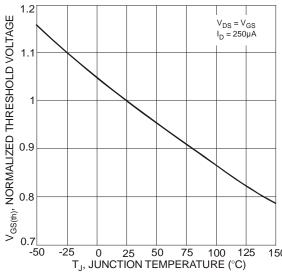


Fig. 3 Gate Threshold Variation with Temperature

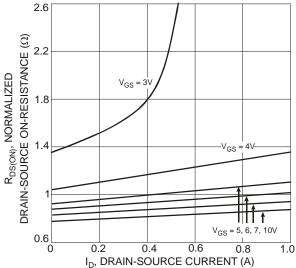


Fig. 2 On-Resistance Variation with Gate Voltage and Drain-Source Current

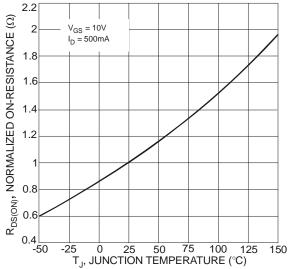
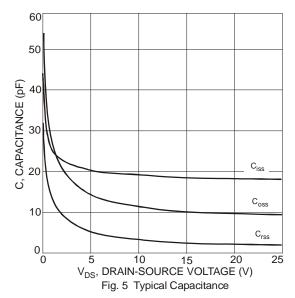
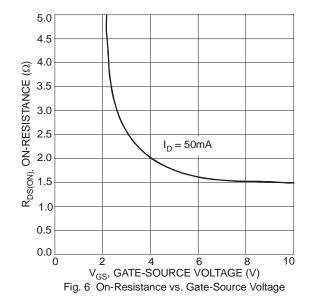


Fig. 4 On-Resistance Variation with Temperature





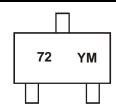


## Ordering Information (Note 6)

Part Number	Case	Packaging
2N7002T-7-F	SOT-523	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

# **Marking Information**



72 = Product Type Marking Code YM = Date Code Marking

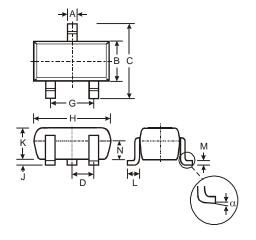
Y = Year (ex: T = 2006)

M = Month (ex: 9 = September)

Date Code Key

Year	2005		2006	2007		2008	2009		2010	2011		2012
Code	S		T	U		V	W		Χ	Υ		Z
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

# **Package Outline Dimensions**

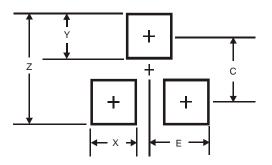


SOT-523					
Dim	Min	Max	Тур		
Α	0.15	0.30	0.22		
В	0.75	0.85	0.80		
C	1.45	1.75	1.60		
D		_	0.50		
G	0.90	1.10	1.00		
Η	1.50	1.70	1.60		
7	0.00	0.10	0.05		
K	0.60	0.80	0.75		
L	0.10	0.30	0.22		
М	0.10	0.20	0.12		
N	0.45	0.65	0.50		
α	0°	8°			
All	Dimens	ions in	mm		

COT FOO



### **Suggested Pad Layout**



Dimensions	Value (in mm)		
Z	1.8		
Х	0.4		
Υ	0.51		
С	1.3		
Е	0.7		

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