

# 74AUP2G80

Low-power dual D-type flip-flop; positive-edge trigger

Rev. 8 — 21 January 2013

Product data sheet

## 1. General description

The 74AUP2G80 provides the dual positive-edge triggered D-type flip-flop. Information on the data input is transferred to the  $\bar{Q}$  output on the LOW-to-HIGH transition of the clock pulse. The input pin D must be stable one setup time prior to the LOW-to-HIGH clock transition for predictable operation.

Schmitt trigger action at all inputs makes the circuit tolerant to slower input rise and fall times across the entire  $V_{CC}$  range from 0.8 V to 3.6 V.

This device ensures a very low static and dynamic power consumption across the entire  $V_{CC}$  range from 0.8 V to 3.6 V.

This device is fully specified for partial power-down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing a damaging backflow current through the device when it is powered down.

## 2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- Complies with JEDEC standards:
  - ◆ JESD8-12 (0.8 V to 1.3 V)
  - ◆ JESD8-11 (0.9 V to 1.65 V)
  - ◆ JESD8-7 (1.2 V to 1.95 V)
  - ◆ JESD8-5 (1.8 V to 2.7 V)
  - ◆ JESD8-B (2.7 V to 3.6 V)
- ESD protection:
  - ◆ HBM JESD22-A114F Class 3A exceeds 5000 V
  - ◆ MM JESD22-A115-A exceeds 200 V
  - ◆ CDM JESD22-C101E exceeds 1000 V
- Low static power consumption;  $I_{CC} = 0.9 \mu A$  (maximum)
- Latch-up performance exceeds 100 mA per JESD78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of  $V_{CC}$
- $I_{OFF}$  circuitry provides partial Power-down mode operation
- Multiple package options
- Specified from  $-40^{\circ}C$  to  $+85^{\circ}C$  and  $-40^{\circ}C$  to  $+125^{\circ}C$

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### 3. Ordering information

**Table 1. Ordering information**

Type number	Package				Version
	Temperature range	Name	Description	Version	
74AUP2G80DC	−40 °C to +125 °C	VSSOP8	plastic very thin shrink small outline package; 8 leads; body width 2.3 mm	SOT765-1	
74AUP2G80GT	−40 °C to +125 °C	XSON8	plastic extremely thin small outline package; no leads; 8 terminals; body 1 × 1.95 × 0.5 mm	SOT833-1	
74AUP2G80GF	−40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.35 × 1 × 0.5 mm	SOT1089	
74AUP2G80GD	−40 °C to +125 °C	XSON8	plastic extremely thin small outline package; no leads; 8 terminals; body 3 × 2 × 0.5 mm	SOT996-2	
74AUP2G80GM	−40 °C to +125 °C	XQFN8	plastic, extremely thin quad flat package; no leads; 8 terminals; body 1.6 × 1.6 × 0.5 mm	SOT902-2	
74AUP2G80GN	−40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.2 × 1.0 × 0.35 mm	SOT1116	
74AUP2G80GS	−40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.35 × 1.0 × 0.35 mm	SOT1203	

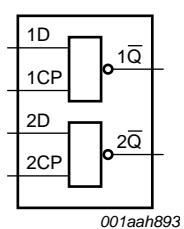
### 4. Marking

**Table 2. Marking codes**

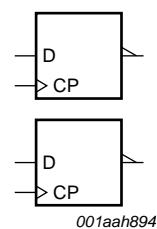
Type number	Marking code <sup>[1]</sup>
74AUP2G80DC	p80
74AUP2G80GT	p80
74AUP2G80GF	pT
74AUP2G80GD	p80
74AUP2G80GM	p80
74AUP2G80GN	pT
74AUP2G80GS	pT

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

### 5. Functional diagram



**Fig 1. Logic symbol**



**Fig 2. IEC logic symbol**

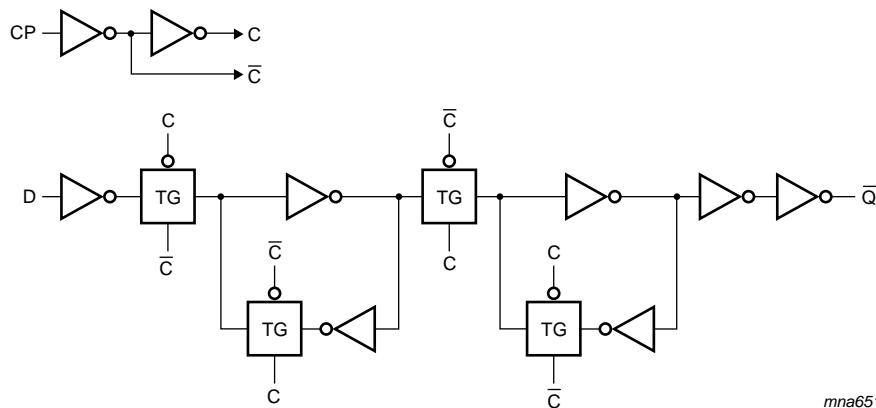


Fig 3. Logic diagram (one flip-flop)

## 6. Pinning information

### 6.1 Pinning

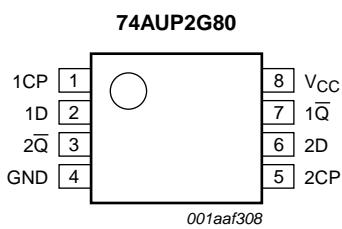


Fig 4. Pin configuration SOT765-1

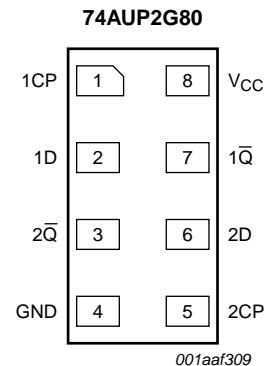
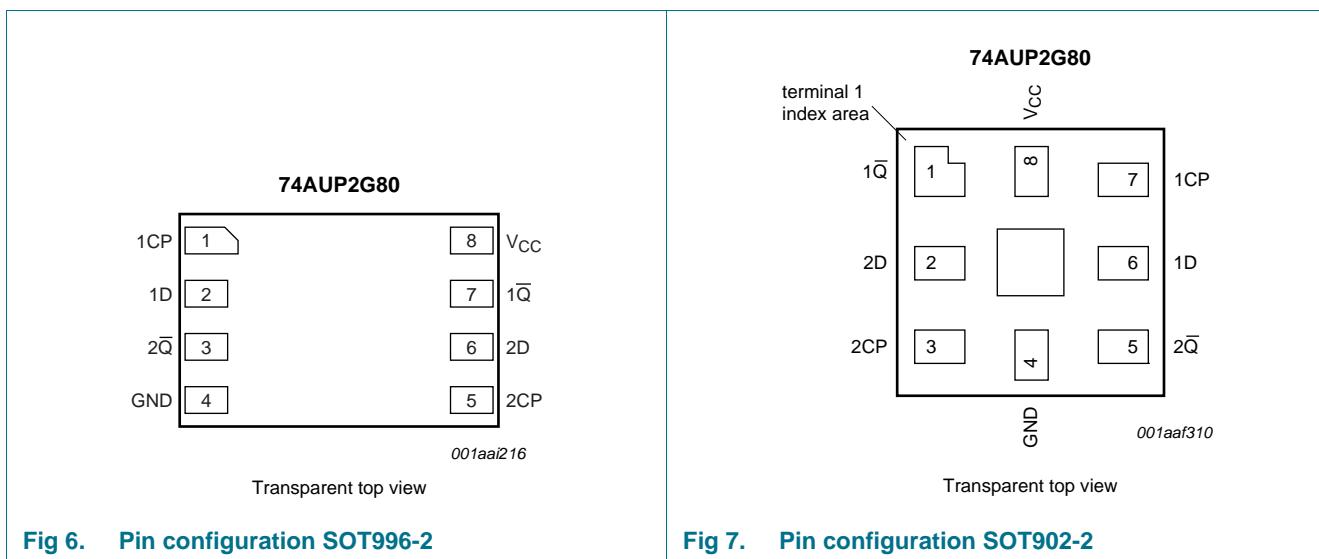


Fig 5. Pin configuration SOT833-1, SOT1089, SOT1116 and SOT1203



## 6.2 Pin description

**Table 3. Pin description**

Symbol	Pin	Description
	<b>SOT765-1, SOT833-1, SOT1089, SOT996-2, SOT1116 and SOT1203</b>	<b>SOT902-2</b>
1CP, 2CP	1, 5	7, 3
1D, 2D	2, 6	6, 2
GND	4	4
1Q, 2Q	7, 3	1, 5
V <sub>CC</sub>	8	8

## 7. Functional description

**Table 4. Function table<sup>[1]</sup>**

Input		Output
<b>nCP</b>	<b>nD</b>	<b>nQ</b>
↑	L	H
↑	H	L
L	X	$\bar{q}$

[1] H = HIGH voltage level;

L = LOW voltage level;

↑ = LOW-to-HIGH CP transition;

X = don't care;

$\bar{q}$  = lower case letter indicates the state of referenced input, one setup time prior to the LOW-to-HIGH CP transition.

## 8. Limiting values

**Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		-0.5	+4.6	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V	-50	-	mA
V <sub>I</sub>	input voltage		[1] -0.5	+4.6	V
I <sub>OK</sub>	output clamping current	V <sub>O</sub> < 0 V	-50	-	mA
V <sub>O</sub>	output voltage	Active mode and Power-down mode	[1] -0.5	+4.6	V
I <sub>O</sub>	output current	V <sub>O</sub> = 0 V to V <sub>CC</sub>	-	±20	mA
I <sub>CC</sub>	supply current		-	+50	mA
I <sub>GND</sub>	ground current		-50	-	mA
T <sub>STG</sub>	storage temperature		-65	+150	°C
P <sub>TOT</sub>	total power dissipation	T <sub>AMB</sub> = -40 °C to +125 °C	[2] -	250	mW

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For VSSOP8 packages: above 110 °C the value of P<sub>TOT</sub> derates linearly with 8.0 mW/K.

For XSON8 and XQFN8 packages: above 118 °C the value of P<sub>TOT</sub> derates linearly with 7.8 mW/K.

## 9. Recommended operating conditions

**Table 6. Operating conditions**

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		0.8	3.6	V
V <sub>I</sub>	input voltage		0	3.6	V
V <sub>O</sub>	output voltage	Active mode	0	V <sub>CC</sub>	V
		Power-down mode; V <sub>CC</sub> = 0 V	0	3.6	V
T <sub>AMB</sub>	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V <sub>CC</sub> = 0.8 V to 3.6 V	-	200	ns/V

## 10. Static characteristics

**Table 7. Static characteristics**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b><math>T_{amb} = 25^\circ\text{C}</math></b>						
$V_{IH}$	HIGH-level input voltage	$V_{CC} = 0.8\text{ V}$	$0.70 \times V_{CC}$	-	-	V
		$V_{CC} = 0.9\text{ V to }1.95\text{ V}$	$0.65 \times V_{CC}$	-	-	V
		$V_{CC} = 2.3\text{ V to }2.7\text{ V}$	1.6	-	-	V
		$V_{CC} = 3.0\text{ V to }3.6\text{ V}$	2.0	-	-	V
$V_{IL}$	LOW-level input voltage	$V_{CC} = 0.8\text{ V}$	-	-	$0.30 \times V_{CC}$	V
		$V_{CC} = 0.9\text{ V to }1.95\text{ V}$	-	-	$0.35 \times V_{CC}$	V
		$V_{CC} = 2.3\text{ V to }2.7\text{ V}$	-	-	0.7	V
		$V_{CC} = 3.0\text{ V to }3.6\text{ V}$	-	-	0.9	V
$V_{OH}$	HIGH-level output voltage	$V_I = V_{IH}$ or $V_{IL}$				
		$I_O = -20\text{ }\mu\text{A}; V_{CC} = 0.8\text{ V to }3.6\text{ V}$	$V_{CC} - 0.1$	-	-	V
		$I_O = -1.1\text{ mA}; V_{CC} = 1.1\text{ V}$	$0.75 \times V_{CC}$	-	-	V
		$I_O = -1.7\text{ mA}; V_{CC} = 1.4\text{ V}$	1.11	-	-	V
		$I_O = -1.9\text{ mA}; V_{CC} = 1.65\text{ V}$	1.32	-	-	V
		$I_O = -2.3\text{ mA}; V_{CC} = 2.3\text{ V}$	2.05	-	-	V
		$I_O = -3.1\text{ mA}; V_{CC} = 2.3\text{ V}$	1.9	-	-	V
		$I_O = -2.7\text{ mA}; V_{CC} = 3.0\text{ V}$	2.72	-	-	V
		$I_O = -4.0\text{ mA}; V_{CC} = 3.0\text{ V}$	2.6	-	-	V
$V_{OL}$	LOW-level output voltage	$V_I = V_{IH}$ or $V_{IL}$				
		$I_O = 20\text{ }\mu\text{A}; V_{CC} = 0.8\text{ V to }3.6\text{ V}$	-	-	0.1	V
		$I_O = 1.1\text{ mA}; V_{CC} = 1.1\text{ V}$	-	-	$0.3 \times V_{CC}$	V
		$I_O = 1.7\text{ mA}; V_{CC} = 1.4\text{ V}$	-	-	0.31	V
		$I_O = 1.9\text{ mA}; V_{CC} = 1.65\text{ V}$	-	-	0.31	V
		$I_O = 2.3\text{ mA}; V_{CC} = 2.3\text{ V}$	-	-	0.31	V
		$I_O = 3.1\text{ mA}; V_{CC} = 2.3\text{ V}$	-	-	0.44	V
		$I_O = 2.7\text{ mA}; V_{CC} = 3.0\text{ V}$	-	-	0.31	V
$I_I$	input leakage current	$V_I = \text{GND to }3.6\text{ V}; V_{CC} = 0\text{ V to }3.6\text{ V}$	-	-	$\pm 0.1$	$\mu\text{A}$
		$V_I \text{ or } V_O = 0\text{ V to }3.6\text{ V}; V_{CC} = 0\text{ V}$	-	-	$\pm 0.2$	$\mu\text{A}$
$\Delta I_{OFF}$	additional power-off leakage current	$V_I \text{ or } V_O = 0\text{ V to }3.6\text{ V}; V_{CC} = 0\text{ V to }0.2\text{ V}$	-	-	$\pm 0.2$	$\mu\text{A}$
$I_{CC}$	supply current	$V_I = \text{GND or }V_{CC}; I_O = 0\text{ A}; V_{CC} = 0.8\text{ V to }3.6\text{ V}$	-	-	0.5	$\mu\text{A}$
$\Delta I_{CC}$	additional supply current	$V_I = V_{CC} - 0.6\text{ V}; I_O = 0\text{ A}; V_{CC} = 3.3\text{ V}$	[1]	-	40	$\mu\text{A}$
$C_I$	input capacitance	$V_{CC} = 0\text{ V to }3.6\text{ V}; V_I = \text{GND or }V_{CC}$	-	0.6	-	pF
$C_O$	output capacitance	$V_O = \text{GND}; V_{CC} = 0\text{ V}$	-	1.3	-	pF

Table 7. Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>T<sub>amb</sub> = -40 °C to +85 °C</b>						
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.70 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.65 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.30 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.35 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output voltage	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>				
		I <sub>O</sub> = -20 µA; V <sub>CC</sub> = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.7 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.03	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.30	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.97	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.85	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.67	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.55	-	-	V
V <sub>OL</sub>	LOW-level output voltage	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>				
		I <sub>O</sub> = 20 µA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.37	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.35	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.33	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.45	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.33	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.45	V
I <sub>I</sub>	input leakage current	V <sub>I</sub> = GND to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.5	µA
I <sub>OFF</sub>	power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V	-	-	±0.5	µA
ΔI <sub>OFF</sub>	additional power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V to 0.2 V	-	-	±0.6	µA
I <sub>CC</sub>	supply current	V <sub>I</sub> = GND or V <sub>CC</sub> ; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.9	µA
ΔI <sub>CC</sub>	additional supply current	V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 3.3 V	[1]	-	50	µA

Table 7. Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>T<sub>amb</sub> = -40 °C to +125 °C</b>						
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.75 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.70 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.25 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.30 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output voltage	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>				
		I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.11	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.6 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	0.93	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.17	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.77	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.67	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.40	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.30	-	-	V
V <sub>OL</sub>	LOW-level output voltage	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>				
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.11	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.33 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.41	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.39	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.36	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.50	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.36	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.50	V
I <sub>I</sub>	input leakage current	V <sub>I</sub> = GND to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.75	μA
I <sub>OFF</sub>	power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V	-	-	±0.75	μA
ΔI <sub>OFF</sub>	additional power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V to 0.2 V	-	-	±0.75	μA
I <sub>CC</sub>	supply current	V <sub>I</sub> = GND or V <sub>CC</sub> ; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	1.4	μA
ΔI <sub>CC</sub>	additional supply current	V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 3.3 V	[1]	-	75	μA

[1] One input at V<sub>CC</sub> - 0.6 V, other input at V<sub>CC</sub> or GND.

## 11. Dynamic characteristics

**Table 8. Dynamic characteristics**

Voltages are referenced to GND (ground = 0 V; for test circuit see [Figure 10](#)).

Symbol	Parameter	Conditions	T <sub>amb</sub> = 25 °C			T <sub>amb</sub> = -40 °C to +125 °C				Unit	
			Min	Typ <sup>[1]</sup>	Max	Min (85 °C)	Max (85 °C)	Min (125 °C)	Max (125 °C)		
<b>C<sub>L</sub> = 5 pF</b>											
t <sub>pd</sub>	propagation delay	nCP to n $\bar{Q}$ ; see <a href="#">Figure 8</a>	<a href="#">[2]</a>								
		V <sub>CC</sub> = 0.8 V	-	20.9	-	-	-	-	-	ns	
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.9	6.0	12.9	2.6	14.3	2.6	15.7	ns	
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.9	4.2	7.6	2.0	8.9	2.0	9.8	ns	
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.7	3.4	5.9	1.6	7.0	1.6	7.7	ns	
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.4	2.6	4.3	1.2	5.6	1.2	6.2	ns	
f <sub>max</sub>	maximum frequency	nCP; see <a href="#">Figure 9</a>									
		V <sub>CC</sub> = 0.8 V	-	53	-	-	-	-	-	MHz	
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	203	-	170	-	170	-	MHz	
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	347	-	310	-	300	-	MHz	
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	435	-	400	-	390	-	MHz	
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	550	-	490	-	480	-	MHz	
C <sub>L</sub> = 10 pF	propagation delay	nCP to n $\bar{Q}$ ; see <a href="#">Figure 8</a>	<a href="#">[2]</a>								
		V <sub>CC</sub> = 0.8 V	-	24.6	-	-	-	-	-	ns	
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.3	6.9	14.9	3.0	16.5	3.0	18.2	ns	
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.6	4.8	8.8	2.3	10.3	2.3	11.3	ns	
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.3	3.9	6.8	2.0	8.1	2.0	8.9	ns	
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.9	3.1	5.1	1.7	6.3	1.7	6.9	ns	
f <sub>max</sub>	maximum frequency	nCP; see <a href="#">Figure 9</a>									
		V <sub>CC</sub> = 0.8 V	-	52	-	-	-	-	-	MHz	
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	192	-	150	-	150	-	MHz	
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	324	-	280	-	230	-	MHz	
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	421	-	310	-	250	-	MHz	
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	486	-	370	-	360	-	MHz	
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	550	-	410	-	360	-	MHz	

Table 8. Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V; for test circuit see [Figure 10](#)).

Symbol	Parameter	Conditions	T <sub>amb</sub> = 25 °C			T <sub>amb</sub> = -40 °C to +125 °C				Unit
			Min	Typ <sup>[1]</sup>	Max	Min (85 °C)	Max (85 °C)	Min (125 °C)	Max (125 °C)	
<b>C<sub>L</sub> = 15 pF</b>										
t <sub>pd</sub>	propagation delay	nCP to n $\bar{Q}$ ; see <a href="#">Figure 8</a>	<a href="#">[2]</a>							
		V <sub>CC</sub> = 0.8 V	-	28.2	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.0	7.6	16.7	3.4	18.6	3.4	20.5	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	3.0	5.3	9.8	2.6	11.5	2.6	12.7	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.6	4.4	7.6	2.3	9.1	2.3	10.0	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.2	3.5	5.7	2.0	6.9	2.0	7.6	ns
f <sub>max</sub>	maximum frequency	nCP; see <a href="#">Figure 9</a>								
		V <sub>CC</sub> = 0.8 V	-	50	-	-	-	-	-	MHz
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	181	-	120	-	120	-	MHz
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	301	-	190	-	160	-	MHz
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	407	-	240	-	190	-	MHz
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	422	-	300	-	270	-	MHz
C <sub>L</sub> = 30 pF			<a href="#">[2]</a>							
	propagation delay	nCP to n $\bar{Q}$ ; see <a href="#">Figure 8</a>								
		V <sub>CC</sub> = 0.8 V	-	38.8	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	4.9	9.8	20.7	4.4	24.7	4.4	27.2	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	4.0	6.8	12.7	3.5	15.0	3.5	16.5	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	3.5	5.6	9.9	2.2	11.9	2.2	13.1	ns
f <sub>max</sub>	maximum frequency	nCP; see <a href="#">Figure 9</a>								
		V <sub>CC</sub> = 0.8 V	-	28	-	-	-	-	-	MHz
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	128	-	70	-	70	-	MHz
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	206	-	120	-	110	-	MHz
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	262	-	150	-	120	-	MHz
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	269	-	190	-	170	-	MHz
<b>C<sub>L</sub> = 5 pF, 10 pF, 15 pF and 30 pF</b>										
t <sub>su(H)</sub>	set-up time HIGH	nD to nCP; see <a href="#">Figure 9</a>								
		V <sub>CC</sub> = 0.8 V	-	2.5	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	0.5	-	2.3	-	2.3	-	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	0.3	-	1.2	-	1.2	-	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	0.3	-	0.8	-	0.8	-	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	0.2	-	0.6	-	0.6	-	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	0.2	-	0.4	-	0.4	-	ns

Table 8. Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V; for test circuit see [Figure 10](#)).

Symbol	Parameter	Conditions	T <sub>amb</sub> = 25 °C			T <sub>amb</sub> = -40 °C to +125 °C			Unit
			Min	Typ <sup>[1]</sup>	Max	Min (85 °C)	Max (85 °C)	Min (125 °C)	
t <sub>su(L)</sub>	set-up time LOW	nD to nCP; see <a href="#">Figure 9</a>							
		V <sub>CC</sub> = 0.8 V	-	1.7	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	0.3	-	1.9	-	1.9	-
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	0.2	-	1.3	-	1.3	-
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	0.2	-	1.1	-	1.1	-
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	0.3	-	0.8	-	0.8	-
t <sub>h</sub>	hold time	nD to nCP; see <a href="#">Figure 9</a>							
		V <sub>CC</sub> = 0.8 V	-	-2.1	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	-0.4	-	0.1	-	0.1	-
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	-0.3	-	0	-	0	-
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	-0.2	-	0	-	0	-
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-0.2	-	0	-	0	-
t <sub>w</sub>	pulse width	nCP HIGH or LOW; see <a href="#">Figure 9</a>							
		V <sub>CC</sub> = 0.8 V	-	5.2	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	1.0	-	3.0	-	3.0	-
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	0.8	-	2.0	-	2.0	-
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	0.6	-	2.0	-	2.0	-
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	0.5	-	2.0	-	2.0	-
C <sub>PD</sub>	power dissipation capacitance	f = 1 MHz; V <sub>I</sub> = GND to V <sub>CC</sub> <sup>[3]</sup>							
		V <sub>CC</sub> = 0.8 V	-	1.8	-	-	-	-	pF
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	1.8	-	-	-	-	pF
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	1.9	-	-	-	-	pF
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	2.0	-	-	-	-	pF
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	2.4	-	-	-	-	pF
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	2.9	-	-	-	-	pF

[1] All typical values are measured at nominal V<sub>CC</sub>.[2] t<sub>pd</sub> is the same as t<sub>PLH</sub> and t<sub>PHL</sub>.[3] C<sub>PD</sub> is used to determine the dynamic power dissipation (P<sub>D</sub> in  $\mu$ W).

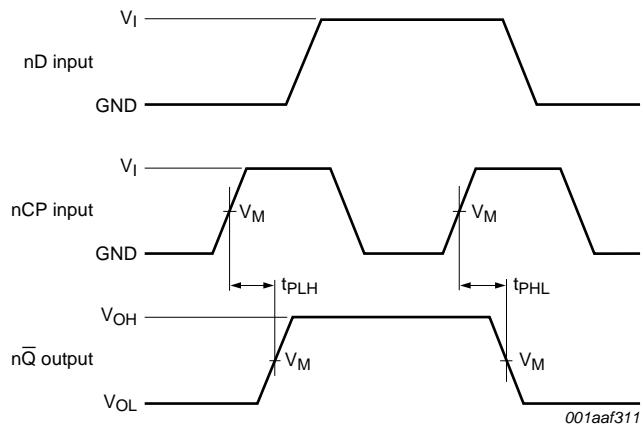
$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum(C_L \times V_{CC}^2 \times f_o)$$

f<sub>i</sub> = input frequency in MHz;f<sub>o</sub> = output frequency in MHz;C<sub>L</sub> = output load capacitance in pF;V<sub>CC</sub> = supply voltage in V;

N = number of inputs switching;

$$\sum(C_L \times V_{CC}^2 \times f_o) = \text{sum of the outputs.}$$

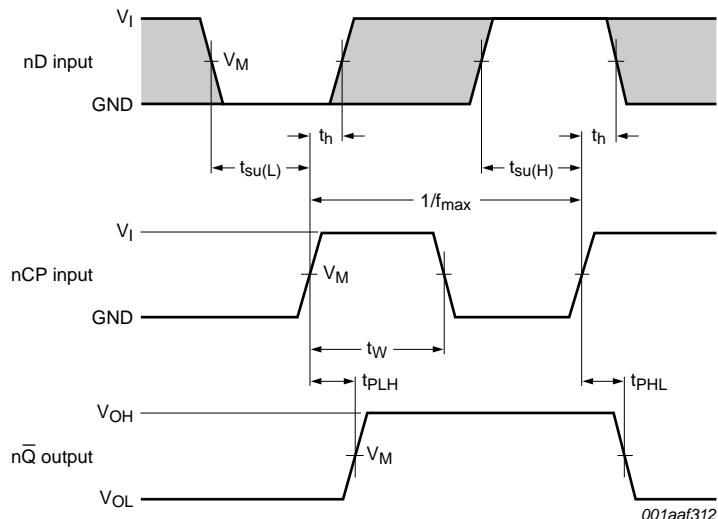
## 12. Waveforms



Measurement points are given in [Table 9](#).

Logic levels:  $V_{OL}$  and  $V_{OH}$  are typical output voltage levels that occur with the output load.

**Fig 8. The clock input (nCP) to output ( $n\bar{Q}$ ) propagation delays**



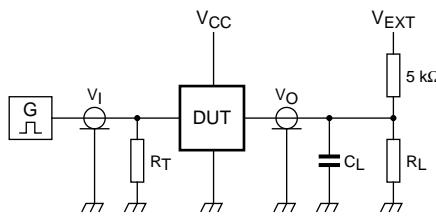
Measurement points are given in [Table 9](#).

Logic levels:  $V_{OL}$  and  $V_{OH}$  are typical output voltage levels that occur with the output load.

**Fig 9. The clock input (nCP) to output ( $n\bar{Q}$ ) propagation delays, clock pulse width, nD to nCP setup and hold times and the nCP maximum frequency**

**Table 9. Measurement points**

Supply voltage	Output	Input		
$V_{CC}$ 0.8 V to 3.6 V	$V_M$ $0.5 \times V_{CC}$	$V_M$ $0.5 \times V_{CC}$	$V_I$ $V_{CC}$	$t_r = t_f \leq 3.0 \text{ ns}$



Test data is given in [Table 10](#).

Definitions for test circuit:

$R_L$  = Load resistance.

$C_L$  = Load capacitance including jig and probe capacitance.

$R_T$  = Termination resistance should be equal to the output impedance  $Z_0$  of the pulse generator.

$V_{EXT}$  = External voltage for measuring switching times.

**Fig 10. Test circuit for measuring switching times**

**Table 10. Test data**

Supply voltage	Load		$V_{EXT}$		
$V_{CC}$	$C_L$	$R_L$ <sup>[1]</sup>	$t_{PLH}, t_{PHL}$	$t_{PZH}, t_{PHZ}$	$t_{PZL}, t_{PLZ}$
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	$2 \times V_{CC}$

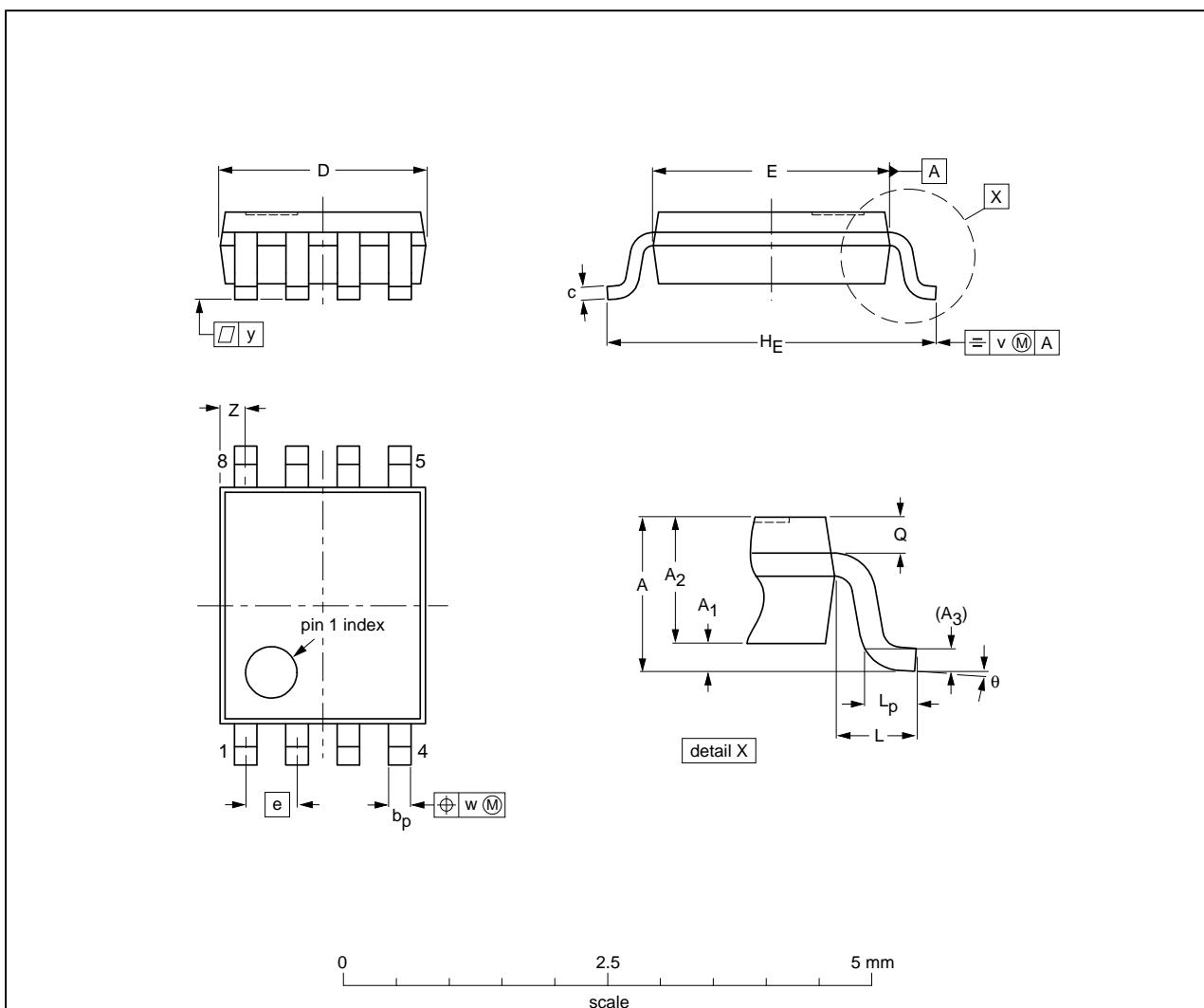
[1] For measuring enable and disable times  $R_L = 5 \text{ k}\Omega$

For measuring propagation delays, setup and hold times and pulse width  $R_L = 1 \text{ M}\Omega$ .

## 13. Package outline

VSSOP8: plastic very thin shrink small outline package; 8 leads; body width 2.3 mm

SOT765-1



### DIMENSIONS (mm are the original dimensions)

UNIT	A max.	A <sub>1</sub>	A <sub>2</sub>	A <sub>3</sub>	b <sub>p</sub>	c	D <sup>(1)</sup>	E <sup>(2)</sup>	e	H <sub>E</sub>	L	L <sub>p</sub>	Q	v	w	y	Z <sup>(1)</sup>	θ
mm	1	0.15 0.00	0.85 0.60	0.12	0.27 0.17	0.23 0.08	2.1 1.9	2.4 2.2	0.5	3.2 3.0	0.4	0.40 0.15	0.21 0.19	0.2	0.13	0.1	0.4 0.1	8° 0°

### Notes

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
2. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT765-1		MO-187				02-06-07

Fig 11. Package outline SOT765-1 (VSSOP8)

XSON8: plastic extremely thin small outline package; no leads; 8 terminals; body 1 x 1.95 x 0.5 mm

SOT833-1

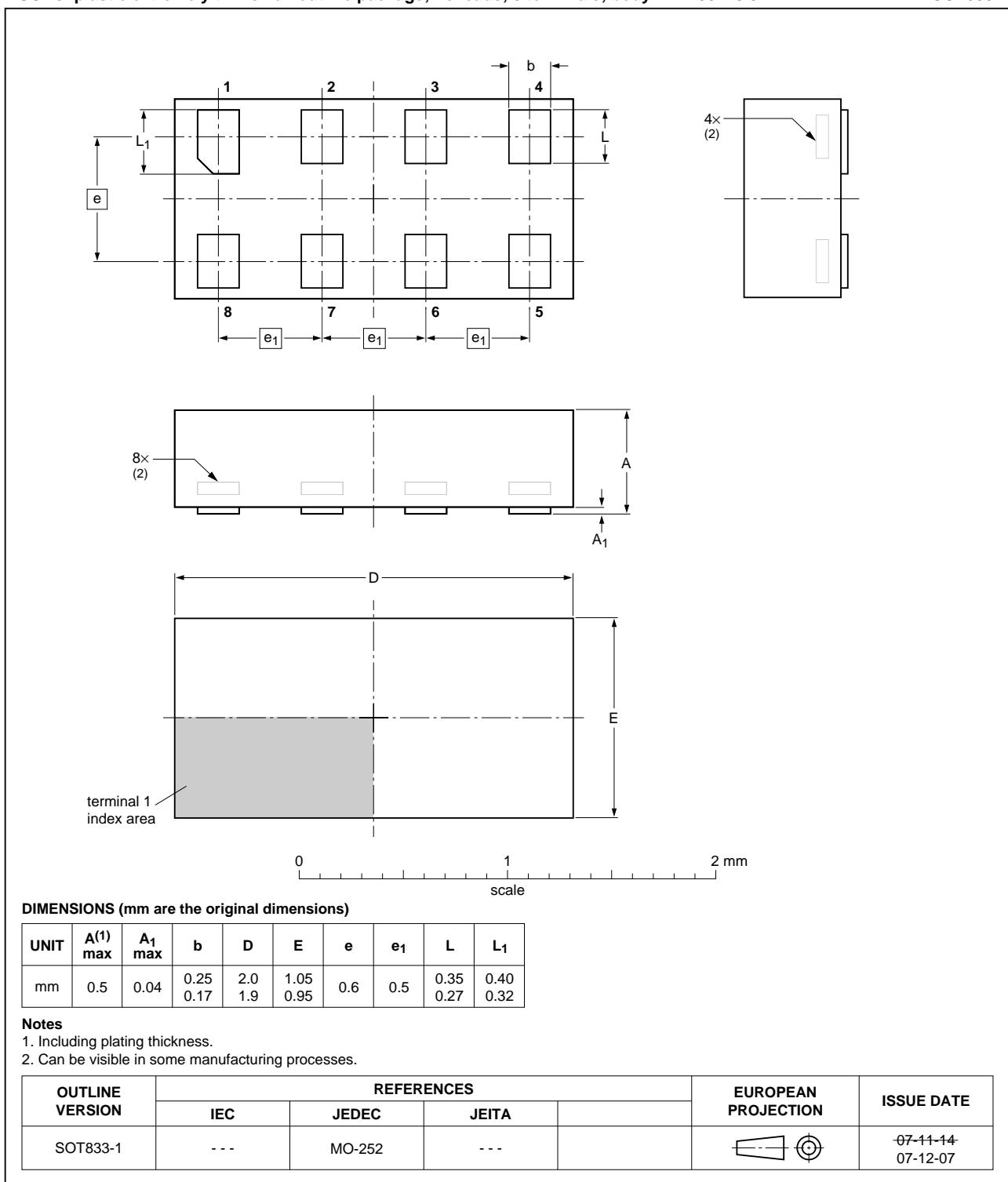


Fig 12. Package outline SOT833-1 (XSON8)

**XSON8: extremely thin small outline package; no leads;  
8 terminals; body 1.35 x 1 x 0.5 mm**

SOT1089

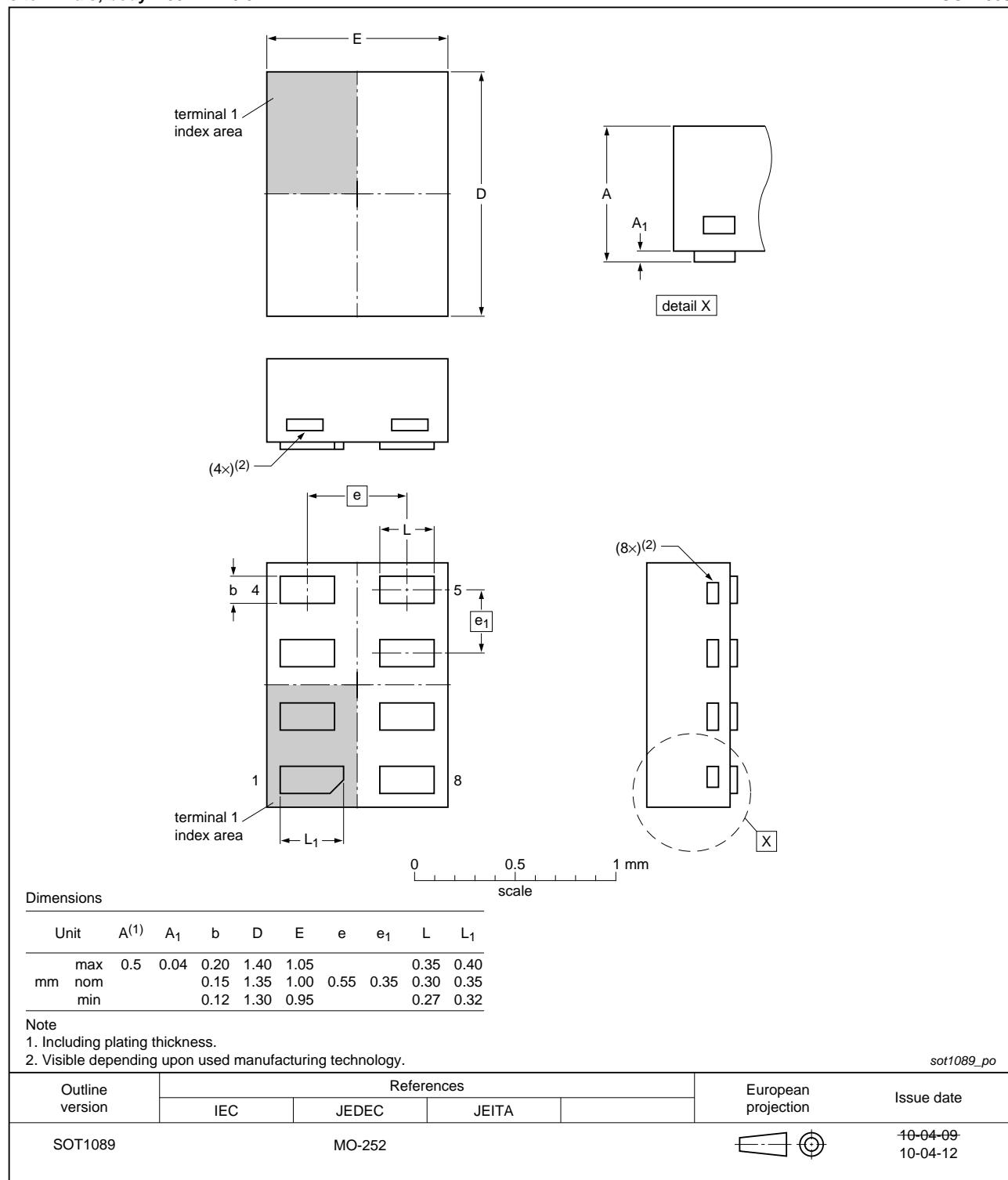


Fig 13. Package outline SOT1089 (XSON8)

XSON8: plastic extremely thin small outline package; no leads;  
8 terminals; body 3 x 2 x 0.5 mm

SOT996-2

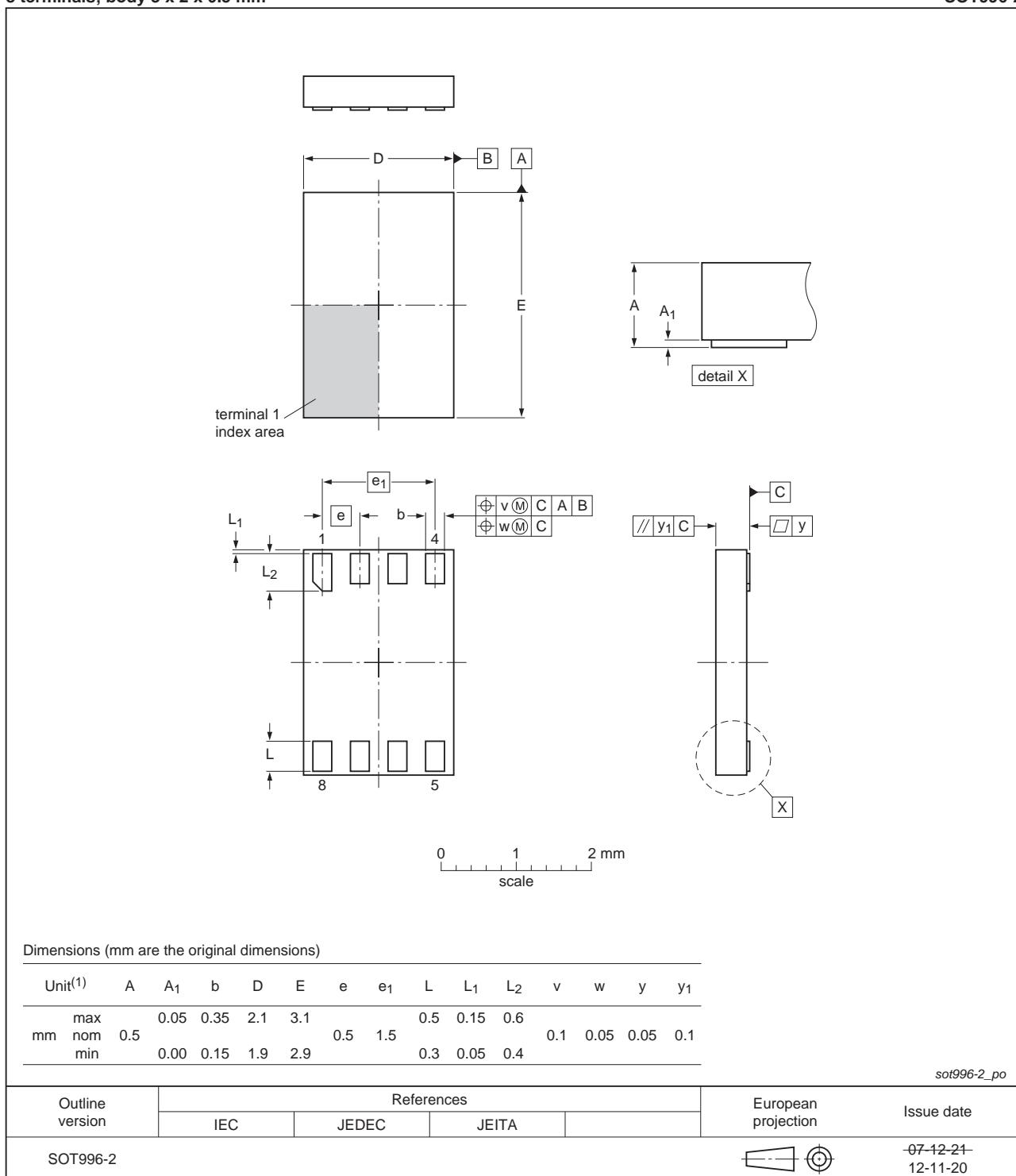


Fig 14. Package outline SOT996-2 (XSON8)

XQFN8: plastic, extremely thin quad flat package; no leads;  
8 terminals; body 1.6 x 1.6 x 0.5 mm

SOT902-2

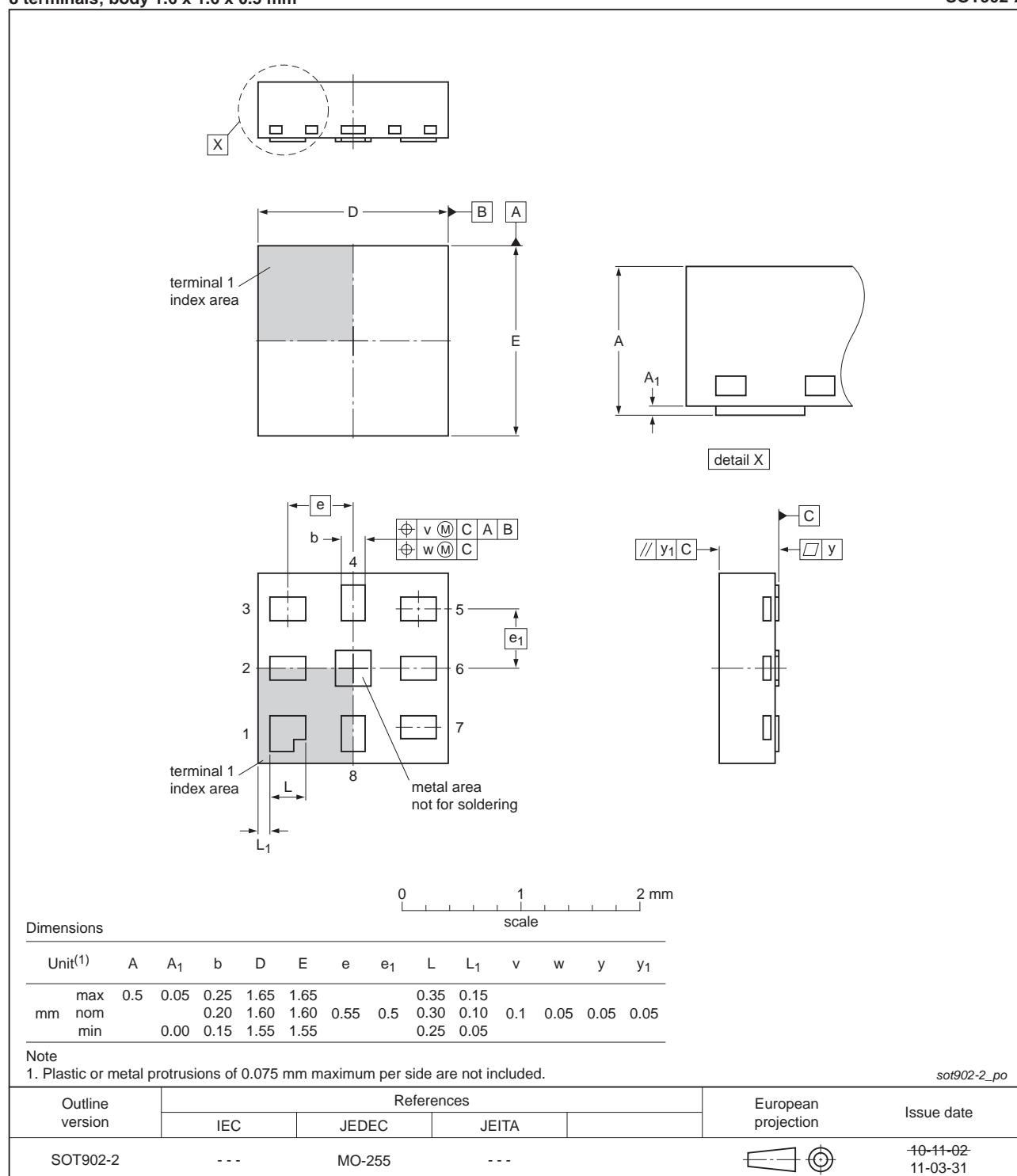


Fig 15. Package outline SOT902-2 (XQFN8)

**XSON8: extremely thin small outline package; no leads;  
8 terminals; body 1.2 x 1.0 x 0.35 mm**

SOT1116

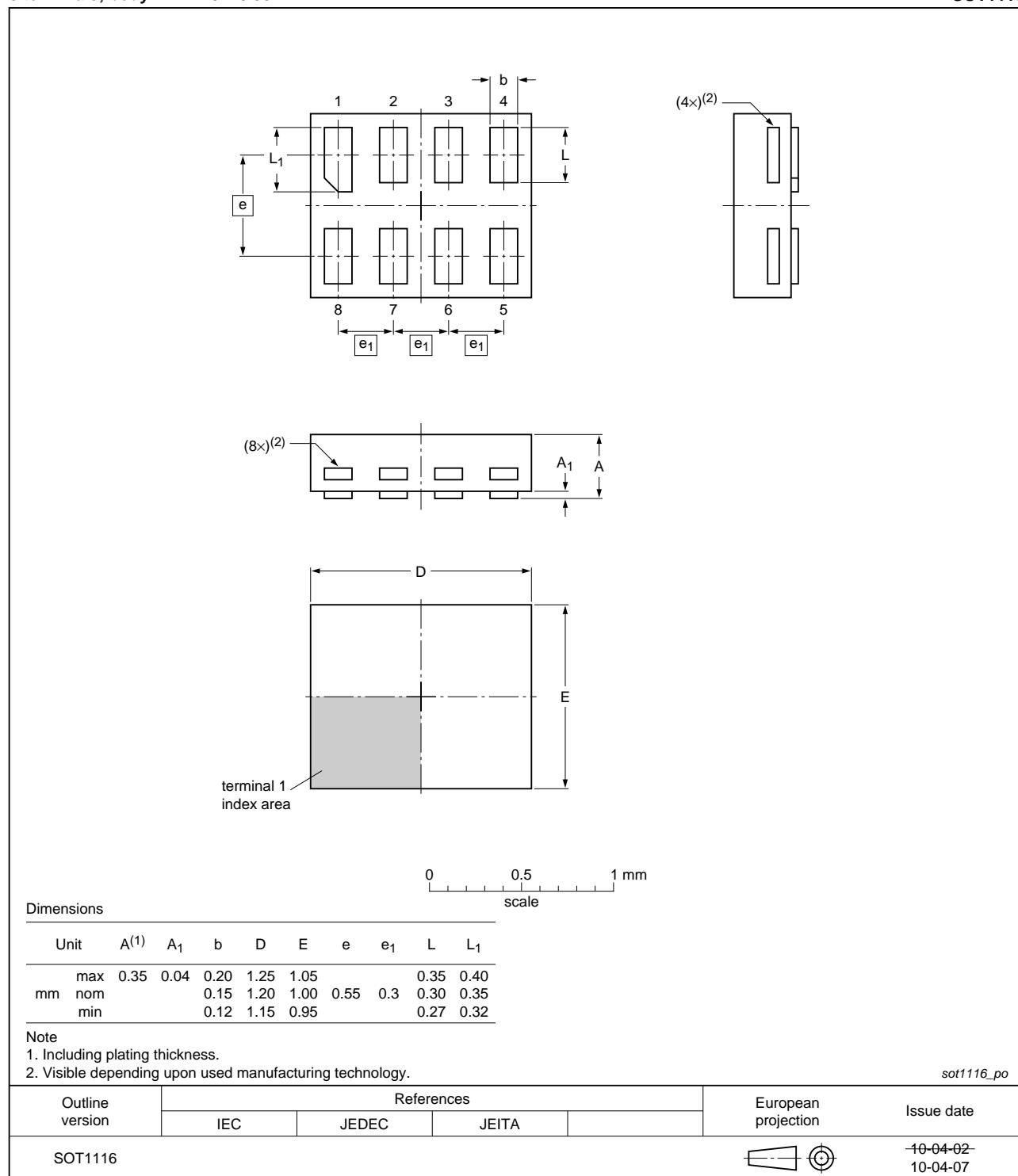
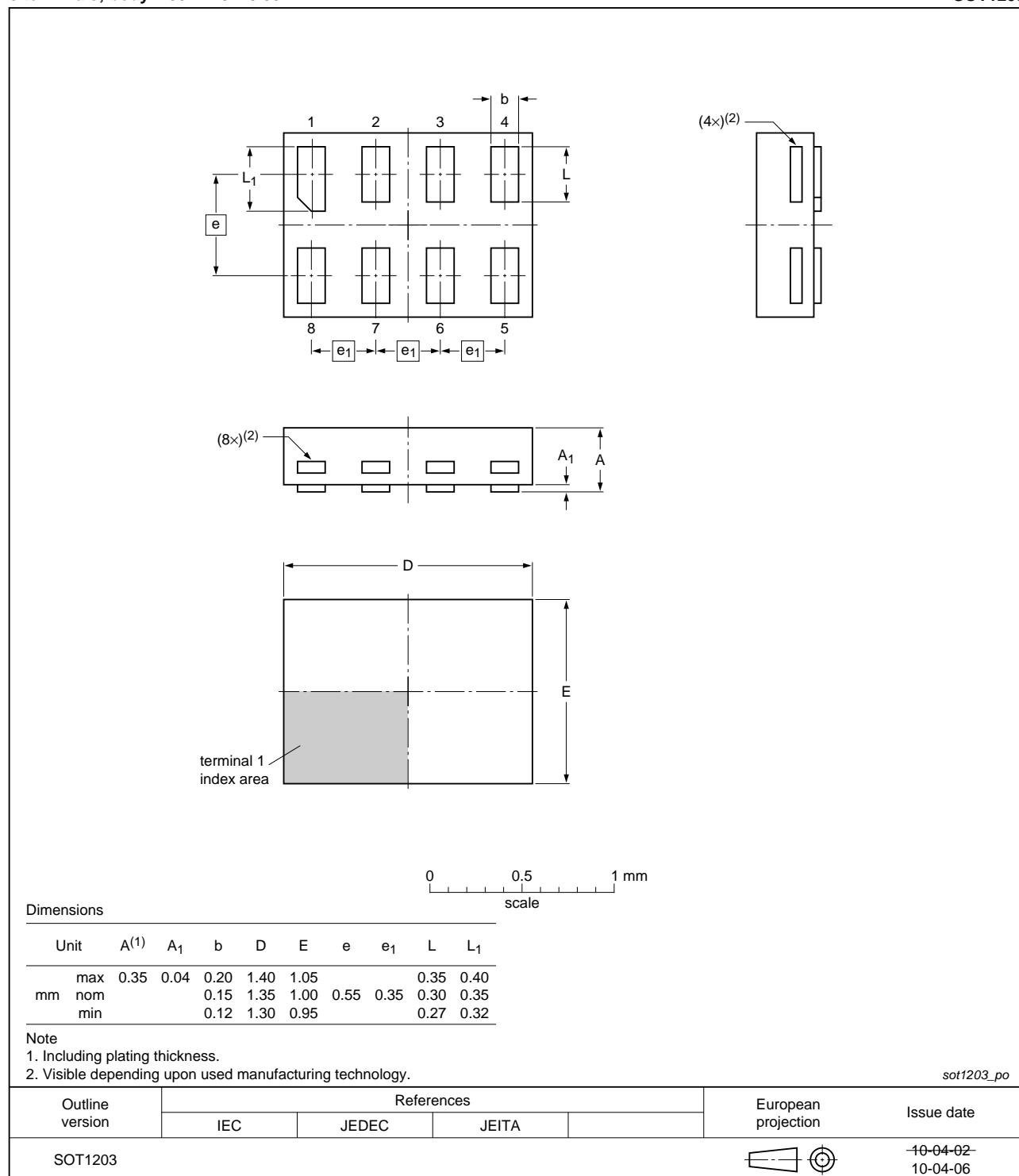


Fig 16. Package outline SOT1116 (XSON8)

**XSON8: extremely thin small outline package; no leads;  
8 terminals; body 1.35 x 1.0 x 0.35 mm**

SOT1203



**Fig 17. Package outline SOT1203 (XSON8)**

## 14. Abbreviations

Table 11. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model

## 15. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74AUP2G80 v.8	20130121	Product data sheet	-	74AUP2G80 v.7	
Modifications:		<ul style="list-style-type: none"> <li>For type number 74AUP2G80GD XSON8U has changed to XSON8.</li> </ul>			
74AUP2G80 v.7	20120614	Product data sheet	-	74AUP2G80 v.6	
74AUP2G80 v.6	20111207	Product data sheet	-	74AUP2G80 v.5	
74AUP2G80 v.5	20101005	Product data sheet	-	74AUP2G80 v.4	
74AUP2G80 v.4	20080602	Product data sheet	-	74AUP2G80 v.3	
74AUP2G80 v.3	20080328	Product data sheet	-	74AUP2G80 v.2	
74AUP2G80 v.2	20070801	Product data sheet	-	74AUP2G80 v.1	
74AUP2G80 v.1	20060825	Product data sheet	-	-	

## 16. Legal information

### 16.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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