

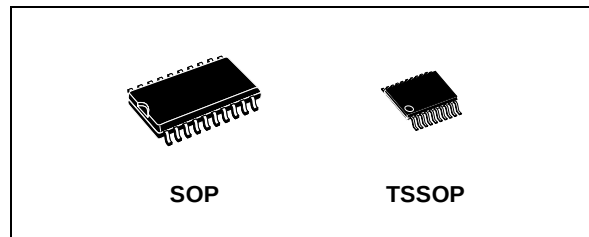
## LOW VOLTAGE CMOS OCTAL D-TYPE LATCH WITH 3 STATE OUTPUTS NON INVERTING

- HIGH SPEED:  
 $t_{PD} = 5.8 \text{ ns (TYP.) at } V_{CC} = 3.3 \text{ V}$
- COMPATIBLE WITH TTL OUTPUTS
- LOW POWER DISSIPATION:  
 $I_{CC} = 4 \mu\text{A (MAX.) at } T_A = 25^\circ\text{C}$
- LOW NOISE:  
 $V_{OLP} = 0.4\text{V (TYP.) at } V_{CC} = 3.3\text{V}$
- $75\Omega$  TRANSMISSION LINE OUTPUT DRIVE CAPABILITY
- SYMMETRICAL OUTPUT IMPEDANCE:  
 $|I_{OH}| = I_{OL} = 12\text{mA (MIN) at } V_{CC} = 3.0 \text{ V}$
- PCI BUS LEVELS GUARANTEED AT 24 mA
- BALANCED PROPAGATION DELAYS:  
 $t_{PLH} \equiv t_{PHL}$
- OPERATING VOLTAGE RANGE:  
 $V_{CC(OPR)} = 2\text{V to } 3.6\text{V (1.2V Data Retention)}$
- PIN AND FUNCTION COMPATIBLE WITH 74 SERIES 373
- IMPROVED LATCH-UP IMMUNITY

### DESCRIPTION

The 74LVQ373 is a low voltage CMOS OCTAL D-TYPE LATCH with 3 STATE OUTPUT NON INVERTING fabricated with sub-micron silicon gate and double-layer metal wiring CMOS technology. It is ideal for low power and low noise 3.3V applications.

These 8 bit D-Type latch are controlled by a latch



**Table 1: Order Codes**

| PACKAGE | T & R       |
|---------|-------------|
| SOP     | 74LVQ373MTR |
| TSSOP   | 74LVQ373TTR |

enable input (LE) and an output enable input ( $\overline{OE}$ ). While the LE input is held at a high level, the Q outputs will follow the data input precisely.

When the LE is taken low, the Q outputs will be latched precisely at the logic level of D input data. While the ( $\overline{OE}$ ) input is low, the 8 outputs will be in a normal logic state (high or low logic level) and while high level the outputs will be in a high impedance state.

All inputs and outputs are equipped with protection circuits against static discharge, giving them 2KV ESD immunity and transient excess voltage.

**Figure 1: Pin Connection And IEC Logic Symbols**

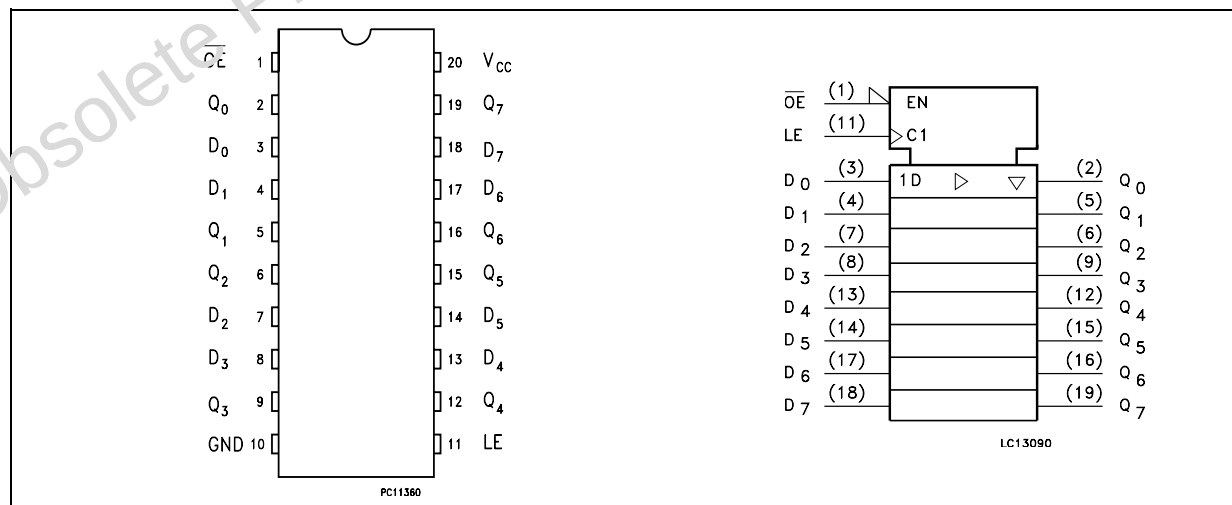


Figure 2: Input And Output Equivalent Circuit

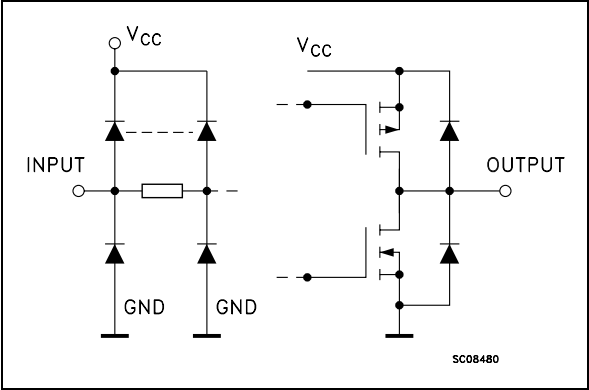


Table 2: Pin Description

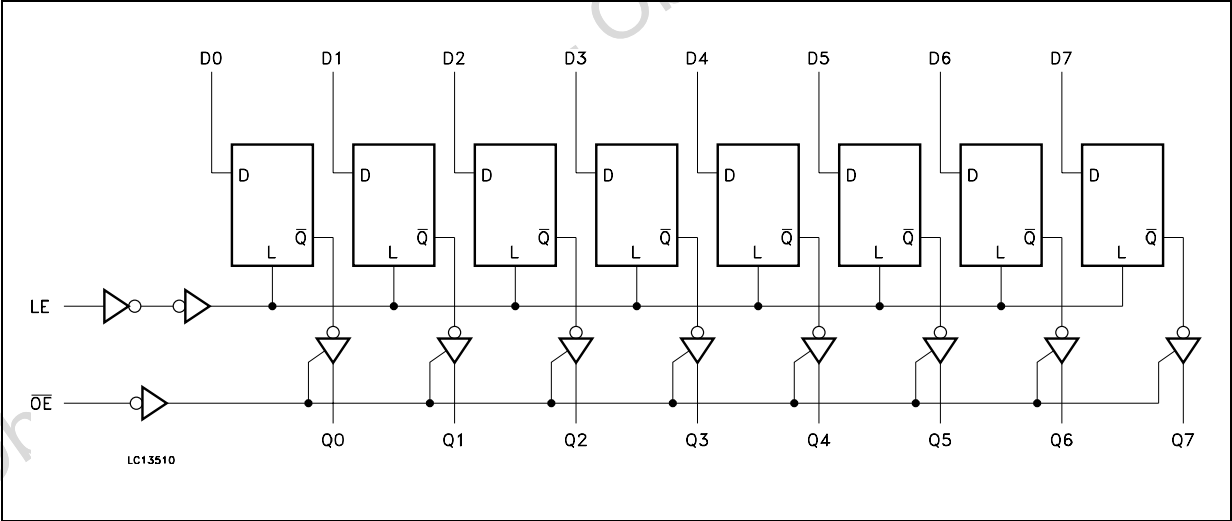
| PIN N°                     | SYMBOL          | NAME AND FUNCTION                        |
|----------------------------|-----------------|------------------------------------------|
| 1                          | $\overline{OE}$ | 3 State Output Enable Input (Active LOW) |
| 2, 5, 6, 9, 12, 15, 16, 19 | Q0 to Q7        | 3-State Latch Outputs                    |
| 3, 4, 7, 8, 13, 14, 17, 18 | D0 to D7        | Data Inputs                              |
| 11                         | LE              | Latch Enable Input                       |
| 10                         | GND             | Ground (0V)                              |
| 20                         | V <sub>CC</sub> | Positive Supply Voltage                  |

Table 3: Truth Table

| INPUTS          |    |   | OUTPUT     |
|-----------------|----|---|------------|
| $\overline{OE}$ | LE | D | Q          |
| H               | X  | X | Z          |
| L               | L  | X | NO CHANGE* |
| L               | H  | L | L          |
| L               | H  | H | H          |

X : Don't Care;  
Z : High Impedance  
\*: Q outputs are latched at the time when the LE input is taken low logic level

Figure 3: Logic Diagram



**Table 4: Absolute Maximum Ratings**

| Symbol                | Parameter                     | Value                  | Unit |
|-----------------------|-------------------------------|------------------------|------|
| $V_{CC}$              | Supply Voltage                | -0.5 to +7             | V    |
| $V_I$                 | DC Input Voltage              | -0.5 to $V_{CC} + 0.5$ | V    |
| $V_O$                 | DC Output Voltage             | -0.5 to $V_{CC} + 0.5$ | V    |
| $I_{IK}$              | DC Input Diode Current        | $\pm 20$               | mA   |
| $I_{OK}$              | DC Output Diode Current       | $\pm 20$               | mA   |
| $I_O$                 | DC Output Current             | $\pm 50$               | mA   |
| $I_{CC}$ or $I_{GND}$ | DC $V_{CC}$ or Ground Current | $\pm 400$              | mA   |
| $T_{stg}$             | Storage Temperature           | -65 to +150            | °C   |
| $T_L$                 | Lead Temperature (10 sec)     | 300                    | °C   |

Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these conditions is not implied

**Table 5: Recommended Operating Conditions**

| Symbol   | Parameter                                         | Value         | Unit |
|----------|---------------------------------------------------|---------------|------|
| $V_{CC}$ | Supply Voltage (note 1)                           | 2 to 3.6      | V    |
| $V_I$    | Input Voltage                                     | 0 to $V_{CC}$ | V    |
| $V_O$    | Output Voltage                                    | 0 to $V_{CC}$ | V    |
| $T_{op}$ | Operating Temperature                             | -55 to 125    | °C   |
| dt/dv    | Input Rise and Fall Time $V_{CC} = 3.0V$ (note 2) | 0 to 10       | ns/V |

1) Truth Table guaranteed: 1.2V to 3.6V

2)  $V_{IN}$  from 0.8V to 2V

Table 6: DC Specifications

| Symbol           | Parameter                             | Test Condition         |                                                                                                | Value                 |       |       |             |      |              |      | Unit |
|------------------|---------------------------------------|------------------------|------------------------------------------------------------------------------------------------|-----------------------|-------|-------|-------------|------|--------------|------|------|
|                  |                                       | V <sub>CC</sub><br>(V) |                                                                                                | T <sub>A</sub> = 25°C |       |       | -40 to 85°C |      | -55 to 125°C |      |      |
|                  |                                       |                        |                                                                                                | Min.                  | Typ.  | Max.  | Min.        | Max. | Min.         | Max. |      |
| V <sub>IH</sub>  | High Level Input Voltage              | 3.0 to 3.6             |                                                                                                | 2.0                   |       |       | 2.0         |      | 2.0          |      | V    |
| V <sub>IL</sub>  | Low Level Input Voltage               |                        |                                                                                                |                       |       | 0.8   |             | 0.8  |              | 0.8  | V    |
| V <sub>OH</sub>  | High Level Output Voltage             | 3.0                    | I <sub>O</sub> = -50 μA                                                                        | 2.9                   | 2.99  |       | 2.9         |      | 2.9          |      | V    |
|                  |                                       |                        | I <sub>O</sub> = -12 mA                                                                        | 2.58                  |       |       | 2.48        |      | 2.48         |      |      |
|                  |                                       |                        | I <sub>O</sub> = -24 mA                                                                        |                       |       |       | 2.2         |      | 2.2          |      |      |
| V <sub>OL</sub>  | Low Level Output Voltage              | 3.0                    | I <sub>O</sub> = 50 μA                                                                         |                       | 0.002 | 0.1   |             | 0.1  |              | 0.1  | V    |
|                  |                                       |                        | I <sub>O</sub> = 12 mA                                                                         |                       | 0     | 0.36  |             | 0.44 |              | 0.44 |      |
|                  |                                       |                        | I <sub>O</sub> = 24 mA                                                                         |                       |       |       |             | 0.55 |              | 0.55 |      |
| I <sub>I</sub>   | Input Leakage Current                 | 3.6                    | V <sub>I</sub> = V <sub>CC</sub> or GND                                                        |                       |       | ± 0.1 |             | ± 1  |              | ± 1  | μA   |
| I <sub>OZ</sub>  | High Impedance Output Leakage Current | 3.6                    | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub><br>V <sub>O</sub> = V <sub>CC</sub> or GND |                       |       | ±0.25 |             | ±2.5 |              | ±5.0 | μA   |
| I <sub>CC</sub>  | Quiescent Supply Current              | 3.6                    | V <sub>I</sub> = V <sub>CC</sub> or GND                                                        |                       |       | 4     |             | 40   |              | 40   | μA   |
| I <sub>OLD</sub> | Dynamic Output Current                | 3.6                    | V <sub>OLD</sub> = 0.8 V max                                                                   |                       |       |       | 36          |      | 25           |      | mA   |
| I <sub>OHD</sub> | Current (note 1, 2)                   |                        | V <sub>OHD</sub> = 2 V min                                                                     |                       |       |       | -25         |      | -25          |      | mA   |

1) Maximum test duration 2ms, one output loaded at time

2) Incident wave switching is guaranteed on transmission lines with impedances as low as 75Ω

Table 7: Dynamic Switching Characteristics

| Symbol           | Parameter                              | Test Condition         |                        | Value                 |      |      |             |      |              | Unit |      |
|------------------|----------------------------------------|------------------------|------------------------|-----------------------|------|------|-------------|------|--------------|------|------|
|                  |                                        | V <sub>CC</sub><br>(V) |                        | T <sub>A</sub> = 25°C |      |      | -40 to 85°C |      | -55 to 125°C |      |      |
|                  |                                        |                        |                        | Min.                  | Typ. | Max. | Min.        | Max. | Min.         |      | Max. |
| V <sub>OLP</sub> | Dynamic Low Voltage Quiet              | 3.3                    | C <sub>L</sub> = 50 pF |                       | 0.4  | 0.8  |             |      |              |      | V    |
| V <sub>OLV</sub> | Output (note 1, 2)                     |                        |                        | -0.8                  | -0.5 |      |             |      |              |      |      |
| V <sub>IHD</sub> | Dynamic High Voltage Input (note 1, 3) |                        |                        | 3.3                   | 2    |      |             |      |              |      |      |
| V <sub>ILD</sub> | Dynamic Low Voltage Input (note 1, 3)  | 3.3                    |                        |                       |      |      | 0.8         |      |              |      |      |

1) Worst case package.

2) Max number of outputs defined as (n). Data inputs are driven 0V to 3.3V, (n-1) outputs switching and one output at GND.

3) Max number of data inputs (n) switching. (n-1) switching 0V to 3.3V. Inputs under test switching: 3.3V to threshold (V<sub>ILD</sub>), 0V to threshold (V<sub>IHD</sub>), f=1MHz.

**Table 8: AC Electrical Characteristics** ( $C_L = 50 \text{ pF}$ ,  $R_L = 500 \Omega$ , Input  $t_r = t_f = 3\text{ns}$ )

| Symbol                                 | Parameter                                   | Test Condition            |  | Value                 |            |              |             |              |              | Unit         |      |
|----------------------------------------|---------------------------------------------|---------------------------|--|-----------------------|------------|--------------|-------------|--------------|--------------|--------------|------|
|                                        |                                             | V <sub>CC</sub><br>(V)    |  | T <sub>A</sub> = 25°C |            |              | -40 to 85°C |              | -55 to 125°C |              |      |
|                                        |                                             |                           |  | Min.                  | Typ.       | Max.         | Min.        | Max.         | Min.         |              | Max. |
| t <sub>PLH</sub><br>t <sub>PHL</sub>   | Propagation Delay<br>Time LE to Q           | 2.7<br>3.3 <sup>(*)</sup> |  |                       | 7.2<br>5.8 | 11.5<br>9.0  |             | 13.5<br>10.5 |              | 15.5<br>12.0 | ns   |
| t <sub>PLH</sub><br>t <sub>PHL</sub>   | Propagation Delay<br>Time D to Q            | 2.7<br>3.3 <sup>(*)</sup> |  |                       | 7.2<br>5.8 | 11.5<br>9.0  |             | 13.5<br>10.5 |              | 15.5<br>12.0 | ns   |
| t <sub>PLZ</sub><br>t <sub>PHZ</sub>   | Output Disable<br>Time                      | 2.7<br>3.3 <sup>(*)</sup> |  |                       | 8.7<br>7.4 | 14.0<br>11.5 |             | 16.0<br>13.5 |              | 18.5<br>15.5 | ns   |
| t <sub>PZL</sub><br>t <sub>PZH</sub>   | Output Enable<br>Time                       | 2.7<br>3.3 <sup>(*)</sup> |  |                       | 8.5<br>7.5 | 14.0<br>11.5 |             | 16.0<br>13.5 |              | 18.5<br>15.5 | ns   |
| t <sub>W</sub>                         | LE Pulse Width<br>HIGH                      | 2.7<br>3.3 <sup>(*)</sup> |  |                       | 2.0<br>1.5 | 5.0<br>4.0   |             | 6.0<br>4.0   |              | 6.0<br>4.0   | ns   |
| t <sub>sL</sub><br>t <sub>sH</sub>     | Setup Time D to<br>LE, HIGH or LOW          | 2.7<br>3.3 <sup>(*)</sup> |  |                       | 0.0<br>0.0 | 4.0<br>3.0   |             | 4.5<br>3.0   |              | 4.5<br>3.0   | ns   |
| t <sub>hL</sub><br>t <sub>hH</sub>     | Hold Time D to LE,<br>HIGH or LOW           | 2.7<br>3.3 <sup>(*)</sup> |  |                       | 0.0<br>0.0 | 1.5<br>1.5   |             | 1.5<br>1.5   |              | 1.5<br>1.5   | ns   |
| t <sub>OSLH</sub><br>t <sub>OSHL</sub> | Output To Output<br>Skew Time<br>(note1, 2) | 2.7<br>3.3 <sup>(*)</sup> |  |                       | 0.5<br>0.5 | 1.0<br>1.0   |             | 1.0<br>1.0   |              | 1.0<br>1.0   | ns   |

1) Skew is defined as the absolute value of the difference between the actual propagation delay for any two outputs of the same device switching in the same direction, either HIGH or LOW ( $t_{OSLH} = |t_{PLHm} - t_{PLHn}|$ ,  $t_{OSHL} = |t_{PHLm} - t_{PHLn}|$ )

2) Parameter guaranteed by design

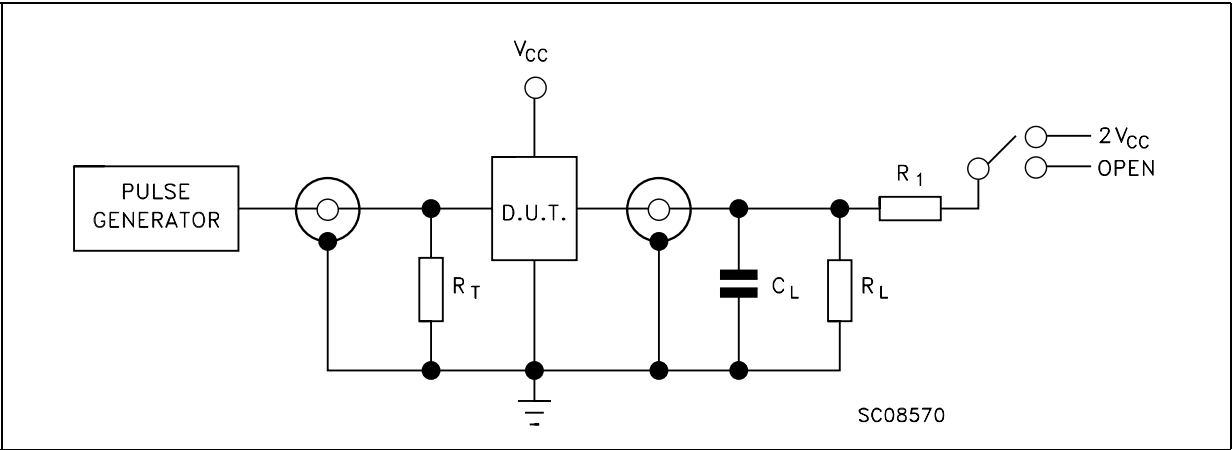
(\*) Voltage range is  $3.3\text{V} \pm 0.3\text{V}$

**Table 9: Capacitive Characteristics**

| Symbol           | Parameter                              | Test Condition         |                         | Value                 |      |      |             |      |              |      |    | Unit |
|------------------|----------------------------------------|------------------------|-------------------------|-----------------------|------|------|-------------|------|--------------|------|----|------|
|                  |                                        | V <sub>CC</sub><br>(V) |                         | T <sub>A</sub> = 25°C |      |      | -40 to 85°C |      | -55 to 125°C |      |    |      |
|                  |                                        |                        |                         | Min.                  | Typ. | Max. | Min.        | Max. | Min.         | Max. |    |      |
| C <sub>IN</sub>  | Input Capacitance                      | 3.3                    |                         |                       | 4    |      |             |      |              |      | pF |      |
| C <sub>OUT</sub> | Output Capacitance                     | 3.3                    |                         |                       | 8    |      |             |      |              |      | pF |      |
| C <sub>PD</sub>  | Power Dissipation Capacitance (note 1) | 3.3                    | f <sub>IN</sub> = 10MHz |                       | 10   |      |             |      |              |      | pF |      |

1)  $C_{PD}$  is defined as the value of the IC's internal equivalent capacitance which is calculated from the operating current consumption without load. (Refer to Test Circuit). Average operating current can be obtained by the following equation.  $I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}/8$  (per Latch)

Figure 4: Test Circuit



| TEST                  | SWITCH    |
|-----------------------|-----------|
| $t_{PLH}$ , $t_{PHL}$ | Open      |
| $t_{PZL}$ , $t_{PLZ}$ | $2V_{CC}$ |
| $t_{PZH}$ , $t_{PHZ}$ | Open      |

$C_L$  = 50pF or equivalent (includes jig and probe capacitance)  
 $R_L$  =  $R_1$  = 500Ω or equivalent  
 $R_T$  =  $Z_{OUT}$  of pulse generator (typically 50Ω)

Figure 5: Waveform - LE To Qn Propagation Delays, LE Minimum Pulse Width, Dn To LE Setup And Hold Times (f=1MHz; 50% duty cycle)

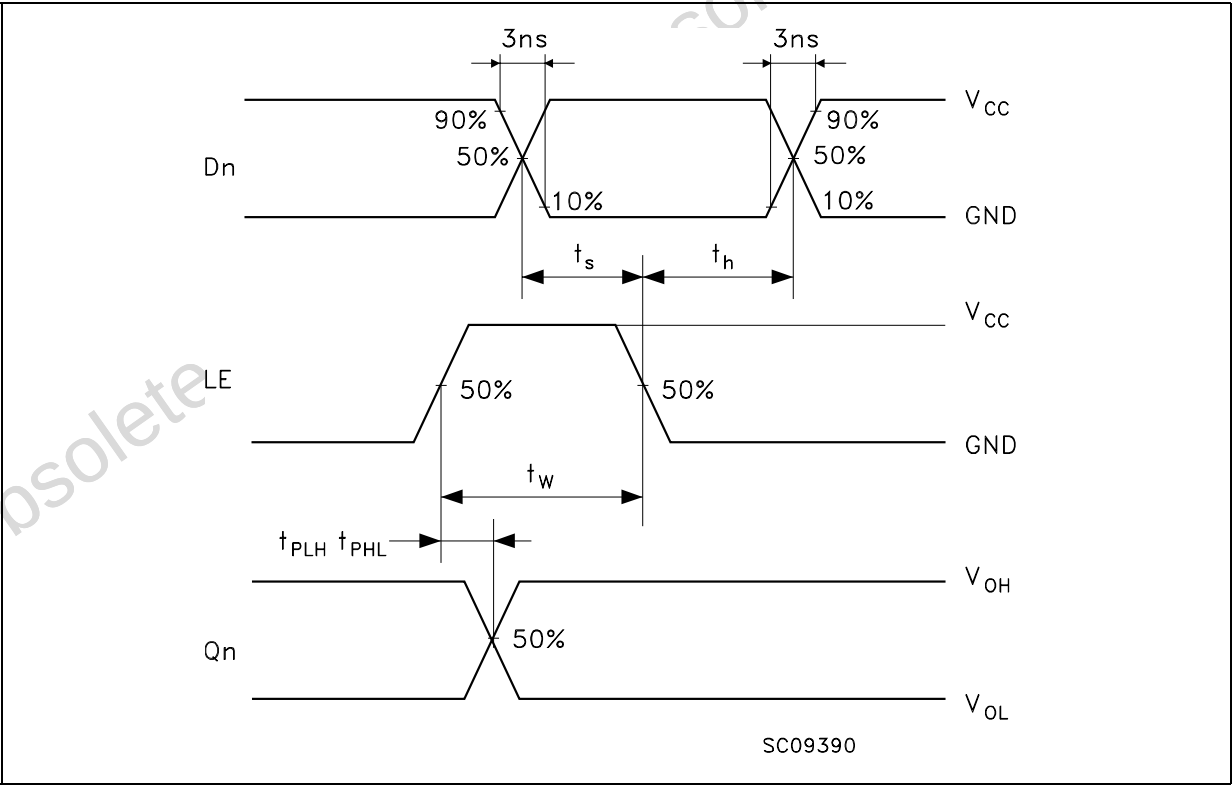


Figure 6: Waveform - Output Enable And Disable Times ( $f=1\text{MHz}$ ; 50% duty cycle)

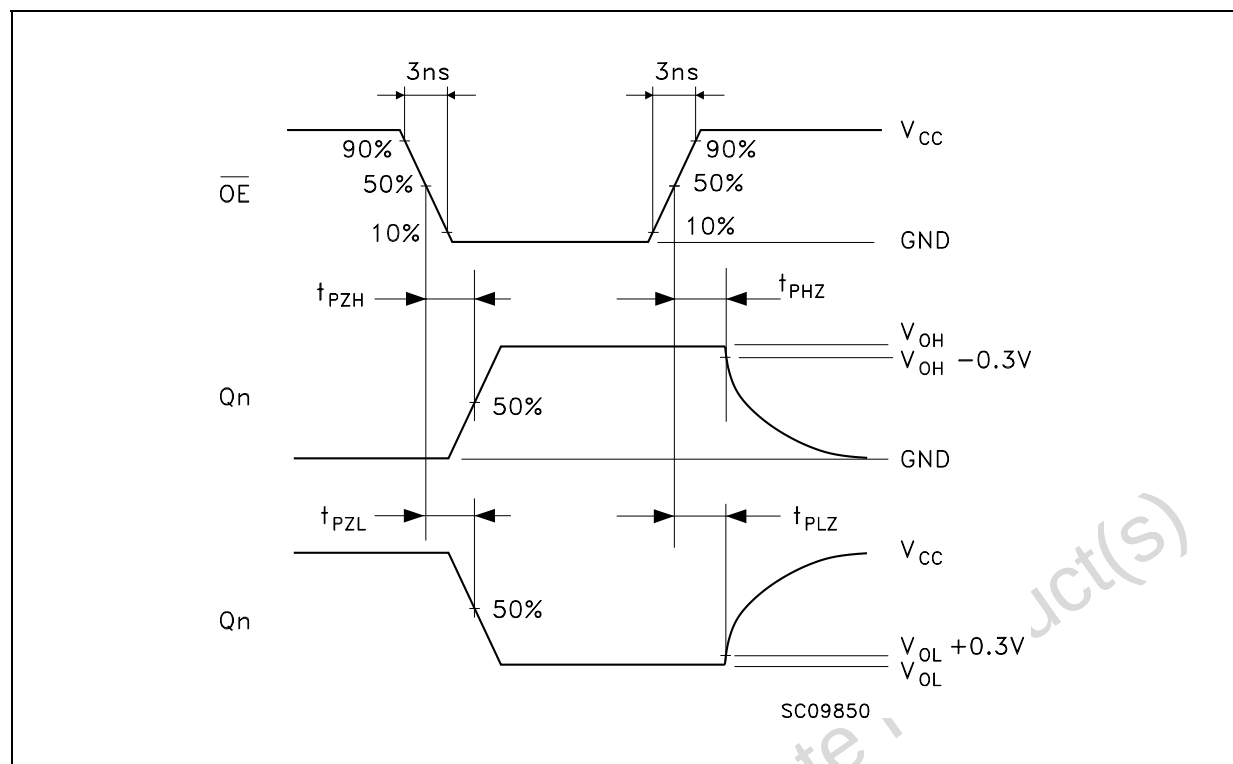
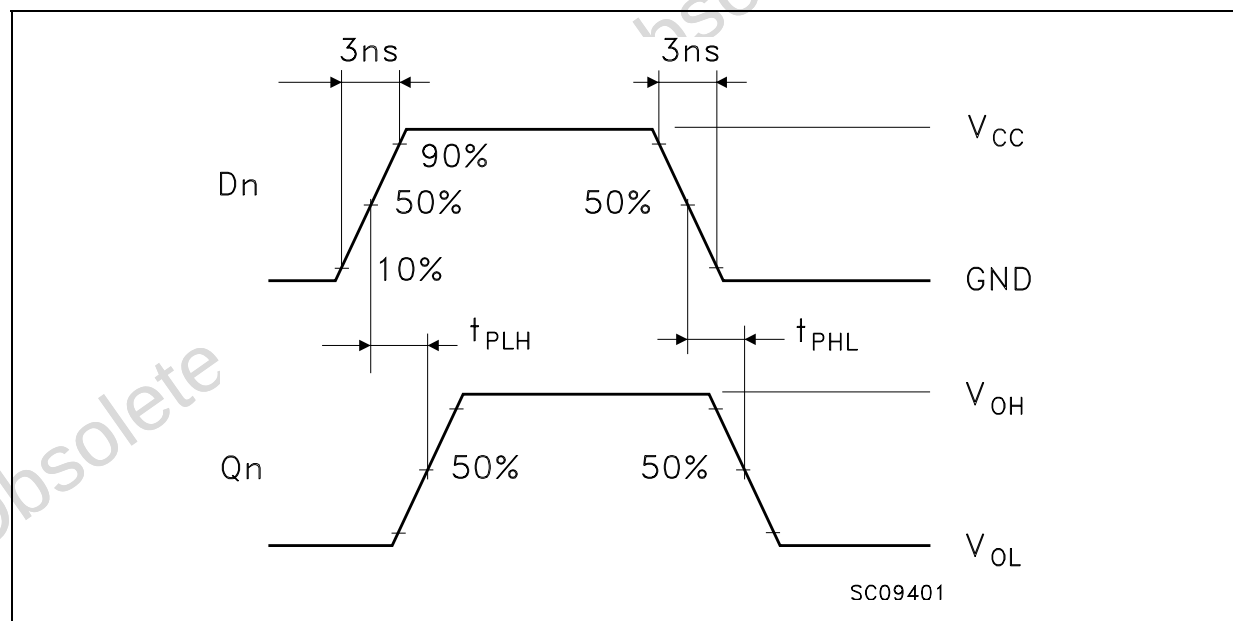


Figure 7: Waveform - Propagation Delay Time ( $f=1\text{MHz}$ ; 50% duty cycle)



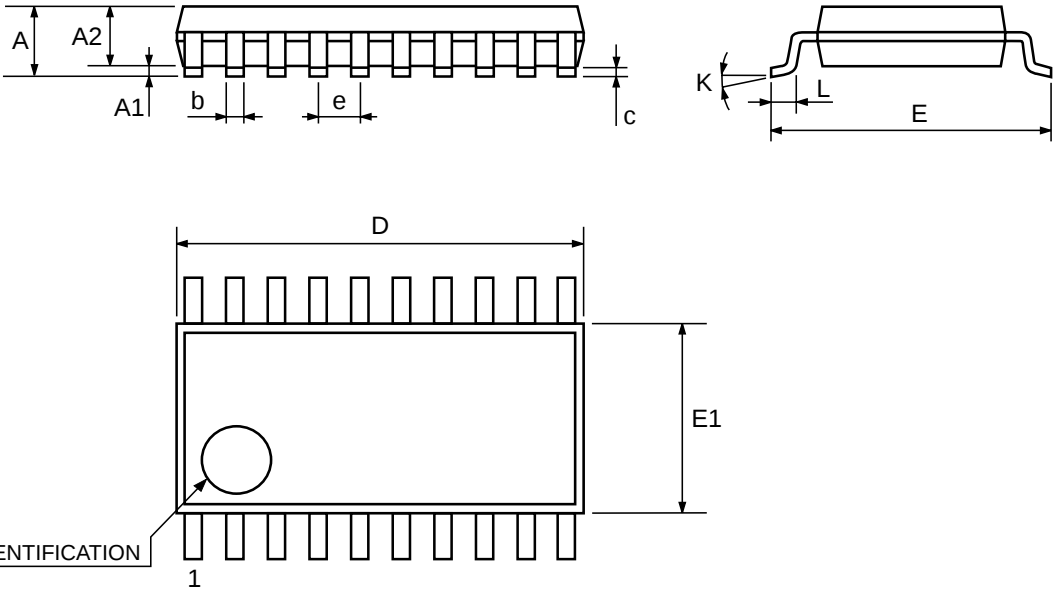
SO-20 MECHANICAL DATA

| DIM. | mm.   |      |       | inch  |       |       |
|------|-------|------|-------|-------|-------|-------|
|      | MIN.  | TYP  | MAX.  | MIN.  | TYP.  | MAX.  |
| A    | 2.35  |      | 2.65  | 0.093 |       | 0.104 |
| A1   | 0.1   |      | 0.30  | 0.004 |       | 0.012 |
| B    | 0.33  |      | 0.51  | 0.013 |       | 0.020 |
| C    | 0.23  |      | 0.32  | 0.009 |       | 0.013 |
| D    | 12.60 |      | 13.00 | 0.496 |       | 0.512 |
| E    | 7.4   |      | 7.6   | 0.291 |       | 0.299 |
| e    |       | 1.27 |       |       | 0.050 |       |
| H    | 10.00 |      | 10.65 | 0.394 |       | 0.419 |
| h    | 0.25  |      | 0.75  | 0.010 |       | 0.030 |
| L    | 0.4   |      | 1.27  | 0.016 |       | 0.050 |
| k    | 0°    |      | 8°    | 0°    |       | 8°    |
| ddd  |       |      | 0.100 |       |       | 0.004 |



TSSOP20 MECHANICAL DATA

| DIM. | mm.  |          |      | inch  |            |        |
|------|------|----------|------|-------|------------|--------|
|      | MIN. | TYP      | MAX. | MIN.  | TYP.       | MAX.   |
| A    |      |          | 1.2  |       |            | 0.047  |
| A1   | 0.05 |          | 0.15 | 0.002 | 0.004      | 0.006  |
| A2   | 0.8  | 1        | 1.05 | 0.031 | 0.039      | 0.041  |
| b    | 0.19 |          | 0.30 | 0.007 |            | 0.012  |
| c    | 0.09 |          | 0.20 | 0.004 |            | 0.0079 |
| D    | 6.4  | 6.5      | 6.6  | 0.252 | 0.256      | 0.260  |
| E    | 6.2  | 6.4      | 6.6  | 0.244 | 0.252      | 0.260  |
| E1   | 4.3  | 4.4      | 4.48 | 0.169 | 0.173      | 0.176  |
| e    |      | 0.65 BSC |      |       | 0.0256 BSC |        |
| K    | 0°   |          | 8°   | 0°    |            | 8°     |
| L    | 0.45 | 0.60     | 0.75 | 0.018 | 0.024      | 0.030  |



0087225C

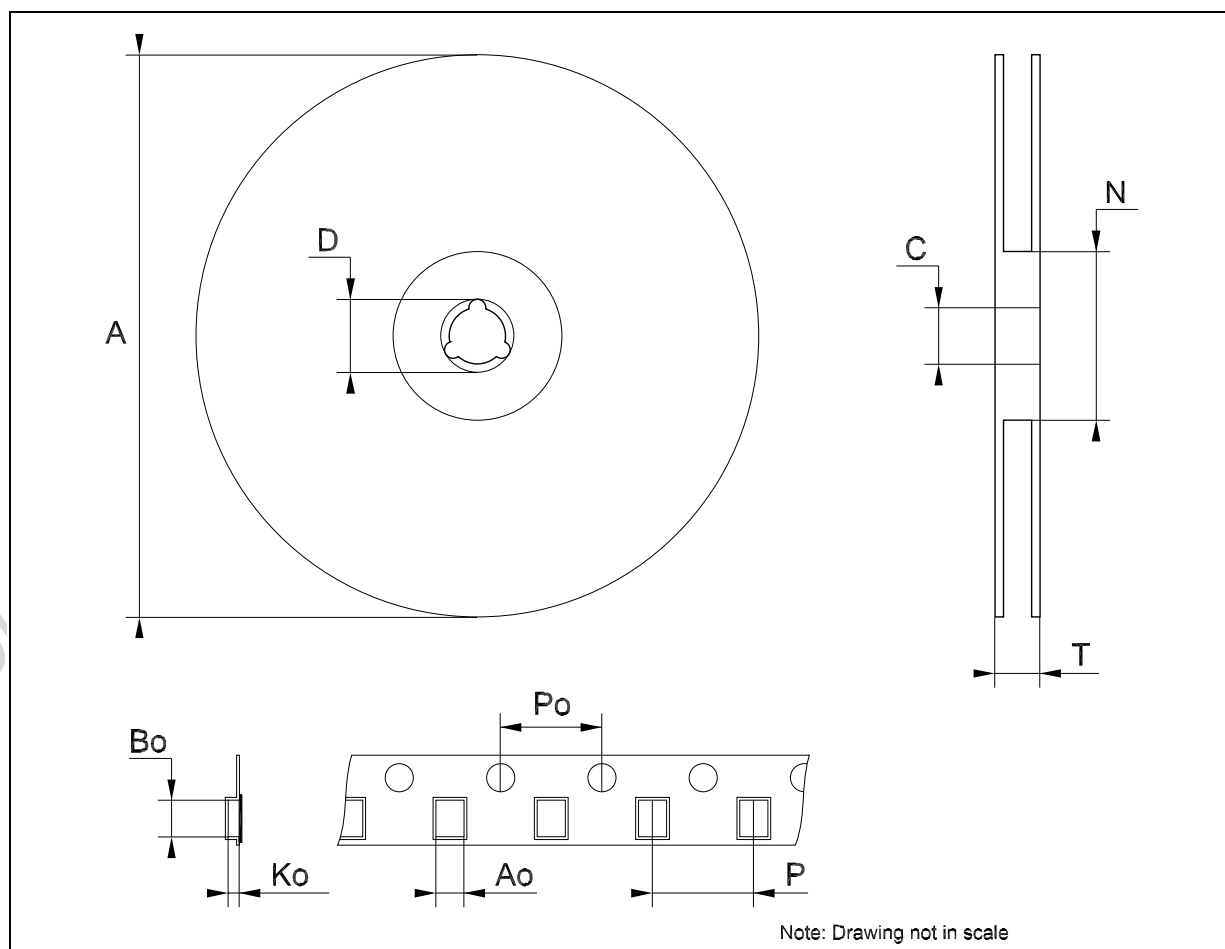
Tape & Reel SO-20 MECHANICAL DATA

| DIM. | mm.  |     |      | inch  |      |        |
|------|------|-----|------|-------|------|--------|
|      | MIN. | TYP | MAX. | MIN.  | TYP. | MAX.   |
| A    |      |     | 330  |       |      | 12.992 |
| C    | 12.8 |     | 13.2 | 0.504 |      | 0.519  |
| D    | 20.2 |     |      | 0.795 |      |        |
| N    | 60   |     |      | 2.362 |      |        |
| T    |      |     | 30.4 |       |      | 1.197  |
| Ao   | 10.8 |     | 11   | 0.425 |      | 0.433  |
| Bo   | 13.2 |     | 13.4 | 0.520 |      | 0.528  |
| Ko   | 3.1  |     | 3.3  | 0.122 |      | 0.130  |
| Po   | 3.9  |     | 4.1  | 0.153 |      | 0.161  |
| P    | 11.9 |     | 12.1 | 0.468 |      | 0.476  |



# Tape & Reel TSSOP20 MECHANICAL DATA

| DIM. | mm.  |     |      | inch  |      |        |
|------|------|-----|------|-------|------|--------|
|      | MIN. | TYP | MAX. | MIN.  | TYP. | MAX.   |
| A    |      |     | 330  |       |      | 12.992 |
| C    | 12.8 |     | 13.2 | 0.504 |      | 0.519  |
| D    | 20.2 |     |      | 0.795 |      |        |
| N    | 60   |     |      | 2.362 |      |        |
| T    |      |     | 22.4 |       |      | 0.882  |
| Ao   | 6.8  |     | 7    | 0.268 |      | 0.276  |
| Bo   | 6.9  |     | 7.1  | 0.272 |      | 0.280  |
| Ko   | 1.7  |     | 1.9  | 0.067 |      | 0.075  |
| Po   | 3.9  |     | 4.1  | 0.153 |      | 0.161  |
| P    | 11.9 |     | 12.1 | 0.468 |      | 0.476  |



**Table 10: Revision History**

| Date        | Revision | Description of Changes            |
|-------------|----------|-----------------------------------|
| 29-Jul-2004 | 5        | Ordering Codes Revision - pag. 1. |

Obsolete Product(s) - Obsolete Product(s)

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