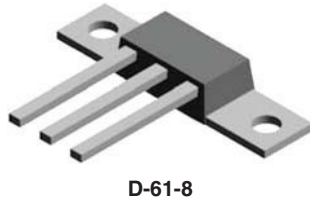




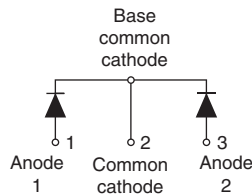
Schottky Rectifier

New Generation 3 D-61 Package, 2 x 40 A

VS-83CNQ...A



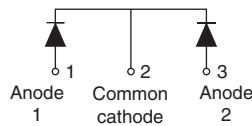
D-61-8



VS-83CNQ...ASM



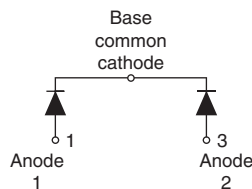
D-61-8-SM



VS-83CNQ...ASL



D-61-8-SL



FEATURES

- 175 °C T_J operation
- Center tap module
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- New fully transfer-mold low profile, small footprint, high current package
- Designed and qualified for industrial level

DESCRIPTION

The center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

PRODUCT SUMMARY

$I_{F(AV)}$	2 x 40 A
V_R	80 V/100 V

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	80	A
V_{RRM}	Range	80/100	V
I_{FSM}	$t_p = 5 \mu s$ sine	7000	A
V_F	40 A _{pk} , $T_J = 125^\circ C$ (per leg)	0.67	V
T_J	Range	- 55 to 175	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	VS-83CNQ080A	VS-83CNQ100A	UNITS
Maximum DC reverse voltage	V_R	80	100	V
Maximum working peak reverse voltage	V_{RWM}			

VS-83CNQ080A, VS-83CNQ100A Series

Vishay Semiconductors

Schottky Rectifier
New Generation 3 D-61 Package, 2 x 40 A

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 132 °C, rectangular waveform		80	A
Maximum peak one cycle non-repetitive surge current per leg See fig. 7	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	7000	
		10 ms sine or 6 ms rect. pulse		720	
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 30 mH		15	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	A

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	40 A	T _J = 25 °C	0.81	V
		80 A		1.00	
		40 A	T _J = 125 °C	0.67	
		80 A		0.82	
Maximum reverse leakage current per leg See fig. 2	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	1.5	mA
		T _J = 125 °C		35	
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz), 25 °C		1400	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		5.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/μs

Note⁽¹⁾ Pulse width < 300 μs , duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 55 to 175	°C
Maximum thermal resistance, junction to case per leg	R _{thJC}	DC operation See fig. 4	0.85	°C/W
Maximum thermal resistance, junction to case per package		DC operation	0.42	
Typical thermal resistance, case to heatsink (D-61-8 only)	R _{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30	
Approximate weight			7.8	g
			0.28	oz.
Mounting torque (D-61-8 only)	minimum	Recommended hardware 3M stainless screw	12 (10)	kgf · cm (lbf · in)
	maximum		24 (20)	
Marking device		Case style D-61-8	83CNQ080A	
			83CNQ100A	
		Case style D-61-8-SM	83CNQ080ASM	
			83CNQ100ASM	
		Case style D-61-8-SL	83CNQ080ASL	
			83CNQ100ASL	



VS-83CNQ080A, VS-83CNQ100A Series

Schottky Rectifier
New Generation 3 D-61 Package, 2 x 40 A

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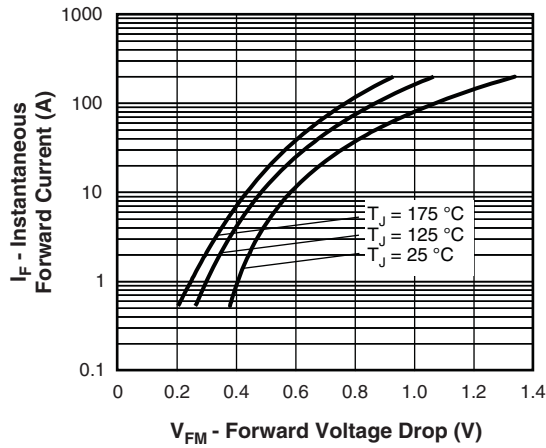


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

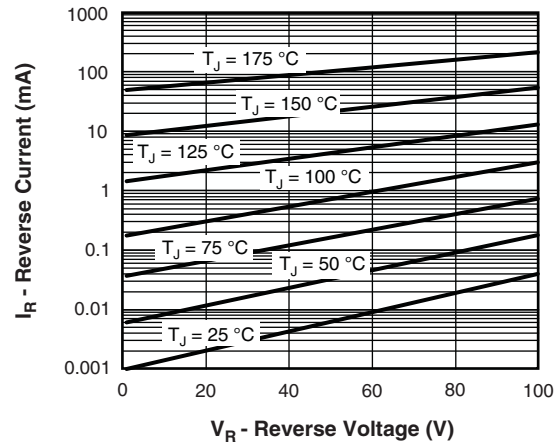


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

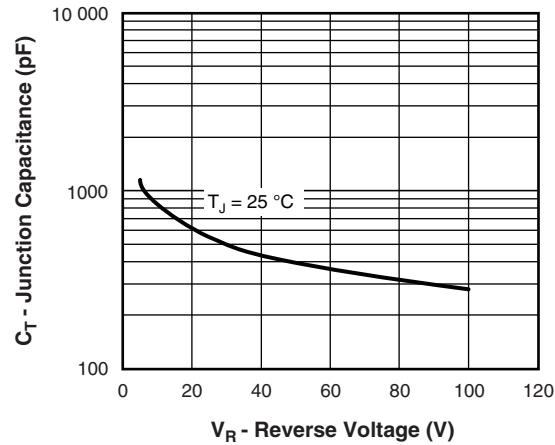


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

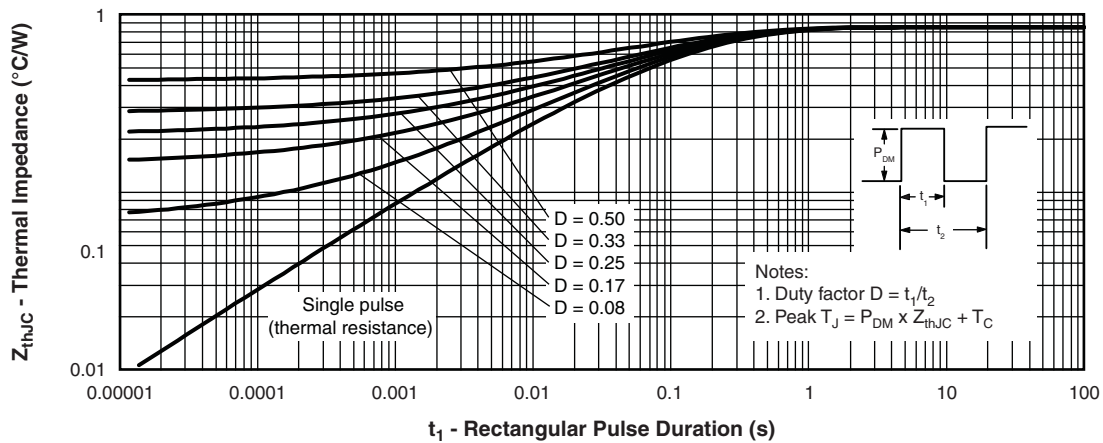


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

VS-83CNQ080A, VS-83CNQ100A Series



Vishay Semiconductors

Schottky Rectifier
New Generation 3 D-61 Package, 2 x 40 A

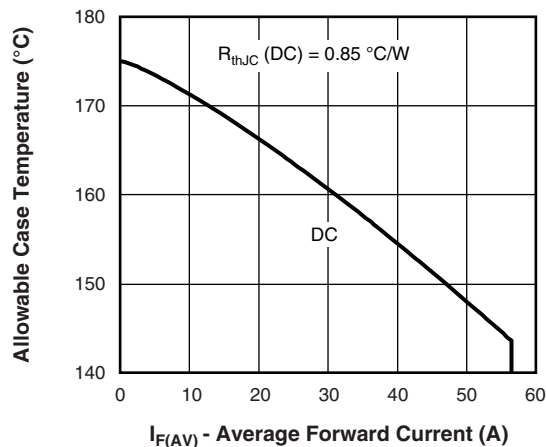


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

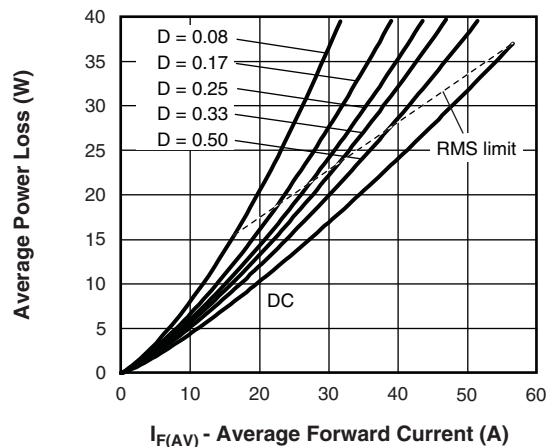


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

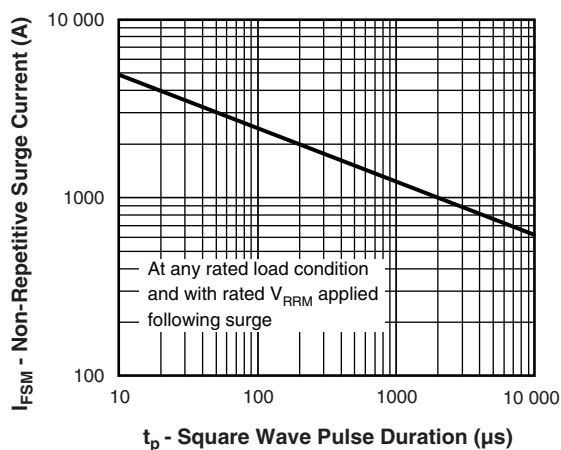


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

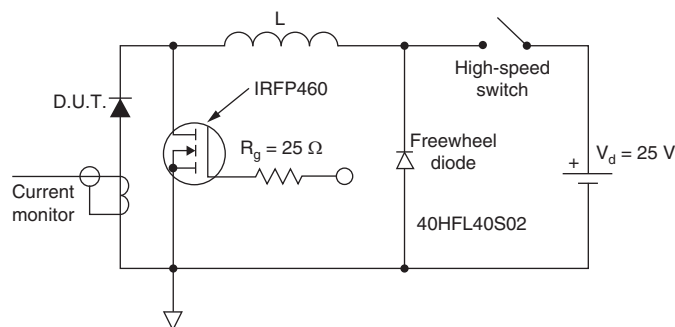


Fig. 8 - Unclamped Inductive Test Circuit



VS-83CNQ080A, VS-83CNQ100A Series

Schottky Rectifier
New Generation 3 D-61 Package, 2 x 40 A

Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code	VS-	83	C	N	Q	100	A	-
	1	2	3	4	5	6	7	8

- 1** - Vishay Semiconductors product
- 2** - Current rating (83 = 80 A)
- 3** - Circuit configuration:
C = Common cathode
- 4** - Package:
N = D-61
- 5** - Schottky "Q" series
- 6** - Voltage ratings ———— 080 = 80 V
100 = 100 V
- 7** - Package style:
 - A = D-61-8
 - ASM = D-61-8-SM
 - ASL = D-61-8-SL
- 8** -
 - None = Standard production
 - PbF = Lead (Pb)-free (D-61-8 only)

Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95354
Part marking information	www.vishay.com/doc?95356
SPICE model	www.vishay.com/doc?95290



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