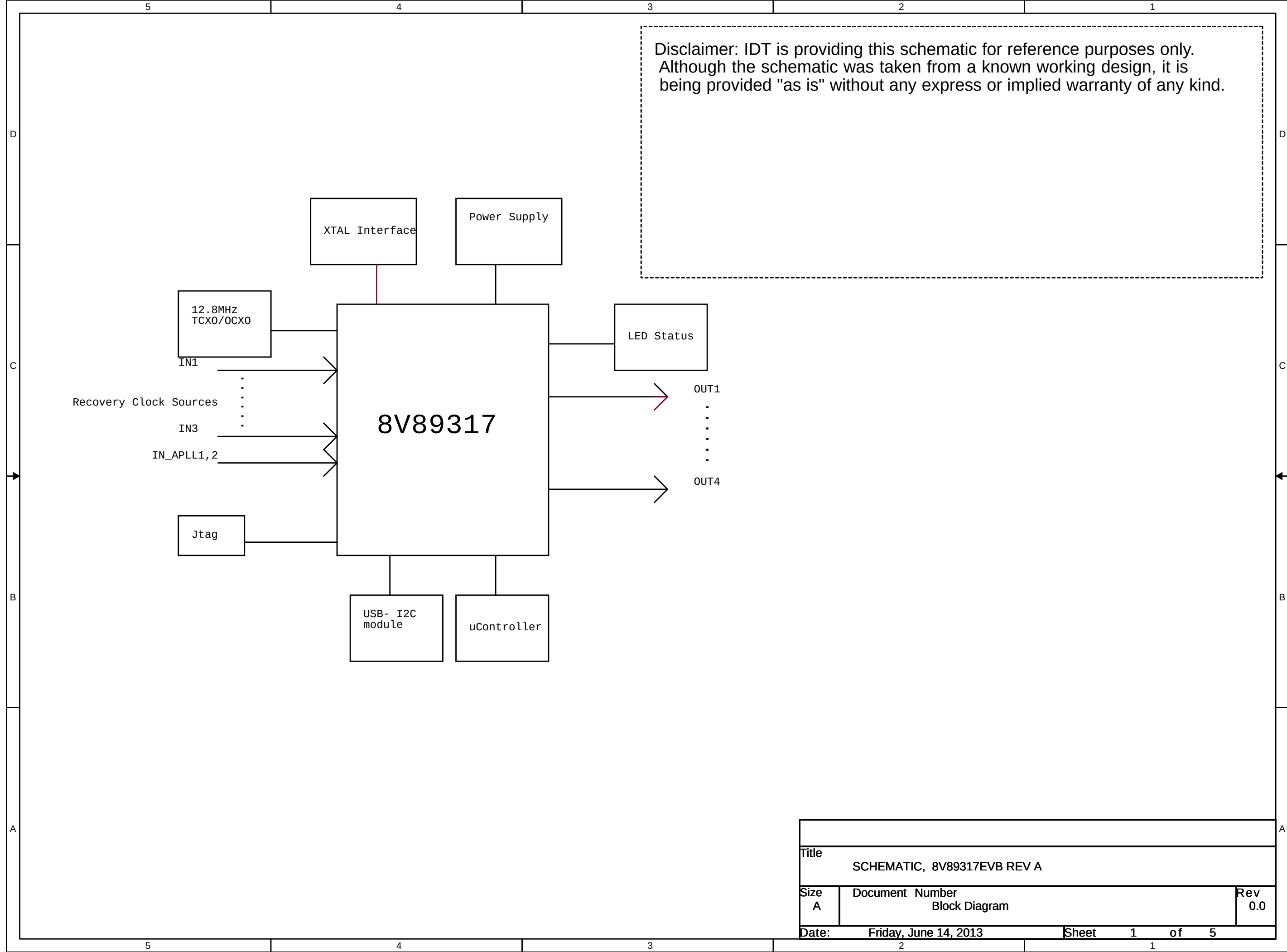
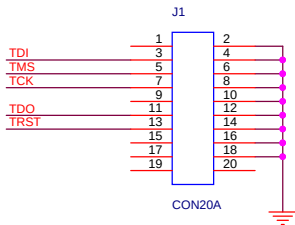
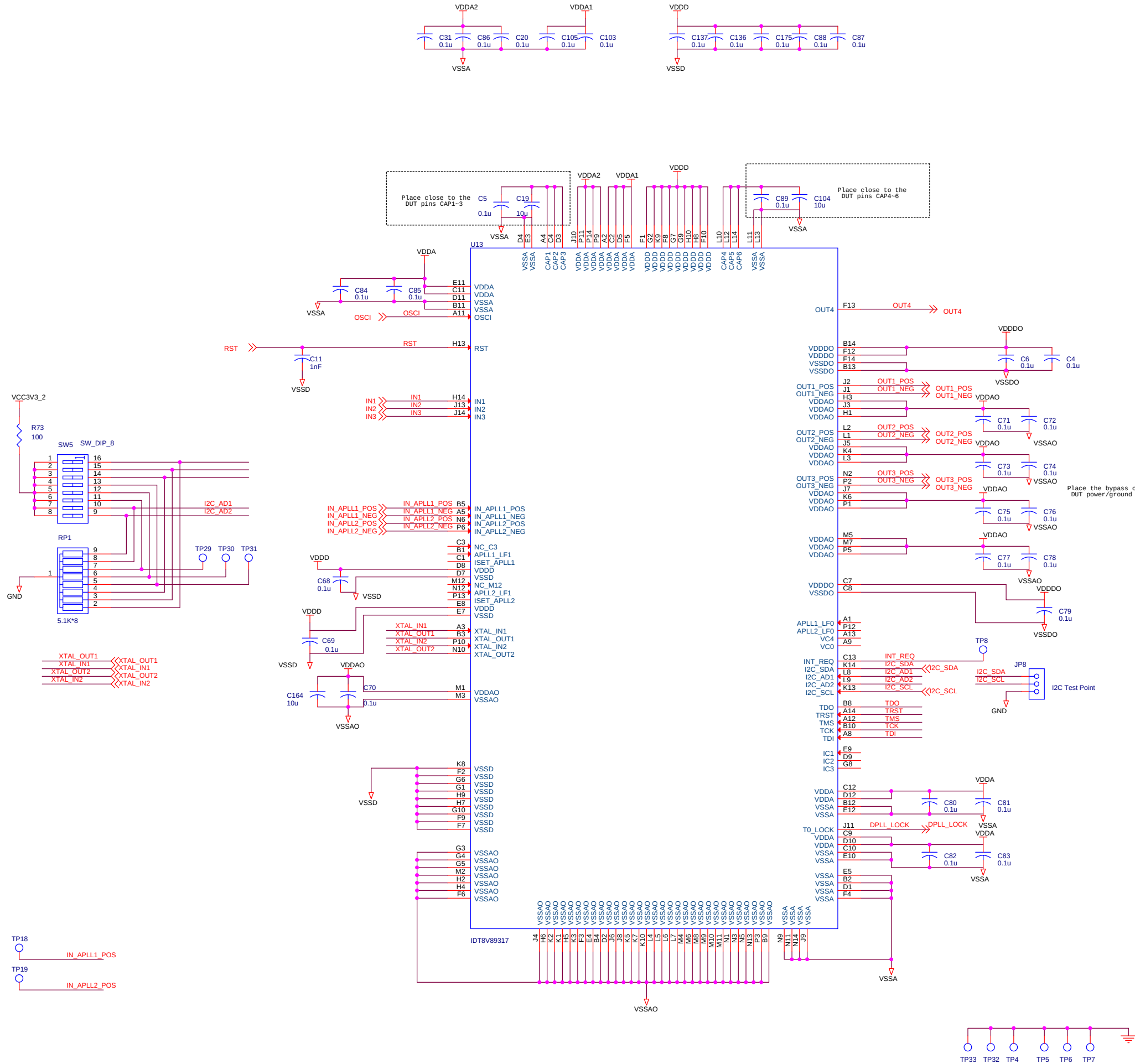
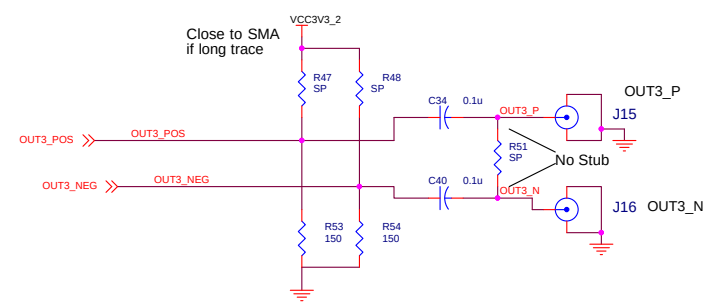
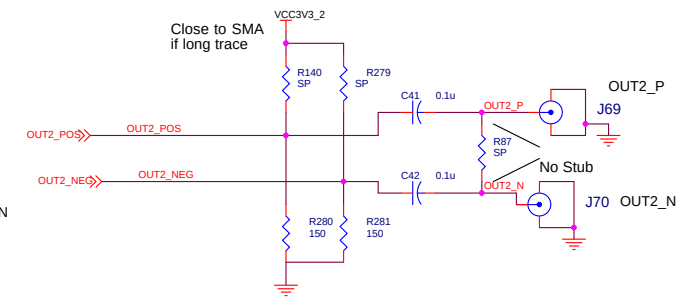
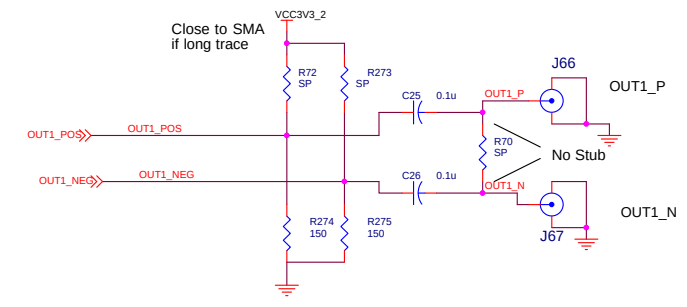
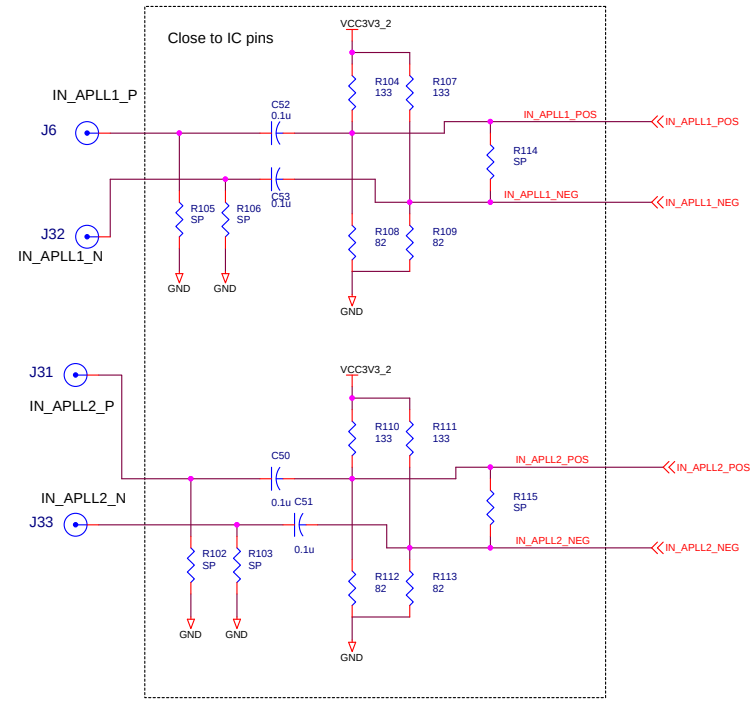
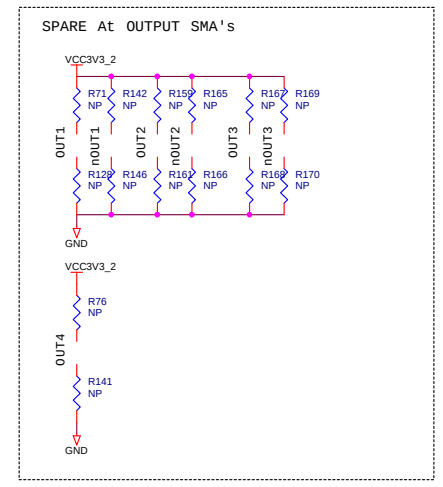
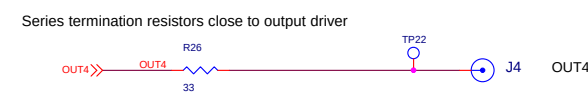
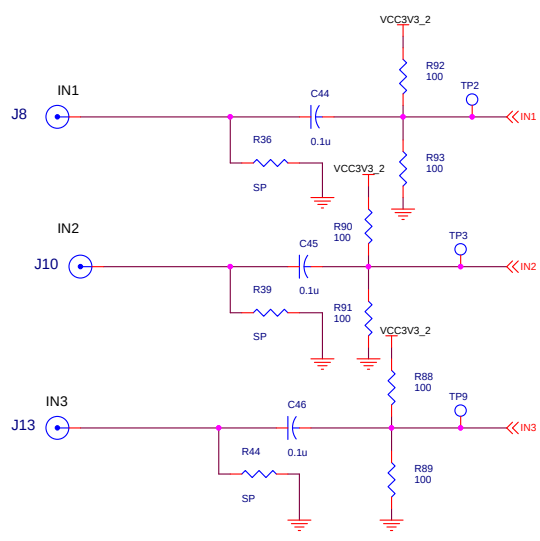




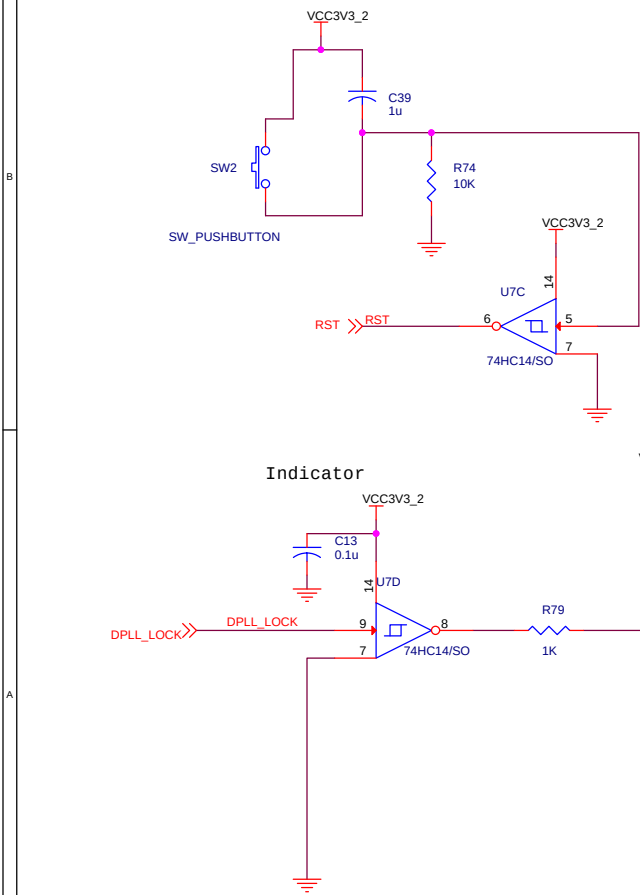
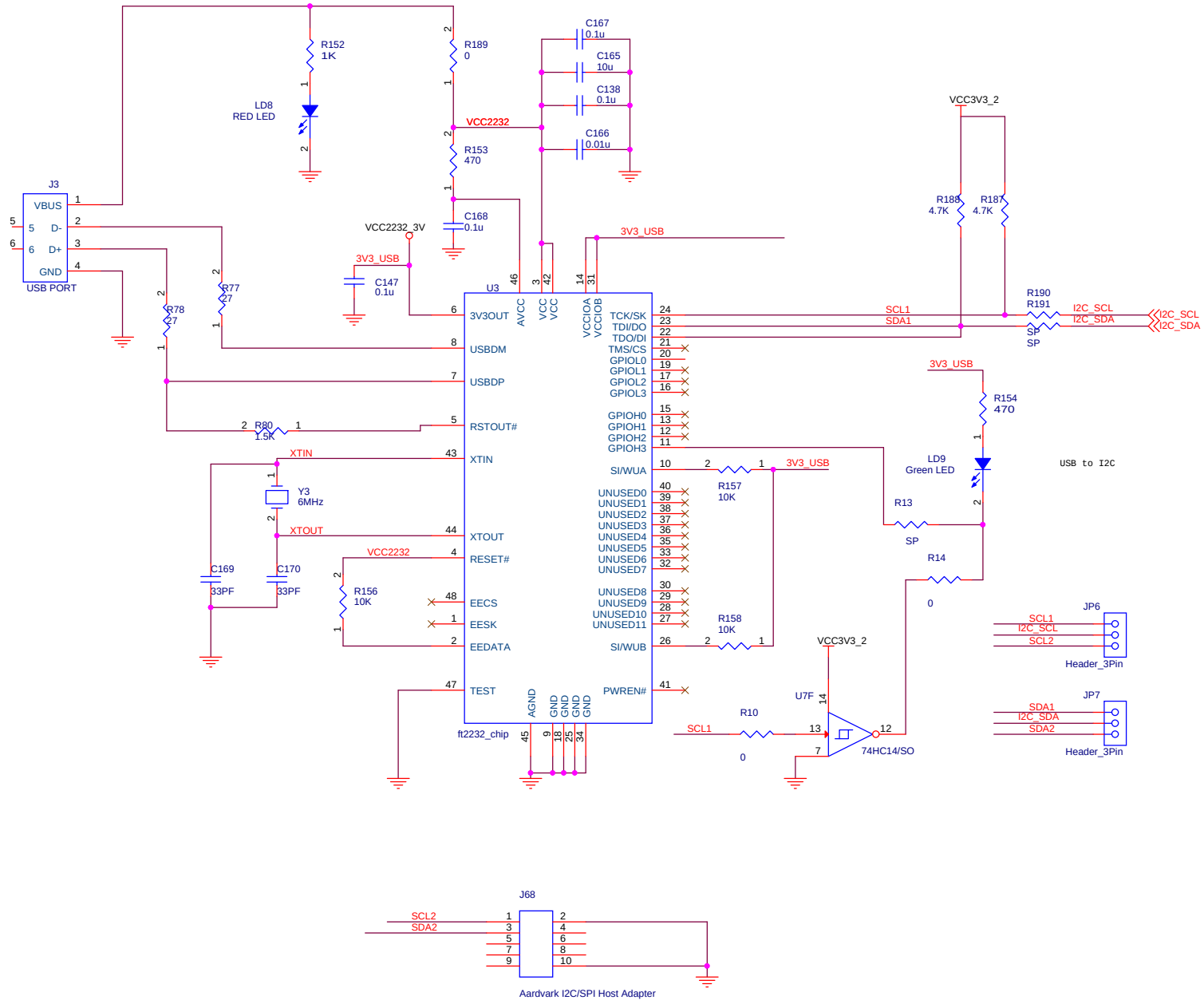
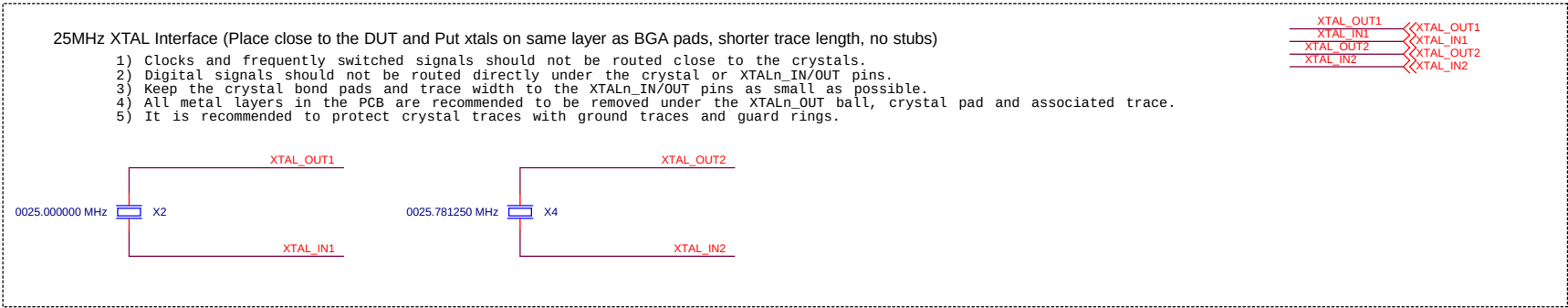
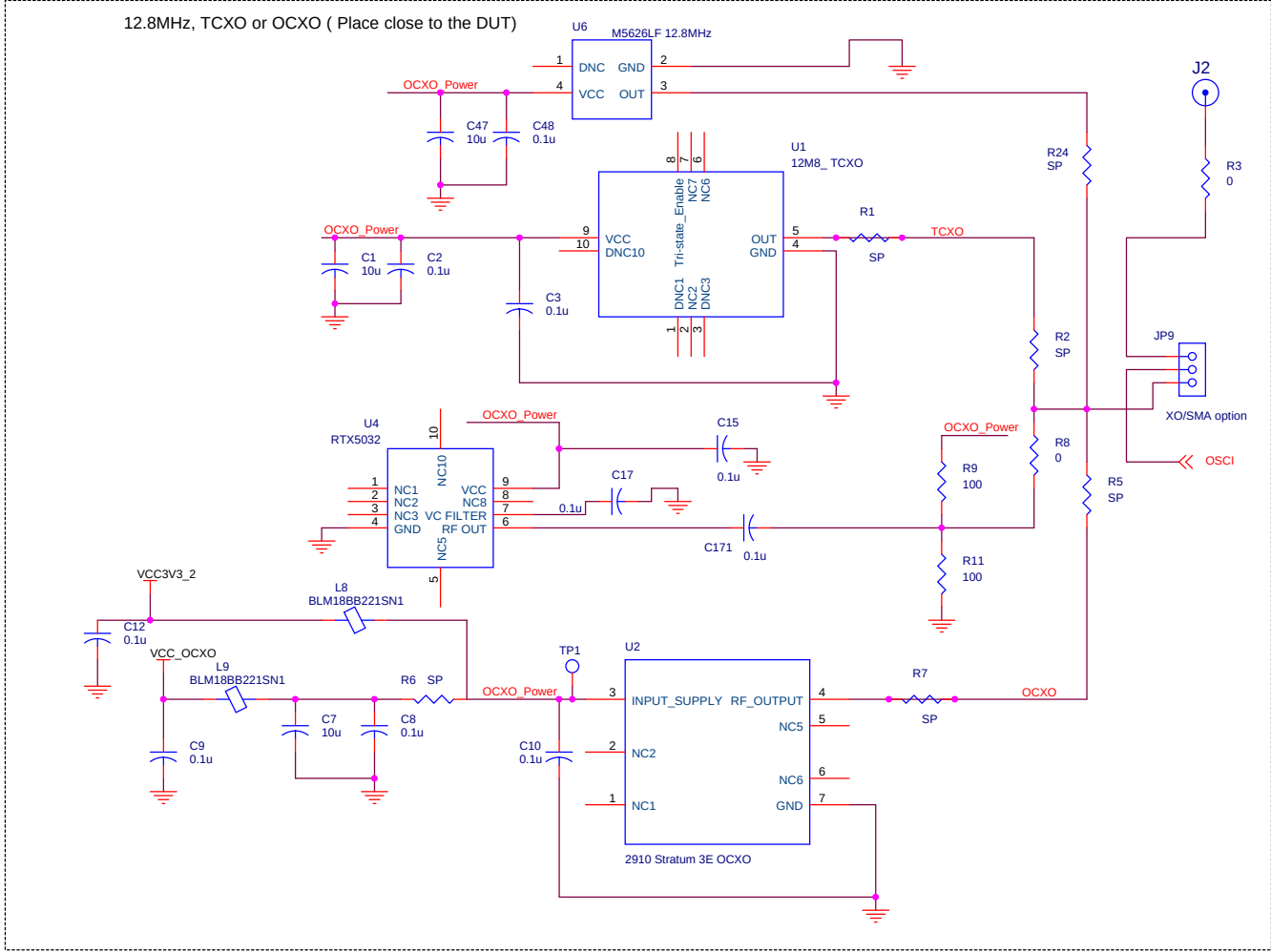


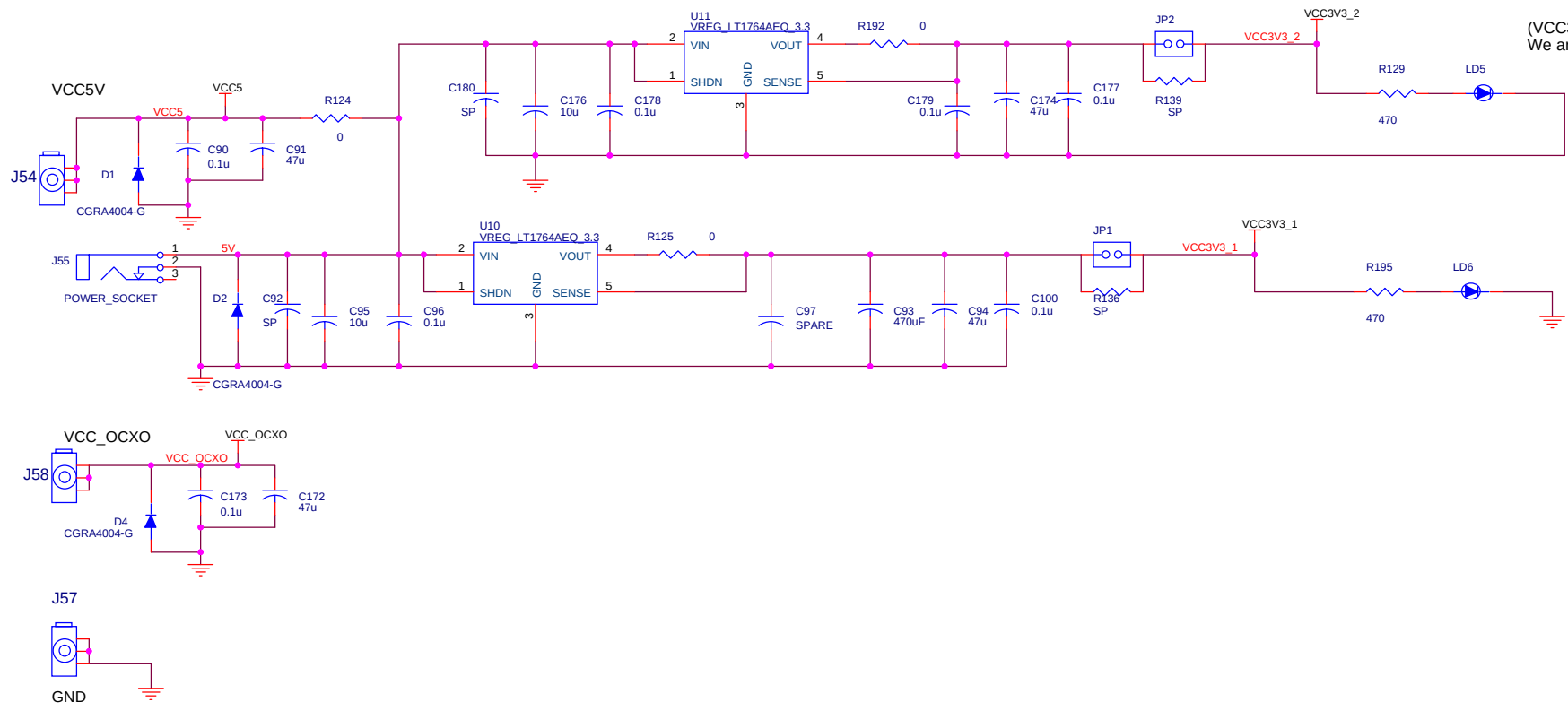
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SCHEMATIC, 8V89317 EVB REV A			
Size	Document Number	Rev	
Custom	8V89317	0.0	
Date:	Friday, June 14, 2013	Sheet	2 of 5



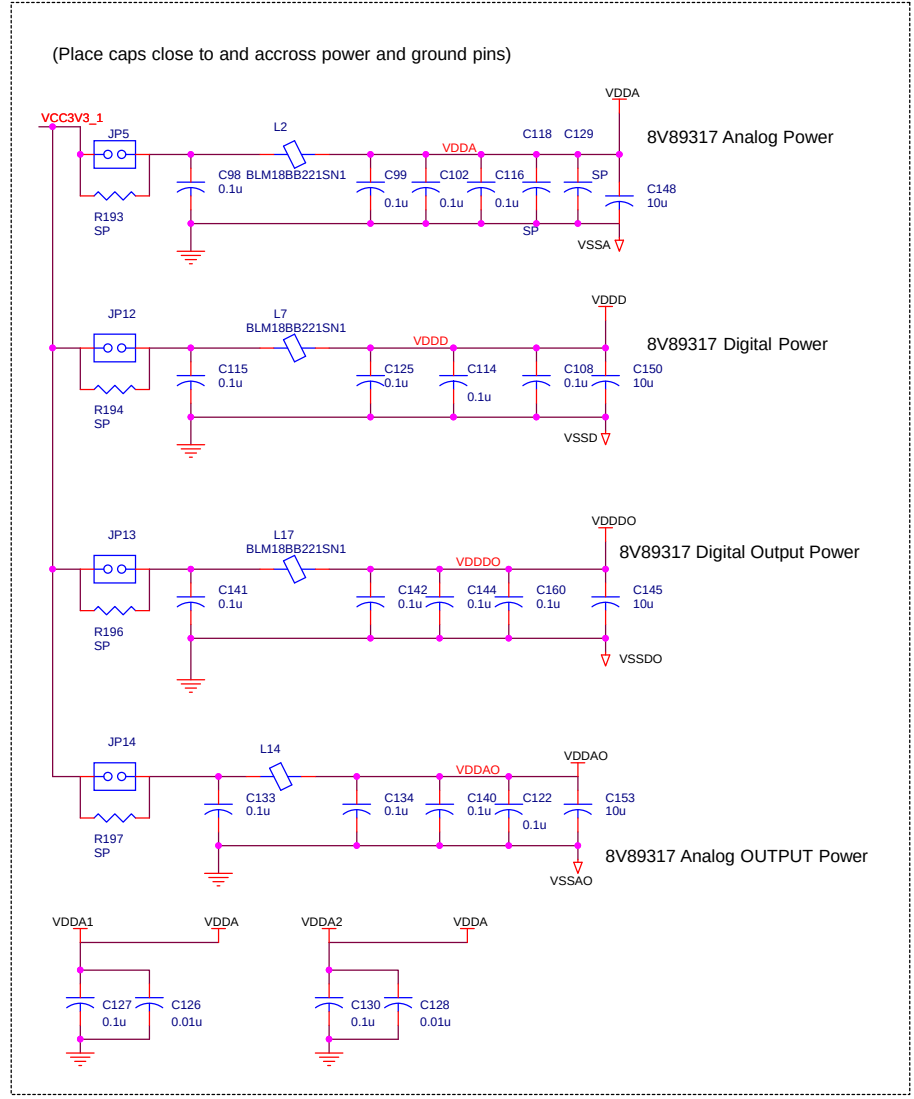
SMA's for input can be either END LAUNCH or STRAIGHT depends on which way save space. TBD during layout

Title			
SCHEMATIC, 8V89317 EVB REV A			
Size	Document	Number	Rev
Custom	8V89317_IO_Termination		0.0
Date:	Friday, June 14, 2013	Sheet	3 of 5





(VCC3V3_1 is used for the 8V89317 DUT and VCC3V3_2 is for the rest of the board circuits.
We are not suggesting a dedicated LDO for the 8V89317 with this reference design, this is just convenient for the test.)



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