

**SHORT FORM DATA SHEET**

The short form datasheet is intended to provide an overview only. Additional details are available from IDT. Contact information may be found on the last page.

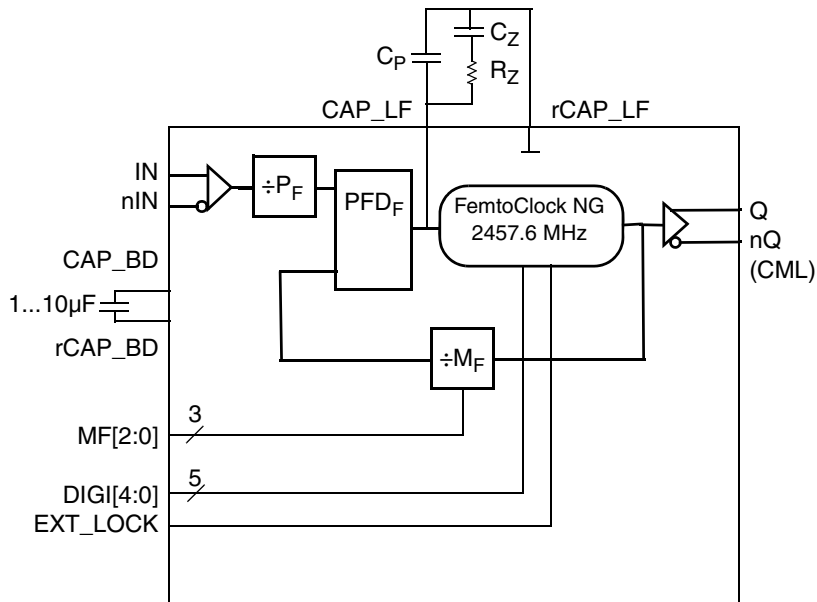
General Description

The IDT8V19N476-011 is a FemtoClock® NG Clock Synthesizer. The device has been designed to synthesize the RF clock frequency for the IDT8V19N486-02I VCXO-PLL. The device is optimized to deliver excellent phase noise performance by using IDT's fourth generation FemtoClock® NG technology. The PLL supports an internal state-of-the-art low phase noise VCO. The device is packaged in a lead-free (RoHS 6) 48-lead VFQFN package. The extended temperature range supports wireless infrastructure, telecommunication and networking end equipment requirements. The device is a member of the high-performance clock family from IDT.

Features

- High frequency 2547.6MHz clock synthesizer
- Designed to generate the RF clock signal for the IDT8V19N486-02I VCXO-PLL
- Fourth generation FemtoClock® NG technology
- Frequency multiplication from 153.6MHz to 2457.6MHz by an state-of-the-art low-phase-noise PLL (integrated VCO)
- Differential RF output, CML style (2457.6MHz)
- Differential clock input
- Supported input frequencies: 153.6MHz, 51.2MHz, 76.8MHz, 102.4MHz, 38.4MHz, 204.8MHz and 307.2MHz
- Clock output phase noise at 2457.6MHz:
 - 10Hz offset: -TBD dBc/Hz
 - 100Hz offset: -TBD dBc/Hz
 - 500Hz offset: -TBD dBc/Hz
 - 1kHz offset: -TBD dBc/Hz
 - 10kHz offset: -TBD dBc/Hz
 - 60kHz offset: -TBD dBc/Hz
 - 100kHz offset: -TBD dBc/Hz
 - 200kHz offset: -TBD dBc/Hz
 - 800kHz offset: -TBD dBc/Hz
 - 5MHz offset: -TBD dBc/Hz
 - 10MHz offset: -TBD dbc/Hz
- 3.3V core and output supply mode
- Supports 3.3V I/O logic levels for all control pins
- -40°C to +85°C ambient operating temperature
- Lead-free (RoHS 6) 48-lead VFQFN packaging

Block Diagram



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