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LOC

	DIST
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REVISIONS

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LTR

DESCRIPTION

DATE _____

DWN

APVD

A

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REVISÉ PER ECO-11-005301

13APR11

	RK
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	HMR
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NOTES/BEMERKUNG

1 AUSFÜHRUNG 928814-4/-5/-6 OHNE LOCH
EXECUTION 928814-4/-5/-6 WITHOUT HOLE

△ 3-7 μm Sn ÜBER 0.8 -1.5 μm Ni
3-7 μm Sn OVER 0.8-1.5 μm Ni

△ 4-8 μm Sn
4-8 μm Sn

△₄ ANLIEFERUNGSZUSTAND: EINZELWARE IN VERSANDKARTON
DELIVERED STATE ; LOOSE PIECE IN CORRUGATED SHIPPING BOX

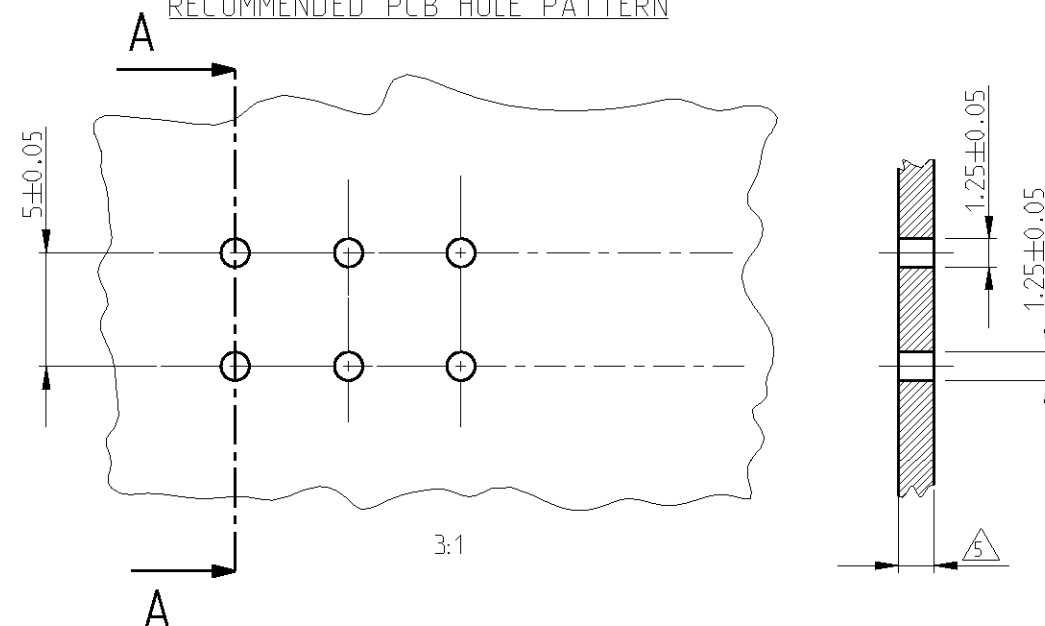
5 LEITERPLATTENDICKE NACH EN 60249-2-4. TABELLE 3
PCB-THICKNESS TO EN 60249-2-4. TABLE 3

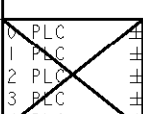
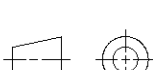
6 OBSOLETE



SCALE 1:1

EMPFOHLENES LEITERPLATTEN LOCHBILD
RECOMMENDED PCB HOLE PATTERN



DIMENSIONS: mm		TOLERANCES UNLESS OTHERWISE SPECIFIED:  ±0.2 ANGLES ±2°		DWN Dhanesh 06-Sep-07		 TE Connectivity					
				CHK W. Hoffman 04-Okt-07							
		APVD Gerlach -		NAME SOLDER TAB 6.3x0.8 FOR PCB							
		PRODUCT SPEC -									
MATERIAL SEE TABLE		APPLICATION SPEC		SIZE A3		CAGE CODE		DRAWING NO. C-928814		RESTRICTED TO	
		WEIGHT -		CUSTOMER DRAWING		SCALE 2:1		SHEET 1 OF 1		REV A2	

Mouser Electronics

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[TE Connectivity:](#)

[928814-1](#)