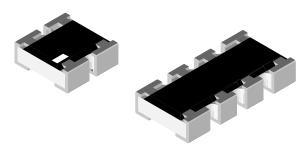
# Vishay Beyschlag



# Precision Thin Film Chip Resistor Array Superior Moisture Resistivity



ACAS 0606 AT and ACAS 0612 AT precision automotive grade thin film chip resistor arrays with convex terminations combine the proven reliability of discrete chip resistors with the advantages of chip resistor arrays. Defined tolerance matching and TCR tracking make this product perfectly suited for applications with outstanding requirements towards stable fixed resistor ratios. Four equal resistor values or two pairs are available for the ACAS 0612 AT, whereas the ACAS 0606 AT is available either with two equal or two different resistor values. Find out more about Vishay's automotive grade product requirements at: <a href="https://www.vishay.com/applications">www.vishay.com/applications</a>

## **FEATURES**

- Superior moisture resistivity,  $|\Delta R/R|$  < 0.5 % (85 °C; 85 % RH; 1000 h)
- Rated dissipation P<sub>70</sub> up to 125 mW per resistor
- ESD capability 1000 V, human body model
- TCR tracking down to 10 ppm/K (± 5 ppm/K) and tolerance matching down to 0.1 % (± 0.05 %)
- AEC-Q200 qualified
- Compliant to RoHS directive 2002/95/EC

#### **APPLICATIONS**

- Precision analogue circuits
- Voltage divider
- · Feedback circuits
- · Signal conditioning

DESCRIPTION	ACAS 0606 AT	ACAS 0612 A	
EIA size	0606	0612	
Metric size	RR 1616MM	RR 1632M	
Configuration, isolated	2 x 0603	4 x 0603	
Design:			
All equal values (AE)	AE	AE	
Two pairs of values (TP)		TP	
Different values (DF)	DF		
Resistance values	47 Ω to 1	50 kΩ <sup>(1)</sup>	
Absolute tolerance	± 0.5 %;	± 0.25 %	
Tolerance matching <sup>(2)</sup>	0.5 % (equivalent to $\pm$ 0.25 %) 0.25 % (equivalent to $\pm$ 0.125 %) 0.1 % (equivalent to $\pm$ 0.05 %)		
Absolute temperature coefficient	± 50 ppm/K; ± 25 ppm/K		
Temperature coefficient tracking (2)	50 ppm/K (equivalent to $\pm$ 25 ppm/K) 25 ppm/K (equivalent to $\pm$ 12.5 ppm/K) 15 ppm/K (equivalent to $\pm$ 7.5 ppm/K) 10 ppm/K (equivalent to $\pm$ 5 ppm/K)		
Max. resistance ratio $R_{min.}/R_{max.}$	1:20		
Rated dissipation: P <sub>70</sub> <sup>(3)</sup>			
Element	0.125 W	0.125 W	
Package	0.2 W 0.4 W		
Operating voltage, <i>U</i> <sub>max.</sub> AC/DC	75	V	
Permissibe film temperature	155 °C		
Insulation voltage ( $U_{ins}$ ) against ambient and between integrated resistors, continuous	75 V		

## **Notes**

- (1) Resistance values to be selected from E24 and E96.
- (2) In applications with defined resistance ratios like voltage dividers or feedback circuits, an array with a defined tracking of e.g. 10 ppm/K is required to replace discrete resistors with a temperature coefficient of resistance of ± 5 ppm/K. Furthermore, in order to achieve the same tolerance of ± 0.05 % of individual resistors, an array requires a matching of 0.1 %.
- (3) Please refer to APPLICATION INFORMATION, see below.



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## **APPLICATION INFORMATION**

The power dissipation on the resistor generates a temperature rise against the local ambient, depending on the heat flow support of the printed-circuit board (thermal resistance). The rated dissipation applies only if the permitted film temperature is not exceeded. These resistors do not feature a limited lifetime when operated within the permissible limits.

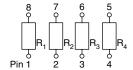
MAXIMUM RESISTANCE CHANGE AT RATED POWER (1)								
DESCRIPTION		ACAS (	0606 AT	ACAS 0612 AT				
Configuration, isolated		2 x 0603		4 x 0603				
Operation mode		Standard	Power	Standard	Power			
Rated power per element, P <sub>70</sub>		0.1 W	0.125 W	0.1 W	0.125 W			
Rated power per package, P <sub>70</sub>		0.15 W	0.2 W	0.3 W	0.4 W			
Film temperature		125 °C	155 °C	125 °C	155 °C			
Max. resistance change at P <sub>70</sub>								
$\Delta R/R$ max., after:	1000 h	± 0.1 %	± 0.25 %	± 0.1 %	± 0.25 %			
	8000 h	± 0.25 %	± 0.5 %	± 0.25 %	± 0.5 %			
Max. relative resistance change (re								
$\Delta R/R$ max., after:	1000 h	0.1 % (2)	0.25 % <sup>(3)</sup>	0.1 % <sup>(2)</sup>	0.25 % <sup>(3)</sup>			
	8000 h	0.25 % (3)	0.5 % <sup>(4)</sup>	0.25 % <sup>(3)</sup>	0.5 % (4)			

#### Notes

## **SKETCHES**



ACAS 0606 AT



ACAS 0612 AT

Marking on ACAS 0606 AT: For types with different resistor values pin 1 is marked.

DESIGN							
TYPE	ACAS 0606 AT	ACAS 0612 AT					
AE	$R_1 = R_2$	$R_1 = R_2 = R_3 = R_4$					
TP		$R_1 = R_4 < R_2 = R_3$					
DF	R <sub>1</sub> < R <sub>2</sub>						

 $<sup>^{(1)}</sup>$  Figures are given for arrays with equal values, design type AE

<sup>(2)</sup> Equivalent to ± 0.05 %

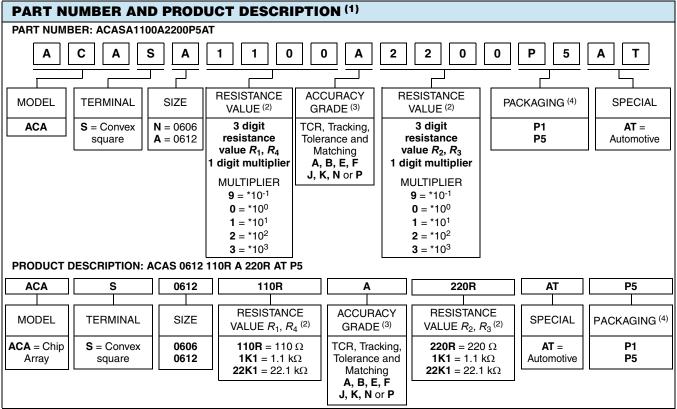
<sup>(3)</sup> Equivalent to ± 0.125 %

<sup>(4)</sup> Equivalent to ± 0.25 %

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# Precision Thin Film Chip Resistor Array Superior Moisture Resistivity





## Notes

- (1) Products can be ordered using either the PART NUMBER or the PRODUCT DESCRIPTION
- (2)  $R_1 = R_4 \le R_2 = R_3$
- (3) Please refer to table TEMPERATURE COEFFICIENT AND RESISTANCE RANGE, see below
- (4) Please refer to table PACKAGING, see below

TEMPERATU	TEMPERATURE COEFFICIENT AND RESISTANCE RANGE									
	DESCRIPTION									
ACCURACY GRADE	ABSOLUTE TCR	TCR TRACKING (5)	ABSOLUTE TOLERANCE	TOLERANCE MATCHING (5)	ACAS 0606 AT ACAS 0612 AT					
Α	± 25 ppm/K	10 ppm/K	± 0.25 %	0.1 %	47 $\Omega$ to 150 k $\Omega$					
В	± 25 ppm/K	10 ppm/K	± 0.5 %	0.25 %	47 $\Omega$ to 150 k $\Omega$					
E	± 25 ppm/K	15 ppm/K	± 0.25 %	0.1 %	47 $\Omega$ to 150 k $\Omega$					
F	± 25 ppm/K	15 ppm/K	± 0.5 %	0.25 %	47 $\Omega$ to 150 k $\Omega$					
J	± 25 ppm/K	25 ppm/K	± 0.25 %	0.1 %	47 $\Omega$ to 150 k $\Omega$					
K	± 25 ppm/K	25 ppm/K	± 0.5 %	0.25 %	47 $\Omega$ to 150 k $\Omega$					
N	± 50 ppm/K	25 ppm/K	± 0.5 %	0.5 %	47 $\Omega$ to 150 k $\Omega$					
Р	± 50 ppm/K	50 ppm/K	± 0.5 %	0.5 %	47 $\Omega$ to 150 k $\Omega$					

#### Note

 $^{(5)}$  Please refer to TECHNICAL SPECIFICATIONS, Note  $^{(2)}$ , see above

For technical questions, contact:  $\underline{\text{thinfilmarray} @ \text{vishay.com}}$ 

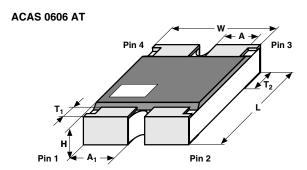


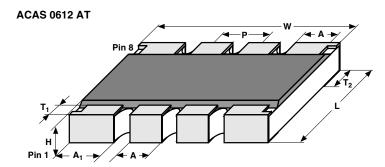
# Precision Thin Film Chip Resistor Array Superior Moisture Resistivity

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PACKAGING									
MODEL	TAPE WIDTH	DIAMETER	PIECES	PITCH	PACKAGING CODE				
MODEL	IAPE WIDTH	DIAMETER	PIECES	PITCH	PAPER TAPE				
ACAS 0606 AT	8 mm	180 mm/7"	1000	4 mm	P1				
ACAS 0612 AT	8 mm	180 mm/7"	5000	4 mm	P5				

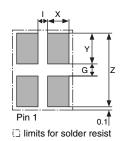
## **DIMENSIONS**

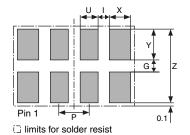




<b>DIMENSIONS</b> - chip resistor array, mass and relevant physical dimensions									
ТҮРЕ	L (mm)	W (mm)	H (mm)	P (mm)	A <sub>1</sub> (mm)	A (mm)	T <sub>1</sub> (mm)	T <sub>2</sub> (mm)	MASS (mg)
ACAS 0606 AT	1.5 ± 0.15	1.6 ± 0.15	$0.45 \pm 0.1$	-	0.6 ± 0.1	0.4 ± 0.1	$0.3 \pm 0.15$	$0.4 \pm 0.15$	3.6
ACAS 0612 AT 1.5 ± 0.15 3.2 ± 0.15 0.45 ± 0.1 0.8 ± 0.1 0.6 ± 0.1 0.4 ± 0.1 0.3 ± 0.15 0.4 ± 0.15 6.8									

# PATTERN STYLES FOR CHIP RESISTOR ARRAYS





Dimensions in mm

RECOMMENDED SOLDER PAD DIMENSIONS FOR CHIP RESISTOR ARRAYS									
TYPE	G (mm)	Y (mm)	X (mm)	U (mm)	Z (mm)	l (mm)	P (mm)		
ACAS 0606 AT	0.7	0.7	0.64	-	2.1	0.3	0.8		
ACAS 0612 AT	0.7	0.7	0.64	0.5	2.1	0.3	0.8		

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# Precision Thin Film Chip Resistor Array Superior Moisture Resistivity



#### **DESCRIPTION**

The production of the components is strictly controlled and follows an extensive set of instructions established for reproducibility. A homogeneous film of metal alloy is deposited on a high grade (96 % Al<sub>2</sub>O<sub>3</sub>) ceramic substrate using a mask to separate the adjacent resistors and conditioned to achieve the desired temperature coefficient. Specially designed inner contacts are realized on both sides. A special laser is used to achieve the target value by smoothly cutting a meander groove in the resistive layer without damaging the ceramics.

The resistor elements are covered by a protective coating designed for electrical, mechanical and climatic protection. The terminations receive a final pure tin on nickel plating.

The result of the determined production is verified by an extensive testing procedure and optical inspection performed on 100 % of the individual chip resistors. Only accepted products are laid directly into the paper tape in accordance with **IEC 60286-3** (3).

#### **ASSEMBLY**

The resistors are suitable for processing on automatic SMD assembly systems. They are suitable for automatic soldering using reflow or vapour phase as shown in **IEC 61760-1** <sup>(3)</sup>. The encapsulation is resistant to all cleaning solvents commonly used in the electronics industry, including alcohols, esters and aqueous solutions.

The suitability of conformal coatings, if applied, shall be qualified by appropriate means to ensure the long-term stability of the whole system. The resistors are RoHS compliant; the pure tin plating provides compatibility with lead (Pb)-free and lead-containing soldering processes. The permitted storage time is 20 years, whereas the solderability is specified for 2 years after production or requalification. The immunity of the plating against tin whisker growth has been proven under extensive testing.

All products comply with the **GADSL** <sup>(1)</sup> and the **CEFIC-EECA-EICTA** <sup>(2)</sup> list of legal restrictions on hazardous substances. This includes full compliance with the following directives:

- 2000/53/EC End of Vehicle life Directive (ELV) and Annex II (ELV II)
- 2002/95/EC Restriction of the use of Hazardous Substances directive (RoHS)
- 2002/96/EC Waste Electrical and Electronic Equipment Directive (WEEE)

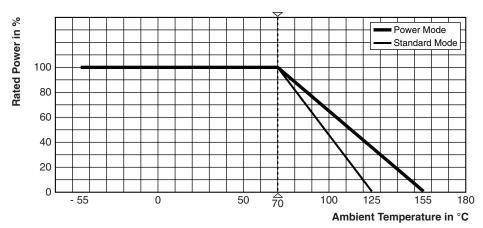
## **APPROVALS**

The chip resistor array is **AEC-Q200** qualified. Where applicable, the resistors are tested in accordance with **EN 140401-801** which refers to **EN 60115-1** and **EN 140400**.

## **Notes**

- (1) Global Automotive Declarable Substance List, see <a href="https://www.gadsl.org">www.gadsl.org</a>
- (2) CEFIC (European Chemical Industry Council), EECA (European Electronic Component Manufacturers Association), EICTA (European trade organisation representing the information and communications technology and consumer electronics), see <a href="https://www.eicta.org">www.eicta.org</a> → policy → environmental policy group → chemicals → jig → Joint Industry Guide (JIG-101 Ed 2.0)
- (3) The quoted IEC standards are also released as EN standards with the same number and identical contents

#### **FUNCTIONAL PERFORMANCE**



For permissible resistance change please refer to table MAXIMUM RESISTANCE CHANGE AT RATED POWER, above

Derating

For technical questions, contact: <a href="mailto:thinfilmarray@vishay.com">thinfilmarray@vishay.com</a>
Document Number: 28770
Revision: 06-Nov-09



# Precision Thin Film Chip Resistor Array Superior Moisture Resistivity

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## **TESTS AND REQUIREMENTS**

Essentially all tests are carried out in accordance with the following specifications:

EN 140400, Sectional specification EN 140401-801, Detail specification

The testing also covers most of the requirements specified by EIA/IS-703 and JIS-C-5202.

The tests are are carried out under standard atmospheric conditions according to **IEC 60068-1** <sup>(1)</sup>, 5.3. Climatic category LCT/UCT/56 (rated temperature range: Lower Category Temperature, Upper Category Temperature; damp heat, long term, 56 days) is valid (LCT = - 55 °C/UCT = 125 °C).

Unless otherwise specified the following values apply:

Temperature: 15 °C to 35 °C Relative humidity: 45 % to 75 %

Air pressure: 86 kPa to 106 kPa (860 mbar to 1060 mbar) The requirements stated in the "Test Procedures and Requirements" table are based on the required tests and permitted limits of EN 140401-801 where applicable.

TEST P	TEST PROCEDURES AND REQUIREMENTS							
EN 60115-1 CLAUSE	IEC 60068-2 <sup>(1)</sup> TEST METHOD	TEST	PROCEDURE	REQUIREMENTS $^{(2)}$ PERMISSIBLE CHANGE $(\Delta R)$				
			Stability for product types:					
			ACAS 0606 AT ACAS 0612 AT	47 $\Omega$ to 150 k $\Omega$ 47 $\Omega$ to 150 k $\Omega$				
4.5	-	Resistance	-	± 0.5 % R; ± 0.25 % R				
4.8.4.2	-	Temperature coefficient	At (20/- 55/ 20) °C and (20/125/20) °C	± 50 ppm/K; ± 25 ppm/K				
4.25.1		Endurance at 70 °C: Standard operation mode	$U = \sqrt{P_{70} \times R}$ or $U = U_{\text{max.}}$ ; 1.5 h on; 0.5 h off; 1000 h: Absolute Relative (3) 8000 h: Absolute Relative (3)	$\pm$ (0.1 % $R$ + 0.05 $\Omega$ ) 0.1 % $R$ + 0.05 $\Omega$ $\pm$ (0.25 % $R$ + 0.05 $\Omega$ ) 0.25 % $R$ + 0.05 $\Omega$				
4.25.1	-	Endurance at 70 °C: Power operation mode	$U = \sqrt{P_{70} \times R}$ or $U = U_{\text{max.}}$ ; 1.5 h on; 0.5 h off; 1000 h: Absolute Relative <sup>(3)</sup> 8000 h: Absolute Relative <sup>(3)</sup>	± (0.25 % $R$ + 0.05 $\Omega$ ) 0.25 % $R$ + 0.05 $\Omega$ ± (0.5 % $R$ + 0.05 $\Omega$ ) 0.5 % $R$ + 0.05 $\Omega$				
4.25.3	-	Endurance at upper category temperature	125 °C; 1000 h: Absolute Relative <sup>(3)</sup> 125 °C; 8000 h: Absolute Relative <sup>(3)</sup> 155 °C; 1000 h: Absolute Relative <sup>(3)</sup>	$\pm (0.25 \% R + 0.05 \Omega)$ $0.25 \% R + 0.05 \Omega)$ $\pm (0.5 \% R + 0.05 \Omega)$ $0.5 \% R + 0.05 \Omega$ $\pm (0.4 \% R + 0.05 \Omega)$ $0.4 \% R + 0.05 \Omega$				

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# Precision Thin Film Chip Resistor Array Superior Moisture Resistivity



TEST PROCEDURES AND REQUIREMENTS							
EN 60115-1 CLAUSE	IEC 60068-2 <sup>(1)</sup> TEST METHOD	TEST	PROCEDURE	REQUIREMENTS $^{(2)}$ PERMISSIBLE CHANGE $(\Delta R)$			
			Stability for product types:				
			ACAS 0606 AT ACAS 0612 AT	47 $\Omega$ to 150 k $\Omega$ 47 $\Omega$ to 150 k $\Omega$			
4.24	78 (Cab)	Damp heat, steady state	(40 ± 2) °C; 56 days; (93 ± 3) % RH	± (0.25 % R + 0.05 Ω)			
4.39	67 (Cy)	Damp heat, steady state, accelerated	$(85 \pm 2)$ °C $(85 \pm 5)$ % RH $U = 0.1 \times \sqrt{P_{70} \times R}$ $\leq 100 \text{ V};$ 1000  h	± (0.5 % R + 0.05 Ω)			
4.13	-	Short time overload (4)	$U = 2.5 \text{ x } \sqrt{P_{70} \text{ x } R} \text{ or } U = 2 \text{ x } U_{\text{max.}}; $ 5 s	± (0.1 % R + 0.01 Ω) no visible damage			
4.40	-	Electrostatic discharge (Human body model) <sup>(4)</sup>	IEC 61340-3-1; 3 pos. + 3 neg. (equivalent to MIL-STD-883, Method 3015); 1000 V	± (0.5 % R + 0.05 Ω)			
4.19	14 (Na)	Rapid change of temperature	30 min at - 55 °C and 30 min at 125 °C; 1000 cycles	± (0.25 % R + 0.05 Ω) no visible damage			
4.18.2	58 (Td)	Resistance to soldering heat	Reflow method 2 (IR/forced gas convention); $(260 \pm 5)$ °C; $(10 \pm 1)$ s	$\pm$ (0.1 % $R$ + 0.01 $\Omega$ ) no visible damage			
4.17.2	58 (Td)	Solderability	Solder bath method; SnPb; non-activated flux accelerated aging 4 h/155 °C (215 ± 3) °C; (3 ± 0.3) s  Solder bath method; SnAgCu; non-activated flux accelerated aging 4 h/155 °C (235 ± 3) °C; (2 ± 0.2) s	Good tinning (≥ 95 % covered); no visible damage			
4.32	21 (Ue <sub>3</sub> )	Shear (adhesion)	45 N	No visible damage			
4.33	21 (Ue <sub>1</sub> )	Substrate bending	Depth 2 mm, 3 times	$\pm$ (0.1 % $R$ + 0.01 $\Omega$ ) no visible damage; no open circuit in bent position			
4.35	-	Flammability	IEC 60695-11-5, needle flame test; 10 s	No burning after 30 s			
4.22	6 (Fc)	Vibration	Endurance by sweeping; 10 to 2000 Hz; no resonance; amplitude ≤ 1.5 mm or ≤ 200 m/s <sup>2</sup> ; 6 h	$\pm$ (0.1 % $R$ + 0.01 $\Omega$ ); no visible damage			
4.7	-	Voltage proof	U <sub>rms</sub> = U <sub>ins</sub> 60 ± 5 s; against ambient, between adjacent resistors	No flashover or breakdown			

#### **Notes**

- (1) The quoted IEC standards are also released as EN standards with the same number and identical contents
- $^{(2)}$  Figures are given for arrays with equal values, design type AE
- $^{(3)}$  Relative values are equivalent to the half of its value with  $\pm$  symbol, i.e. 0.1 % is equivalent to  $\pm$  0.05 %
- (4) For a single element



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Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

Revision: 02-Oct-12 Document Number: 91000