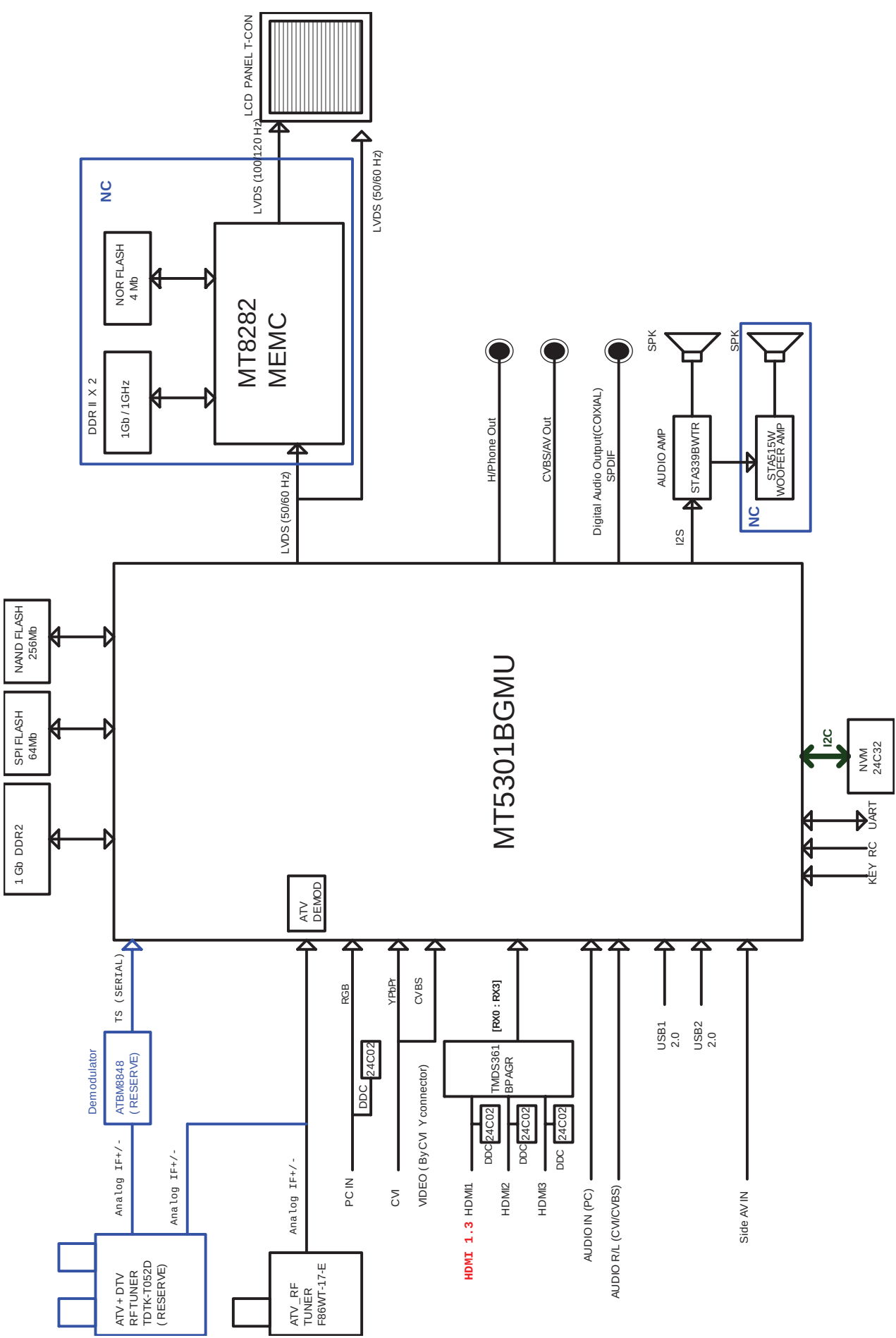
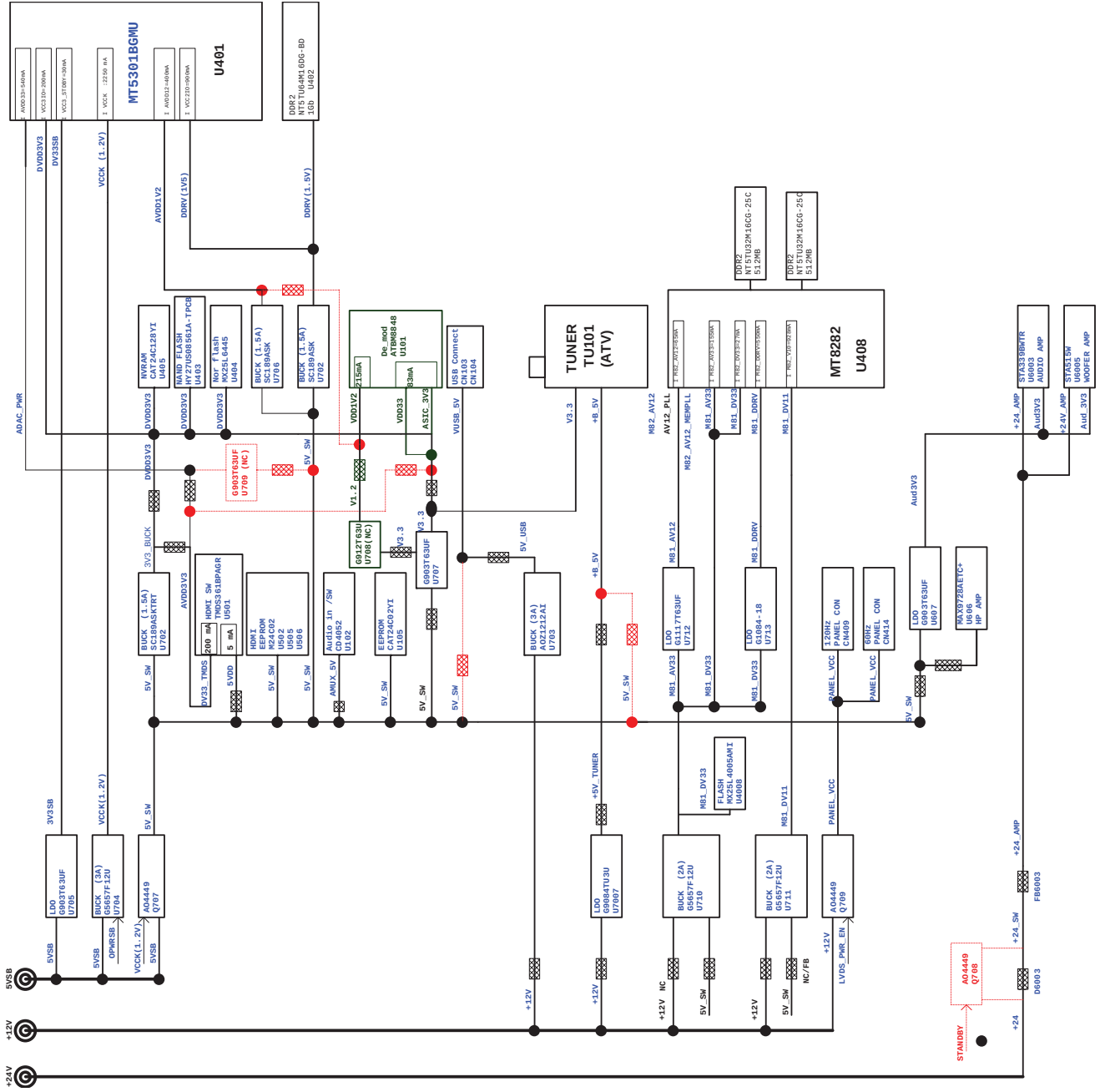


5. 电气方框图
主板方框图

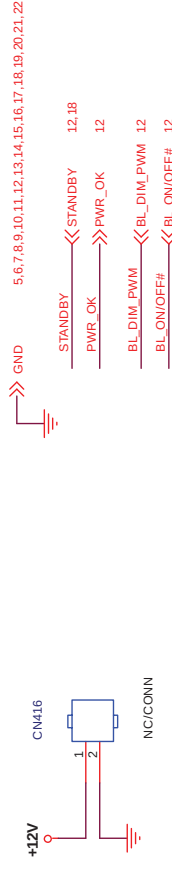
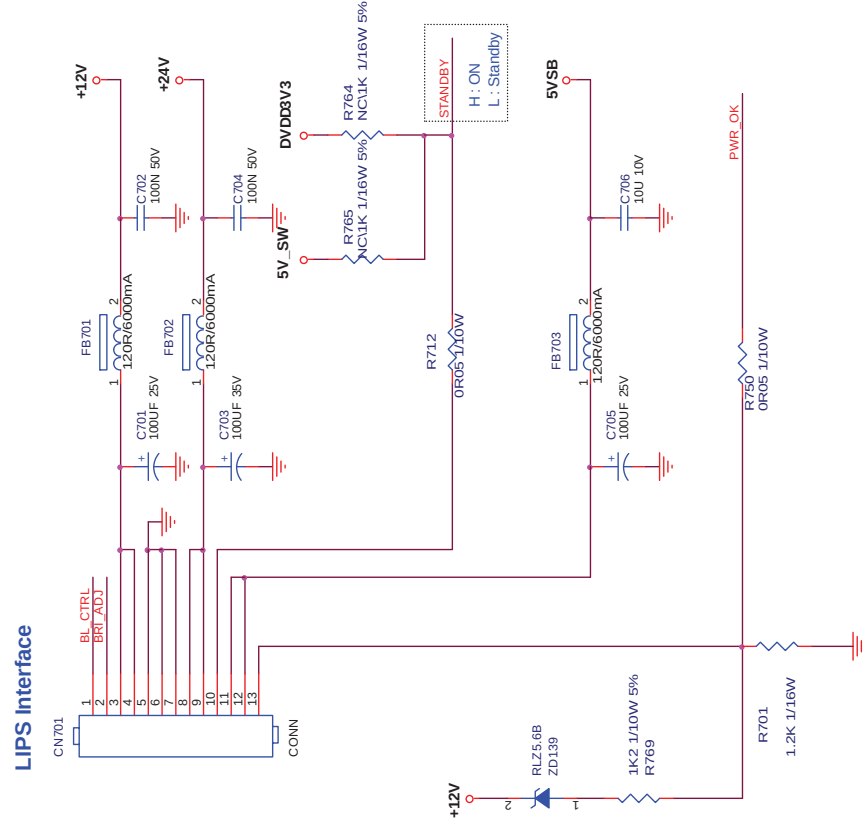
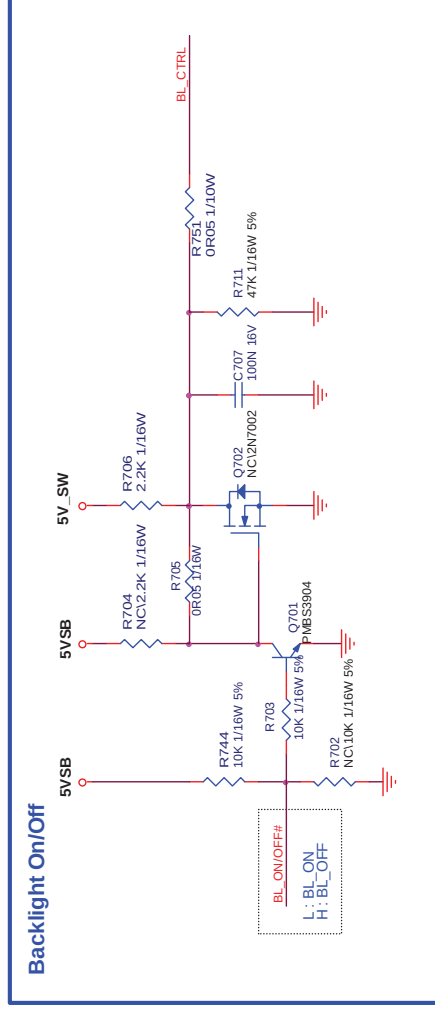
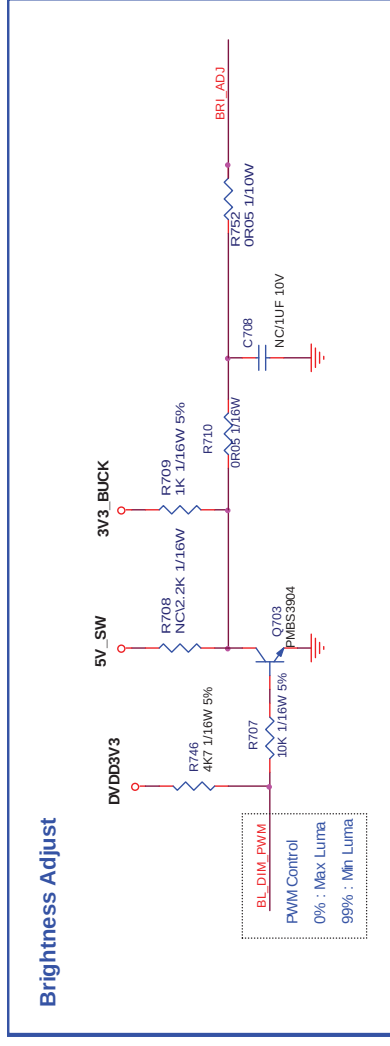


电源框图



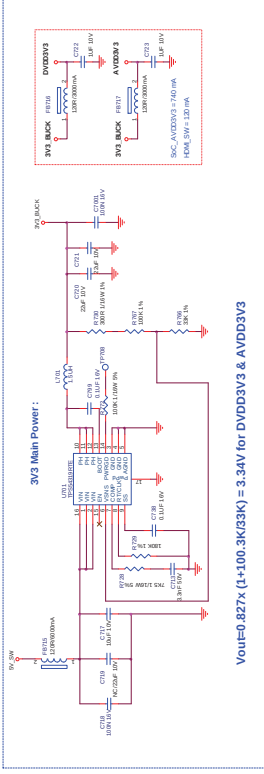
6. 线路图

6.1 主板 715G4611M1A000004K

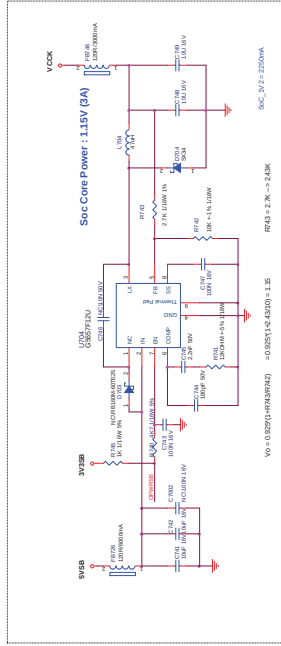
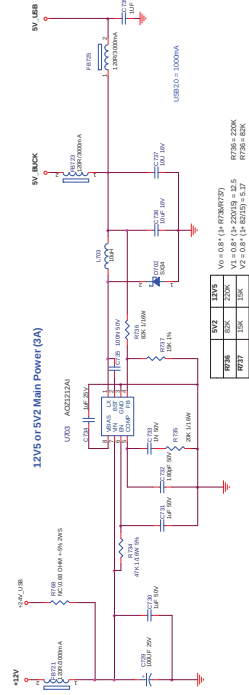
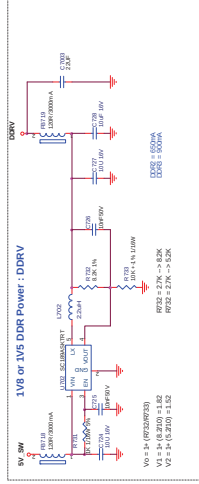


TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	Size	B
結構爪銅膜	TPV MODEL	Rev	MOC
Key Component	PCB NAME		HW4
04 PSU & Inverter I/F			
Date	Wednesday March 02 2011	Sheet	4 of 22

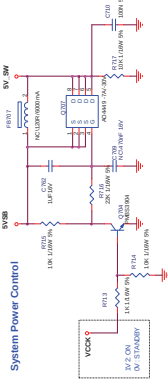
DC-DC Buck & Converter



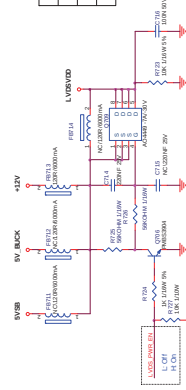
$$V_{out}=0.827 \times (1+100.3K/33K) = 3.34V \text{ for DVDD3V3 \& AVDD3V3}$$



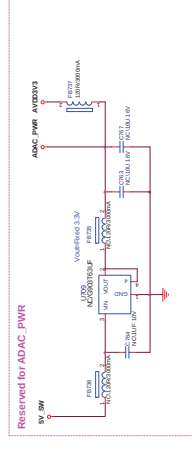
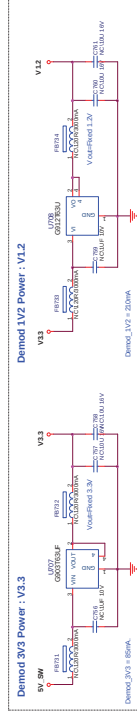
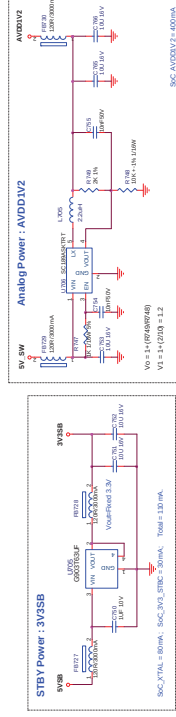
Power MOSFET Switch



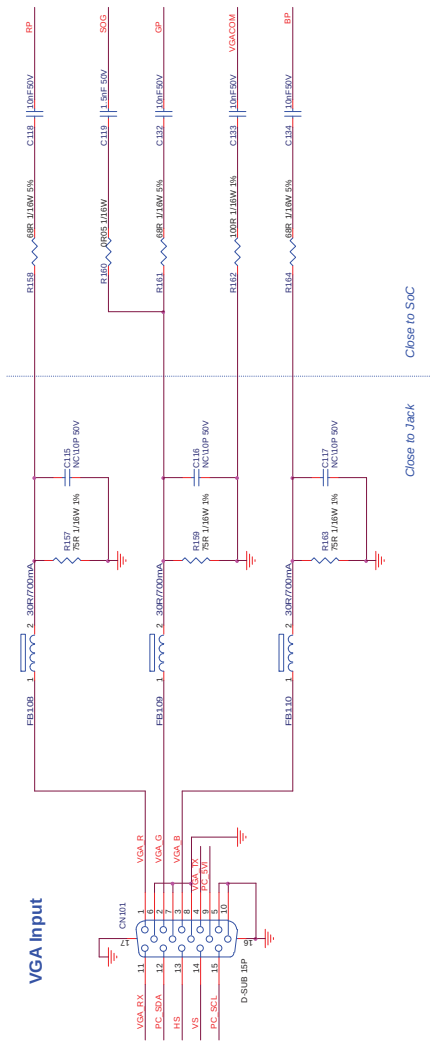
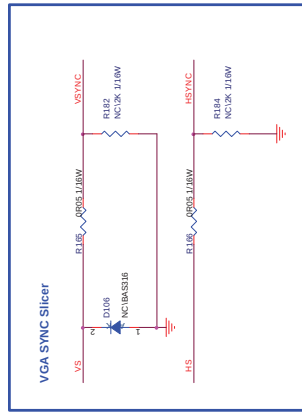
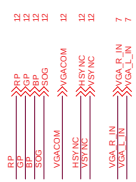
	5V panel	12V panel
FB713	NC	YES
FB712	NC	NC
FB711	YES	NC



LDO Regulator

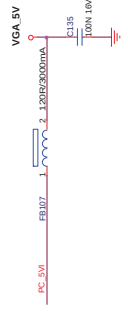


4.57/8.93/0.11/1.12/1.14/1.15/1.16/1.17/1.18/1.20/1.22

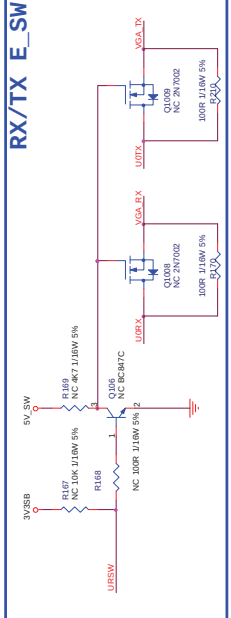


Close to Jack

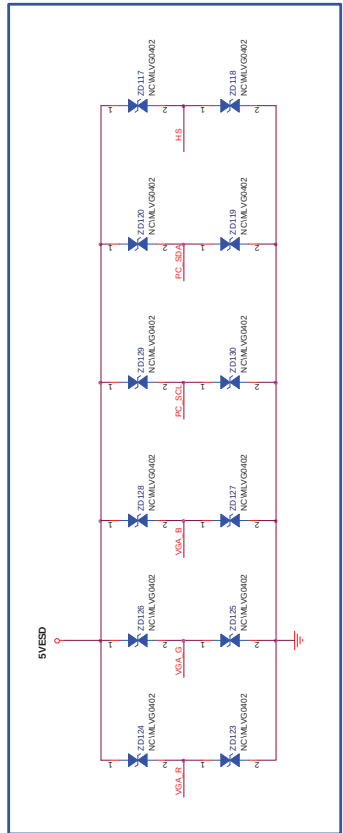
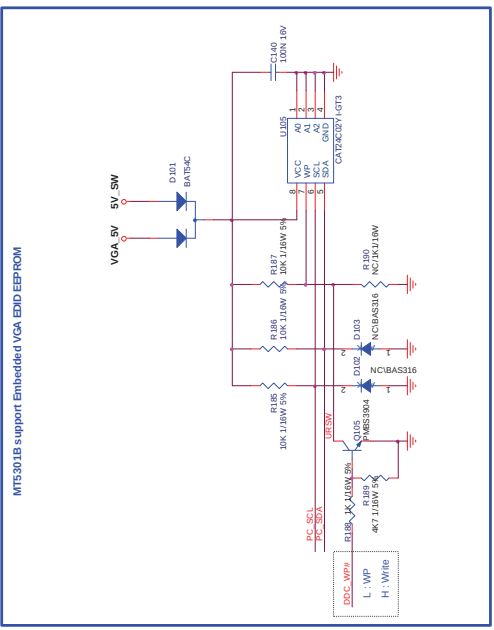
Close to SoC



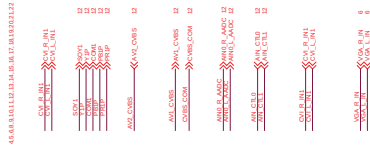
RX/TX E_SW



MTS301B support Embedded VGA BIOD EEPROM



TPV (Top Victory Electronics Co., Ltd.)	Size	Custom
TPV MODEL	Rev	M/C
PCB NAME	Rev	H/W4
File Component	08_VGA & PC Line-in	Rev
Date	Wednesday, March 02, 2011	Sheet
	6 of 22	

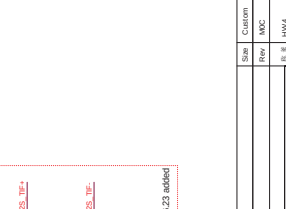
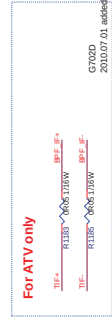
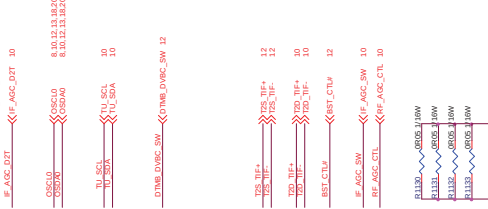
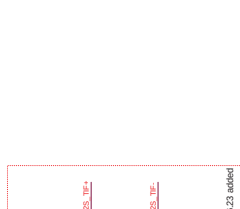
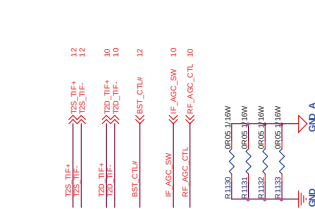
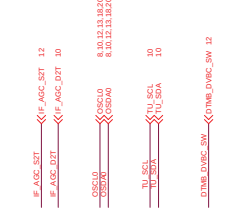


VGA_R_IN
VGA_L_IN



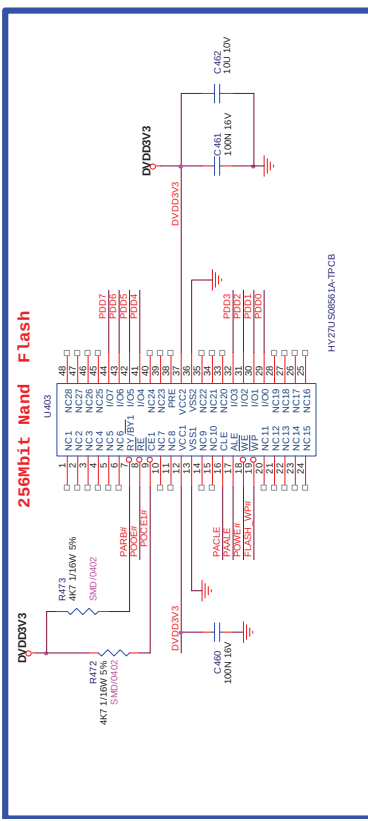
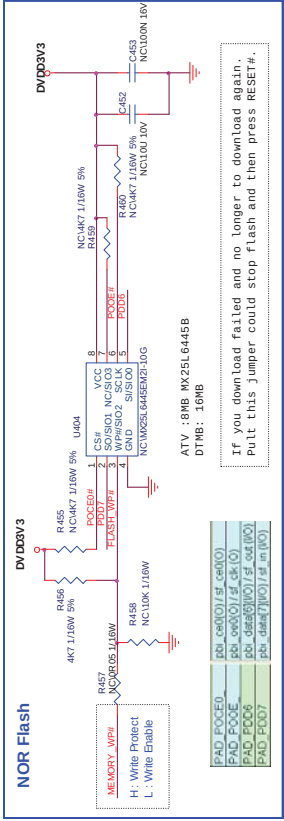
VGA_R_IN
VGA_L_IN

VGA_R_IN
VGA_L_IN

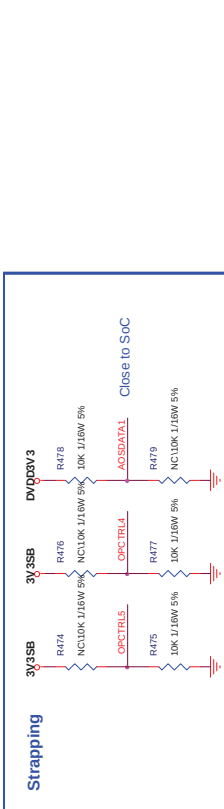
295. The.

23 added

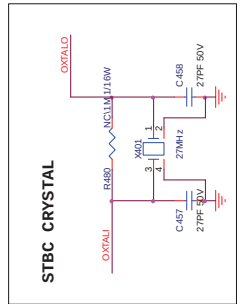
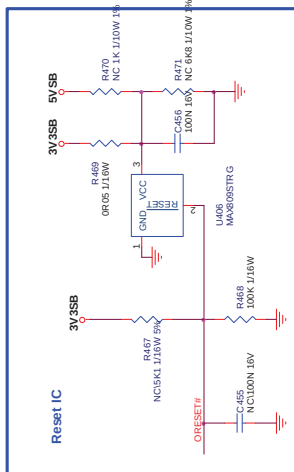
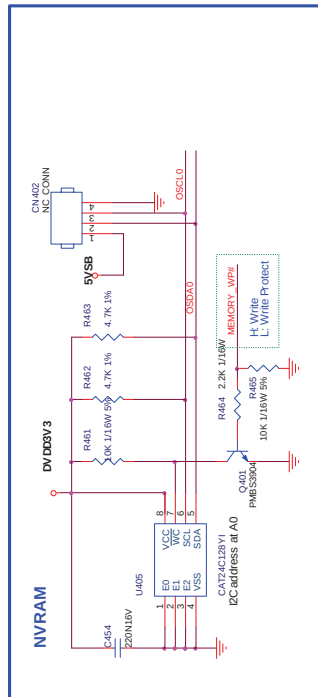
	Size	Custom
	Rev	MDC
22	料 差	HW4



Name	Part Number	Size	Type	Package
Samsung	K9F1G08U0B	1Gbit	SLC	TSOP48
Samsung	K9F5608U0B	256Mbit	SLC	TSOP48
Samsung	K9F12081208U0C-PIBOT	512Mbit	SLC	TSOP48
Samsung	K9F1G08U0C	1Gbit	SLC	TSOP48
ST	NAND128W3A	128Mbit	SLC	TSOP48
ST	NAND256W3A2BN6	256Mbit	SLC	TSOP48
ST	NAND01GW3B2B	1Gbit	SLC	TSOP48
ST	NAND01GW3B2C	1Gbit	SLC	TSOP48
ST	NAND02GW3B2D	2Gbit	SLC	TSOP48
ST	NAND08GW3F2A	8Gbit	SLC	TSOP48
Numonyx	NAND512W3A2DNGF	512Mbit	SLC	TSOP48
HYNIX	HY27U508281	128Mbit	SLC	TSOP48
HYNIX	HY27U508561	256Mbit	SLC	TSOP48
HYNIX	HY27U5081G2A	1Gbit	SLC	TSOP48
HYNIX	HY27U5082G2B	2Gbit	SLC	TSOP48
HYNIX	H27U518S2CTP-BI	512Mbit	SLC	TSOP48
HYNIX	H27UAG8T2A	16Gbit	MLC	TSOP48
HYNIX	H27UAG8T2M	16Gbit	MLC	TSOP48
Micron	MT29F1G08AAC	1Gbit	SLC	TSOP48
Micron	MT29F2G08AAD	2Gbit	SLC	TSOP48

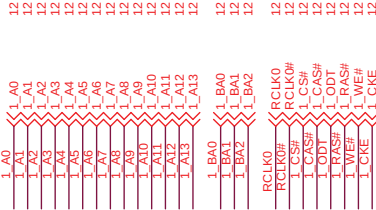
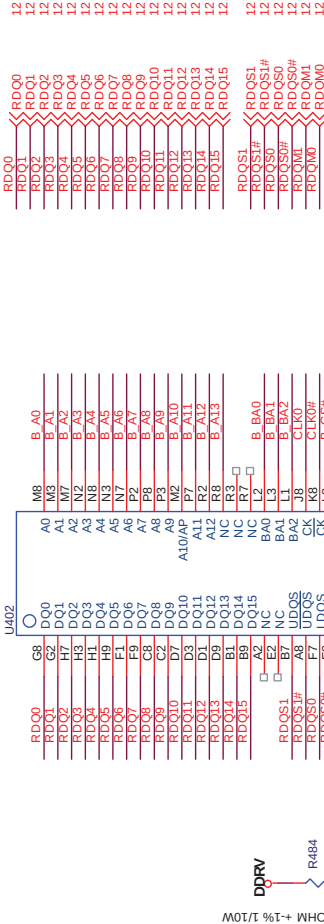
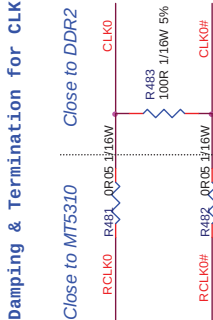
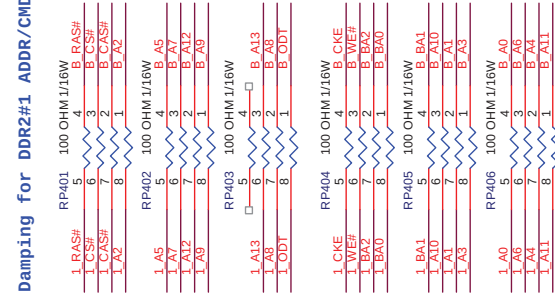
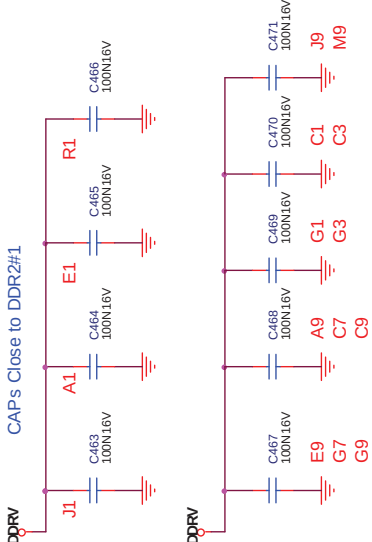


Strapping	OPCTRL5	OPCTRL4	OPCTRL3	OPCTRL2	OPCTRL1	OPCTRL0	AOSDATA1	AOSDATA0
ICE mode + 27M + serial boot	0	0	0	0	0	0	0	0
ICE mode + ROM boot + 27M	0	0	0	0	0	0	0	0
ICE mode + 54M + serial boot	0	0	0	0	0	0	0	0
ICE mode + 54M + ROM boot + 54M	0	0	0	0	0	0	0	0
QUT mode	1	0	0	0	0	0	0	0
scan mode	1	0	0	0	0	0	0	0
cpu mode	1	0	0	0	0	0	0	0
Boundary scan mode	1	0	0	0	0	0	0	0



TPV (Top Victory Electronics Co., Ltd.)	DEM MODEL	Size	C
越南胜利电子	TPV MODEL	Rev	MOC
Key comment: 1.3. Soc Peripheral	PCB NAME		
D880	Sheet	13 of 22	HW4

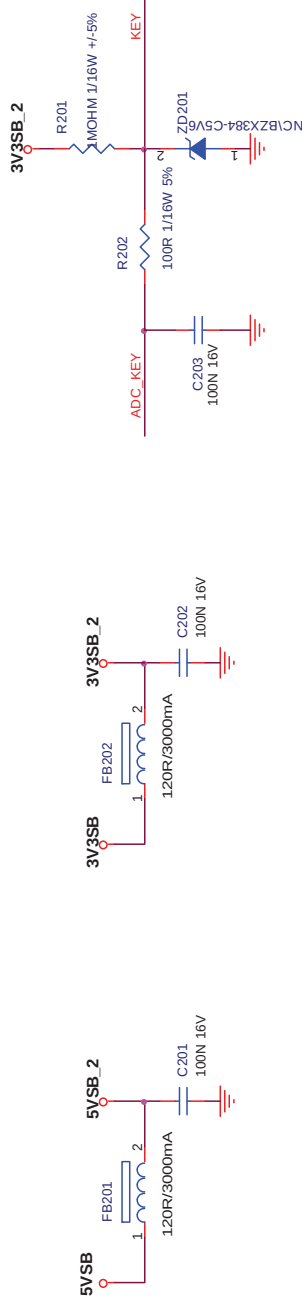
DDR2#1-64Mx16-1066MHZ



Name	Part Number	Size	Type
Samsung	K4T1G164QF-BCF8	64Mx16	1066
Nanya	NT5TU64M16GG-BE	64Mx16	1066
Nanya	NT5TU64M16GG-BD	64Mx16	1066
Hynix	H5PS1G63EFR-G7C	64Mx16	1066
Nanya	NT5TU64M16GG-AC	64Mx16	800
Winbond	W971GG6JB-25	64Mx16	800
Hynix	H5PS1G63EFR-S6C	64Mx16	800
Hynix	H5PS5162FFR-S6C	32Mx16	800
Samsung	K4T51163BFR-H9C	32Mx16	800



TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	Size	B
Key Component	TPV MODEL	Rev	MOC
Date	PCB NAME	Sheet	HW4
Wednesday, March 02, 2011	14. DDR2	14 of 22	



[illegible]

Serial Flash 4 Mbit

Trapping : Default Low when no configuration external

Pad_jtdio ^v	Description ^v
0 ^v	pad_jtdio tie l => flash boot ^v
1 ^v	pad_jtdio tie l => flash boot ^v

I2C CONNECTION		
M02_SDA	R410	Q05A2
M02_SCL	R411	Q05A0

AV33 XTAL POWER

The schematic diagram for the AV12 PLL Power section shows a power supply line connected to a 120R/300mA fuse (FBR102). The line then splits into two parallel branches. The first branch contains a 330pF capacitor (C4105) in series with a 100nF capacitor (C4104). The second branch contains a 4.7uF capacitor (C4103) in series with a 100nF capacitor (C4104). Both branches rejoin the main power line, which is then connected to the AV12_PLL pin.

Crystal

The diagram shows the timing of the **MT2822 RESET#** signal. The **OSRD** signal (blue) transitions from high to low, then back to high. The **DMA_ACK** signal (red) transitions from high to low during the first low pulse of **OSRD**. The **DMA_ACK_GFN** signal (green) transitions from high to low during the second low pulse of **OSRD**. The **RESET#** signal (purple) is shown as a single high pulse that occurs during the first low pulse of **OSRD**. The **TP21** label is at the end of the **DMA_ACK_GFN** signal line.

MT2822 POWER

NC/VREF3A/GND

DV33 IO POWER

5V

NR1_DV33

C4111 4.7µF 10V

C4112 100nF

C4113 100nF

C4114 3.3µF 50V

C4115 3.3µF 50V

TPV (Top Victory Electronics Co., Ltd.)	Size	Custom
振興系振興		
TPV MODEL	Rev	MPC
20 MEMC PERIPHERAL		
Key Component		
PCB NAME		料表
Wednesday, March 02, 2011		
Sheet	20 of 22	
Date		



I2C to MT5363

GPIO control to Panel

22	M02_GPIO00	M02_GPIO00
22	M02_GPIO01	M02_GPIO01
22	M02_GPIO02	M02_GPIO02

RESET from MT5363

MJC_RST << MJC_RST 12

[illegible]



Damping and Termination for CLK

Close to MT8281 Close to DRAM

Close to MT8281 Close to DRAM

Close to MT8281 Close to DRAM

Close to MT8281 Close to DRAM

Close to MT8281 Close to DRAM

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Close to MT8281 Close to DRAM

Close to MT8281 Close to DRAM

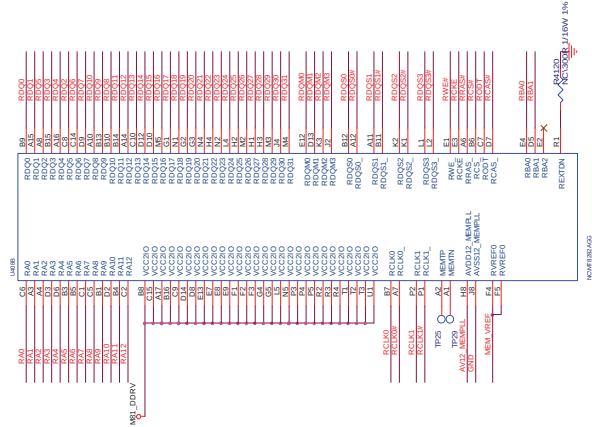
Close to MT8281 Close to DRAM

Close to MT8281 Close to DRAM

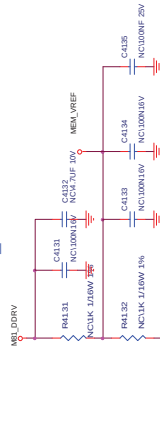
Close to MT8281 Close to DRAM

Close to MT8281 Close to DRAM

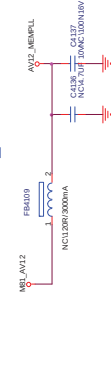
Close to MT8281 Close to DRAM



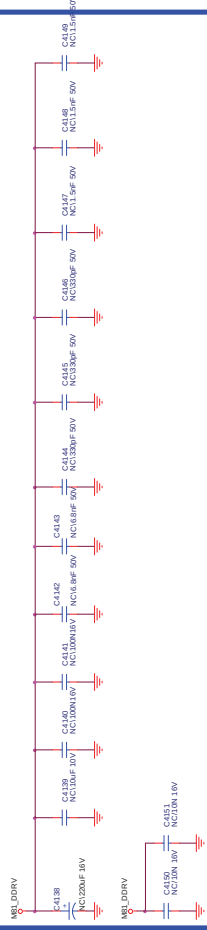
MEM_VREF for DRAM



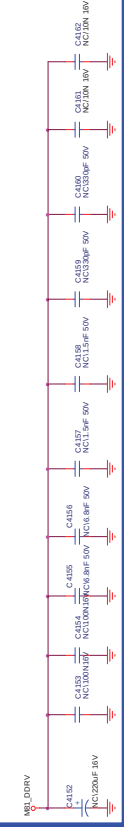
CAPS for AV12_MEMPLL



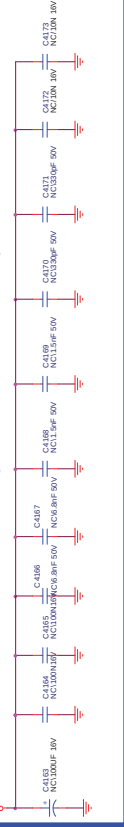
CAPS for DRAM IO Power (Beneath the MT2822)



CAPS for DRAM IO Power (Beneath the DDR#1)

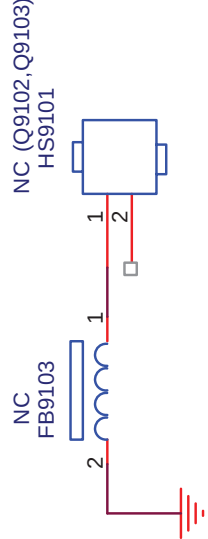
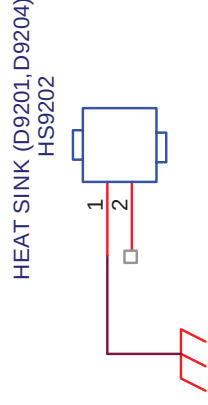
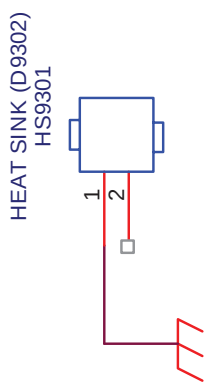
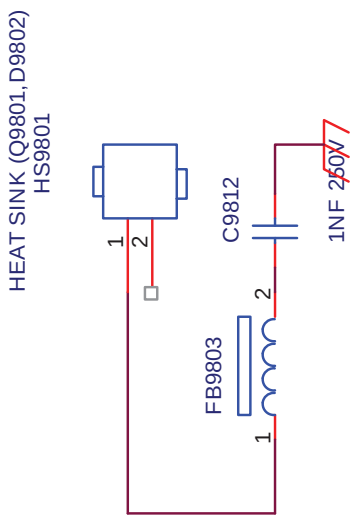
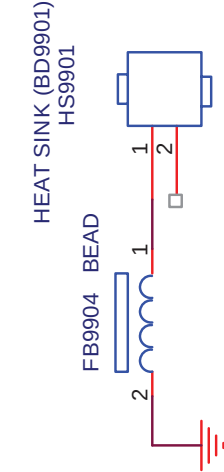
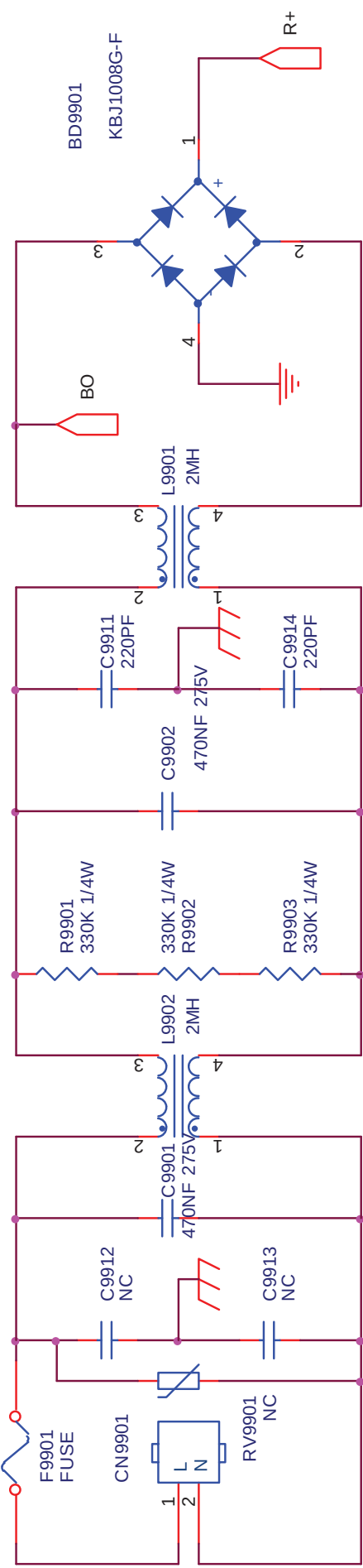


CAPS for DRAM IO Power (Beneath the DDR#2)

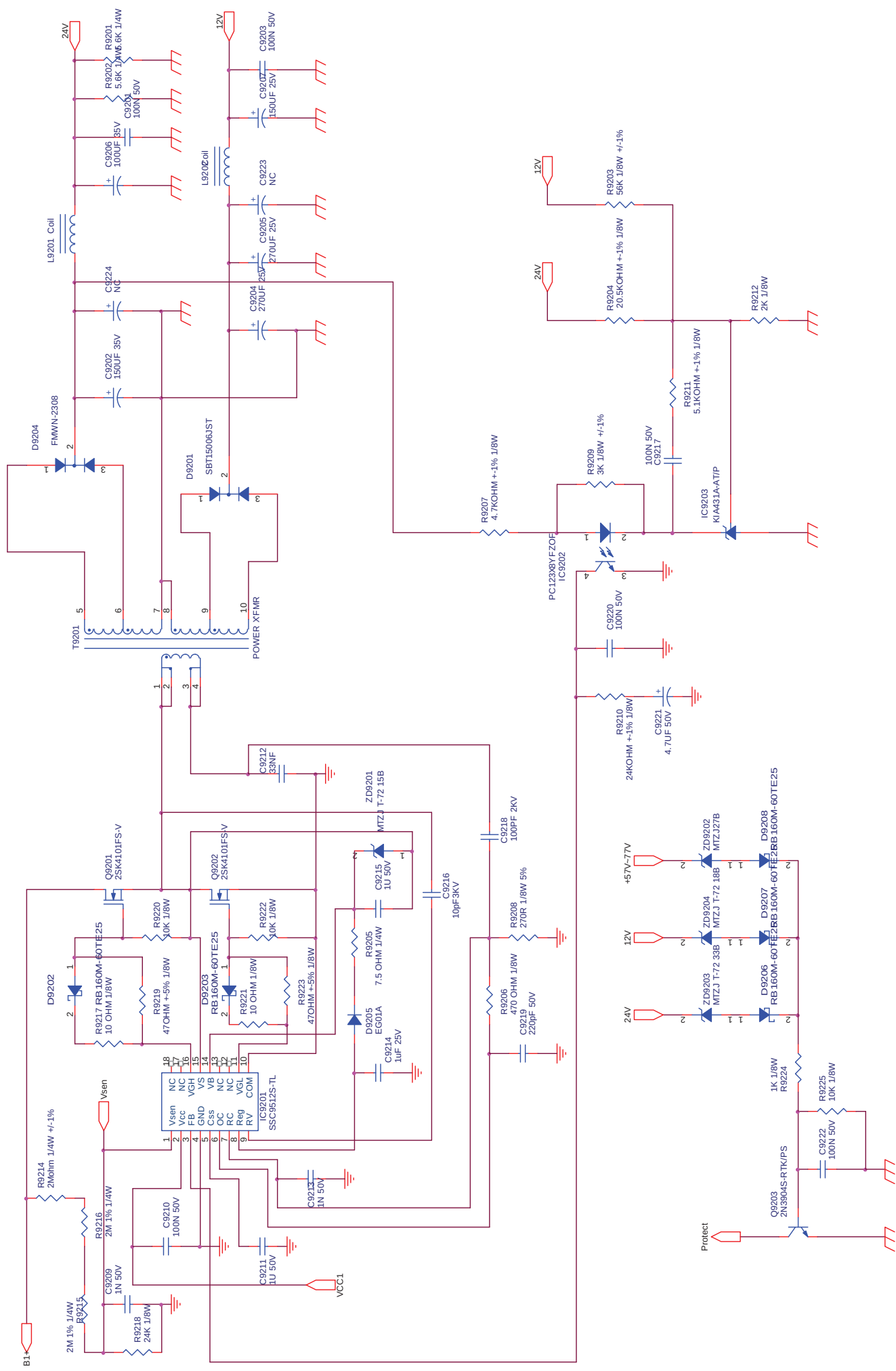


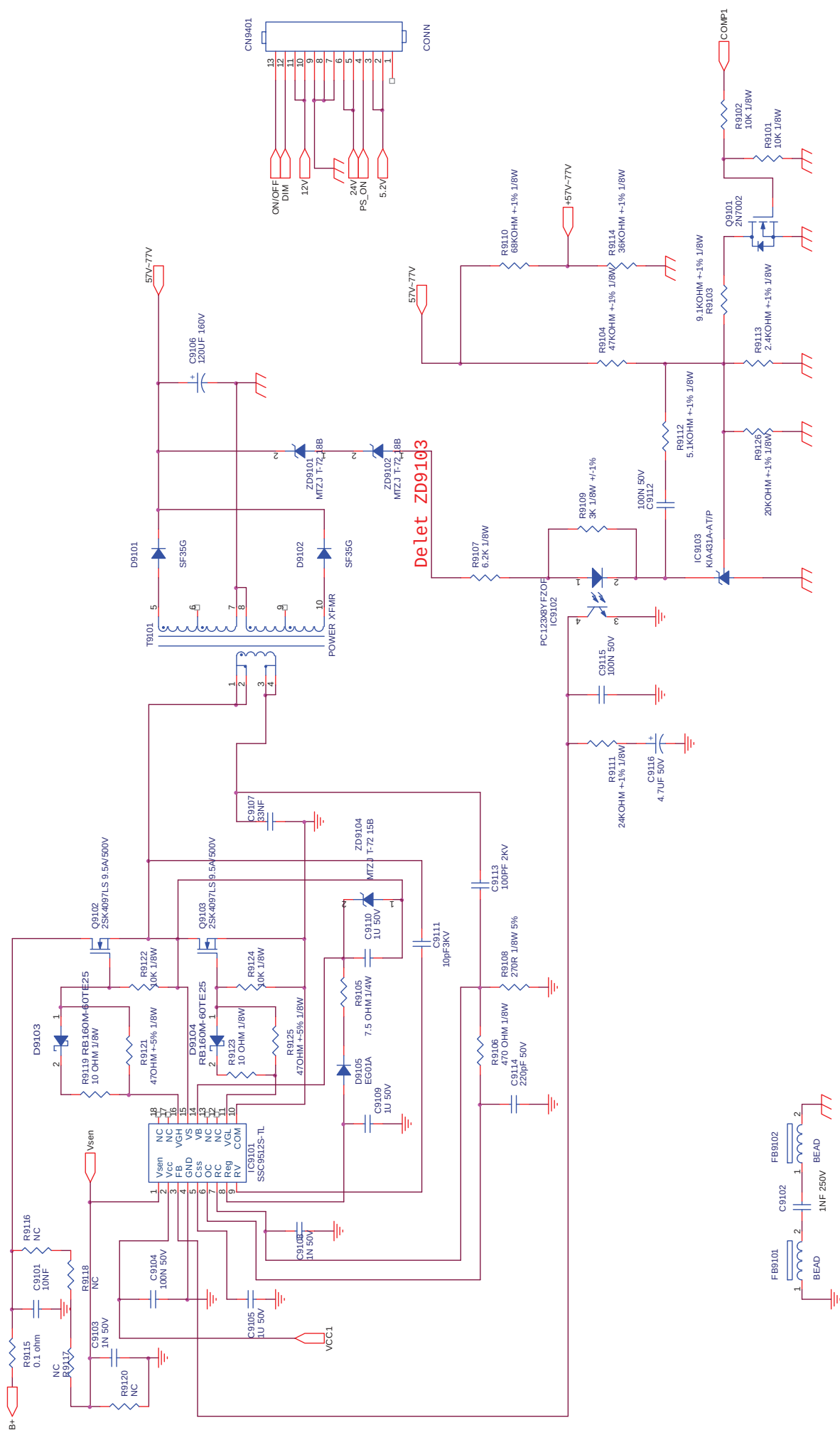
TPV (Top Victory Electronics Co., Ltd.)	OSM MODEL	Size	Custom
TPV MODEL	Rev	Rev	WOC
Environment 2A, MEMC, DDRH	PCB NAME	IS 8	HW4.0n
DATE: 2011.02.21	DATE: 2011.02.21	DATE: 2011.02.21	DATE: 2011.02.21

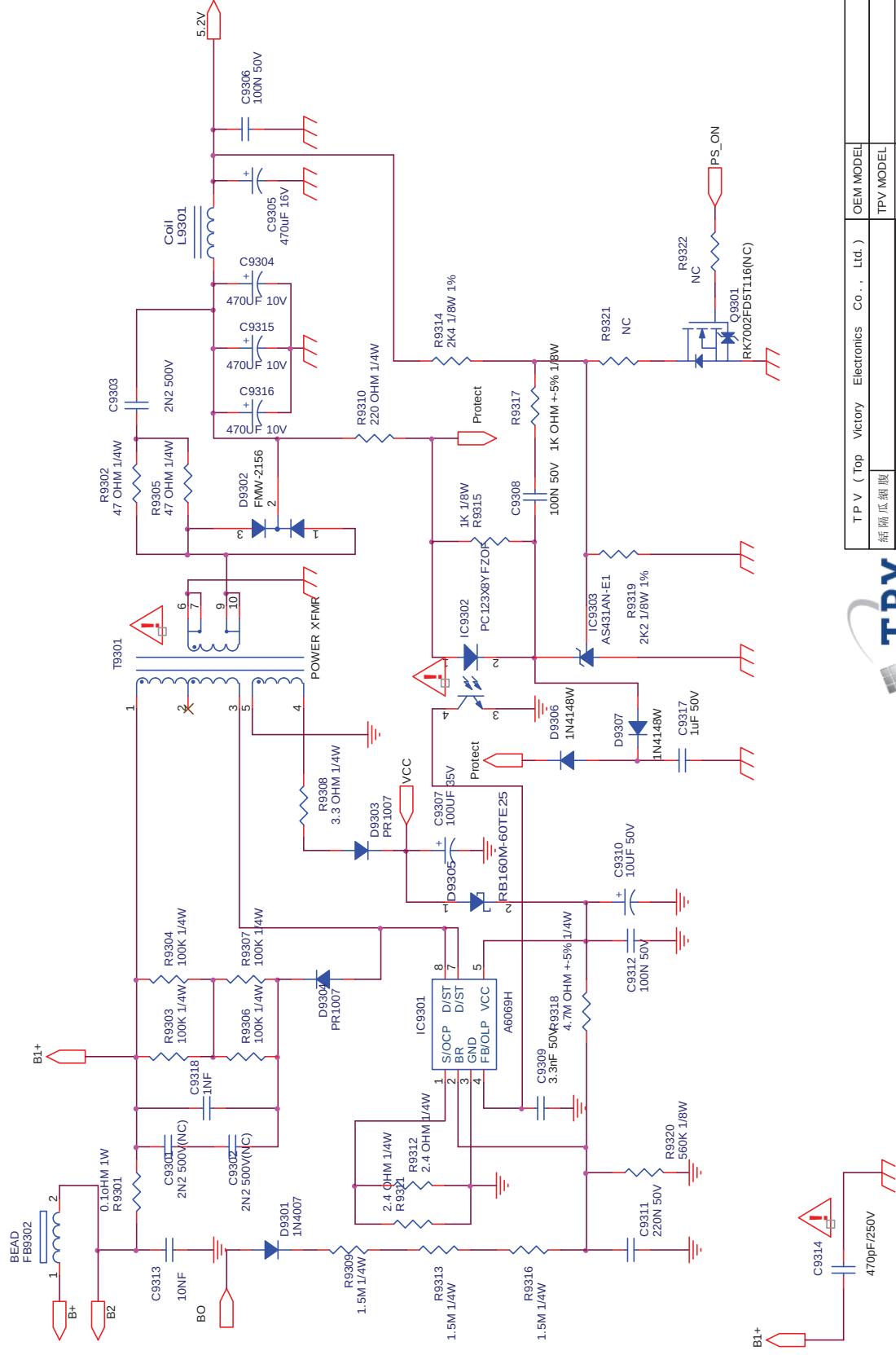
6.2 电源板
32PFL3390/T3 715G4581P01W31003S



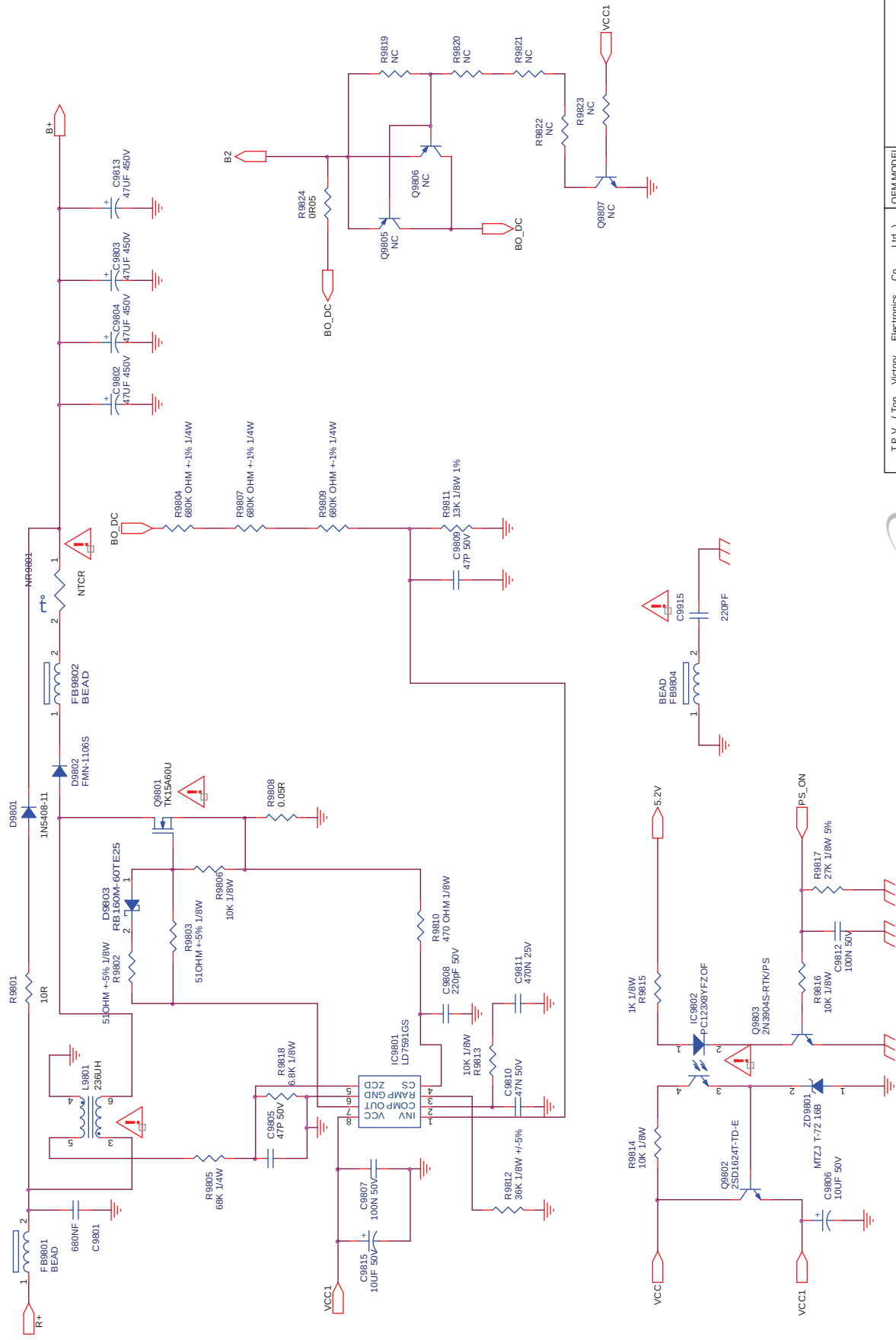




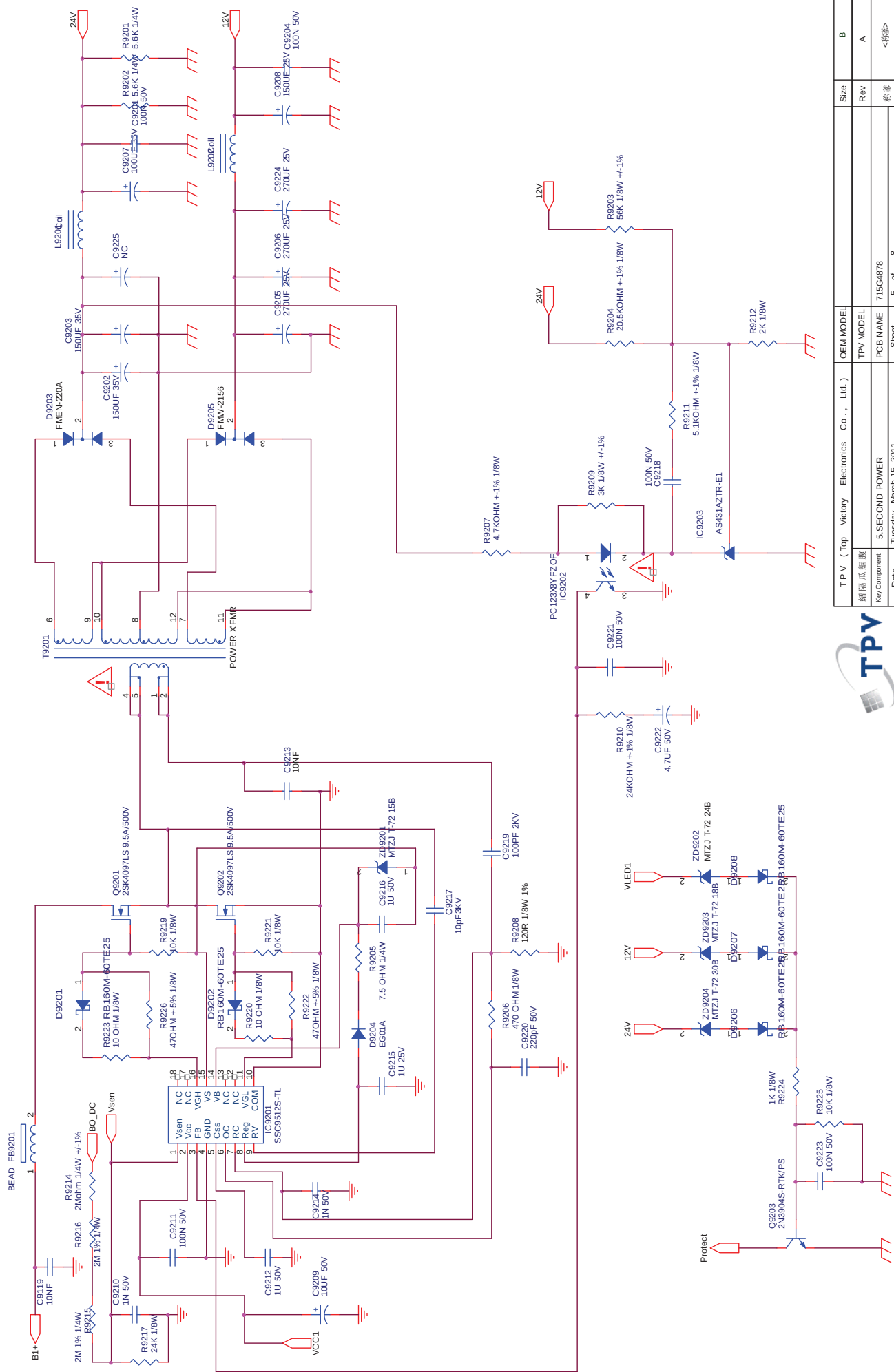




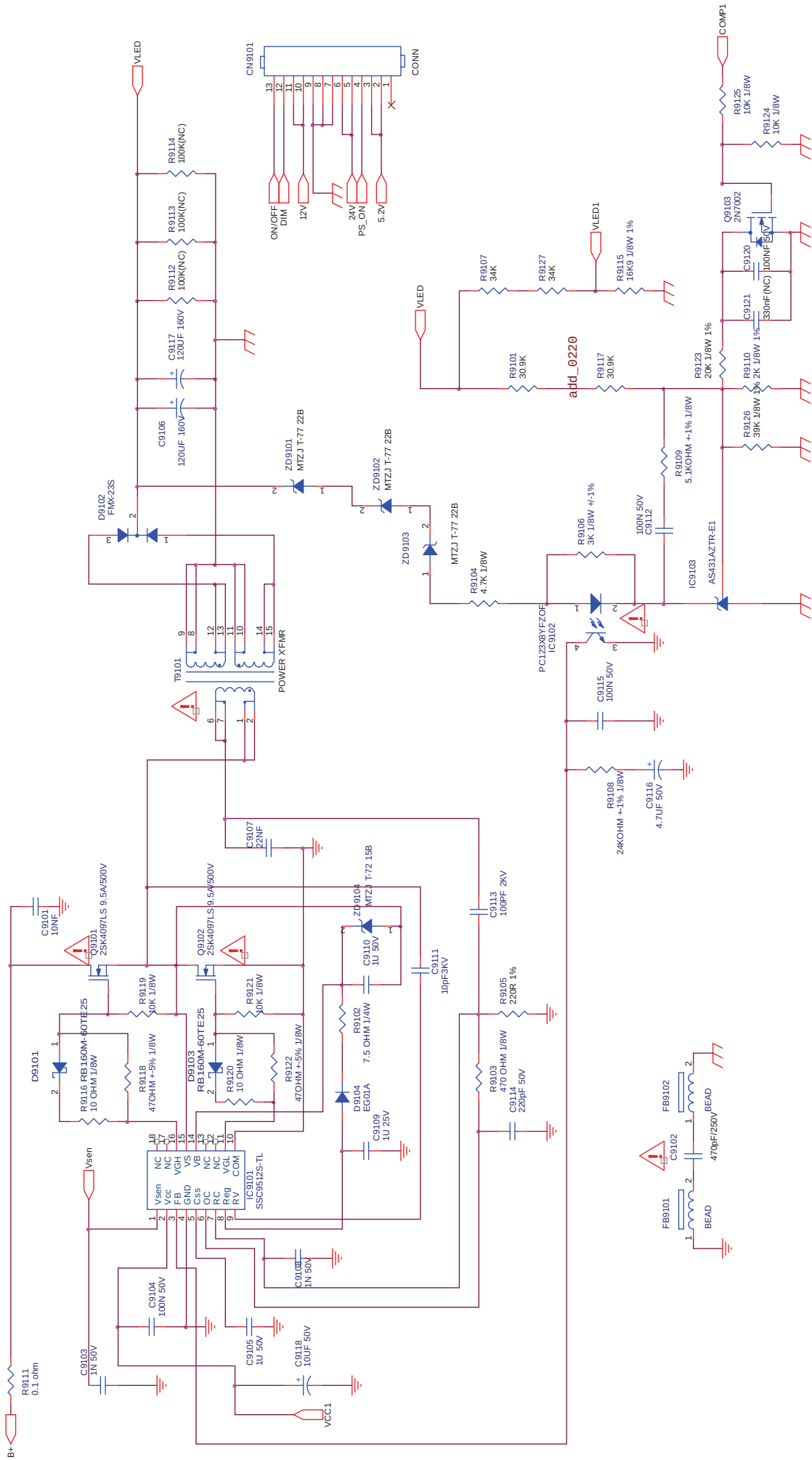
TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	Size	B
紙隔瓜線腹	TPV MODEL	Rev	A
Key Component	PCB NAME	715G4878	<称参>
Date	Tuesday, March 15, 2011	Sheet	3 of 8



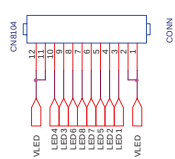
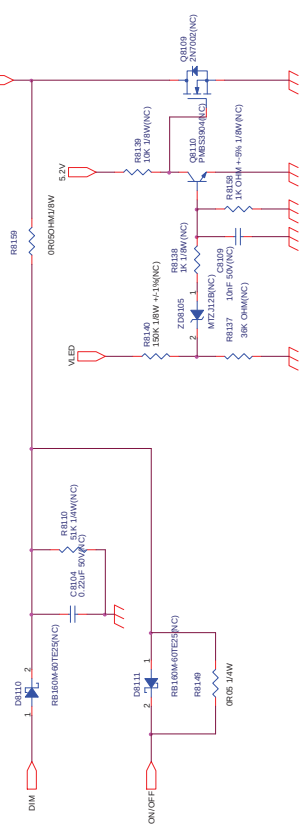
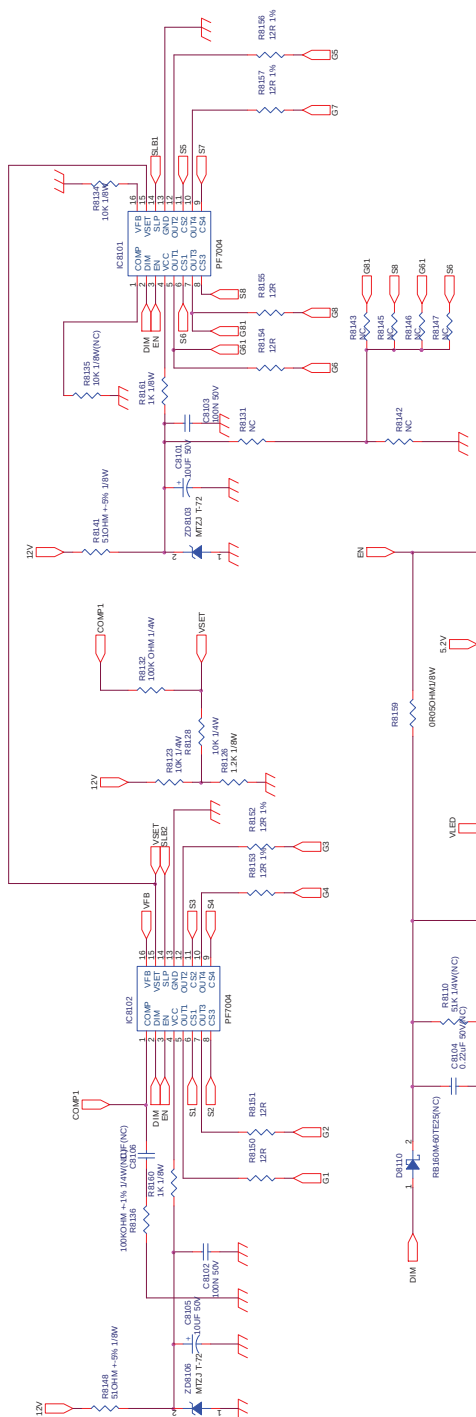
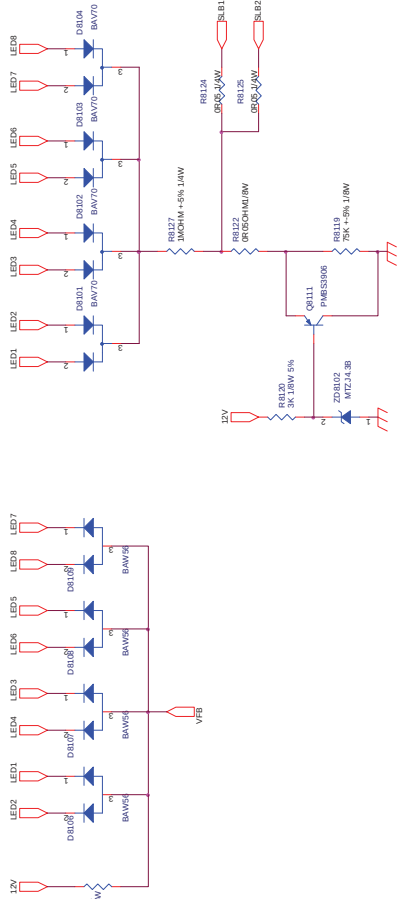
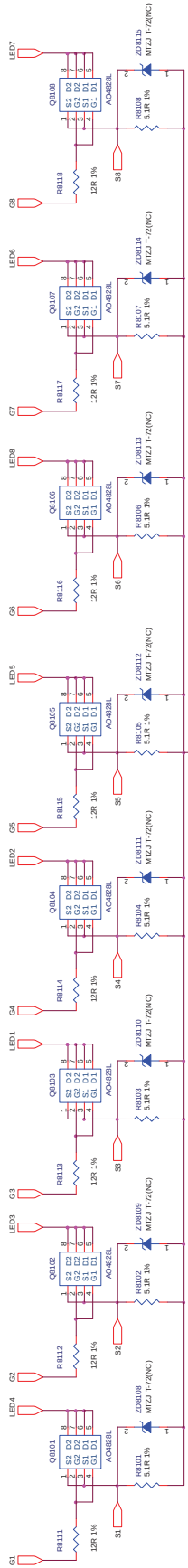
TPV (Top Victory Electronics Co., Ltd.)		OEM MODEL	Size	B
照南瓜銅版		TPV MODEL	Rev	A
Key Component		PCB NAME	715G4878	<修參>
Date	Tuesday, March 15, 2011	Sheet	4	8



TPV (Top Victory Electronics Co., Ltd.)		OEM MODEL	Size	B
Key Component		TPV MODEL	Rev	A
Date		PCB NAME	称参	
Tuesday, March 15, 2011		715G4878	<修参>	
		Sheet	5 of	8

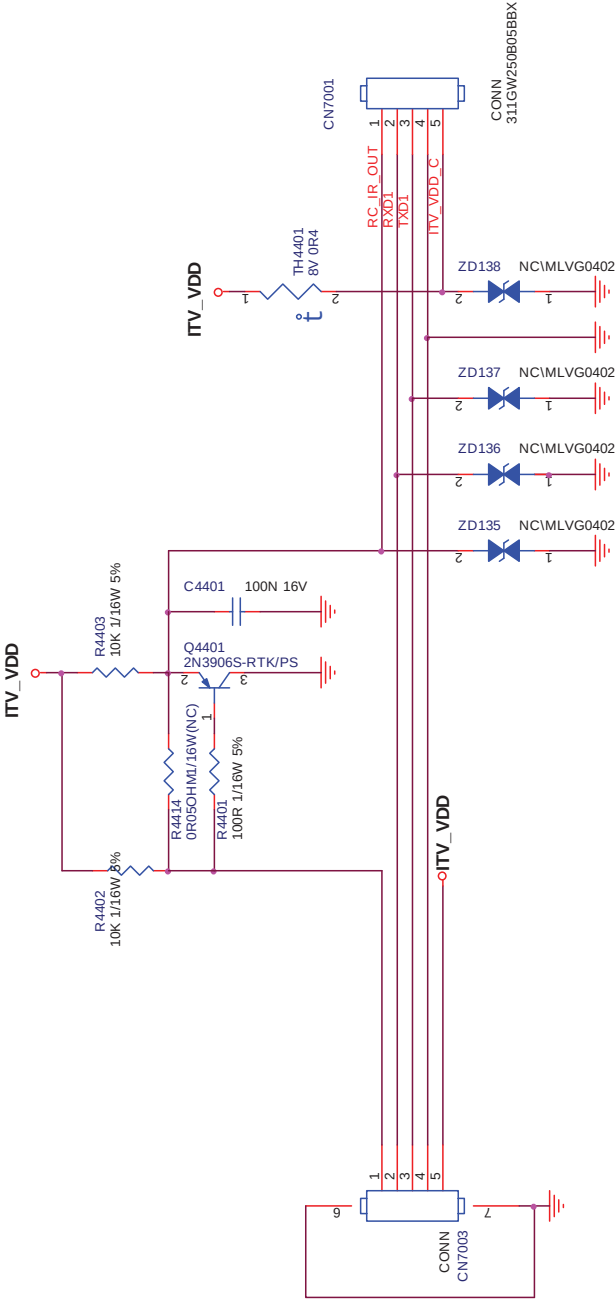


TPV (Top Victory Electronics Co., Ltd.)		OEM MODEL	Size	B
威陽瓜錫園		TPV MODEL	Rev	A
Key Component		PCB NAME	715G4878	
Date		Sheet	6	8
Tuesday, March 15, 2011		称参	<称参>	



TPV (Top Victory Electronics Co., Ltd.)	QEM MODEL	Size	C
胜利达微	TPV MODEL	Rev	A
7-LED DRIVER	PCB NAME	715G4F9	
Tuesday, March 16, 2011	Sheet	7 of 8	<前页>

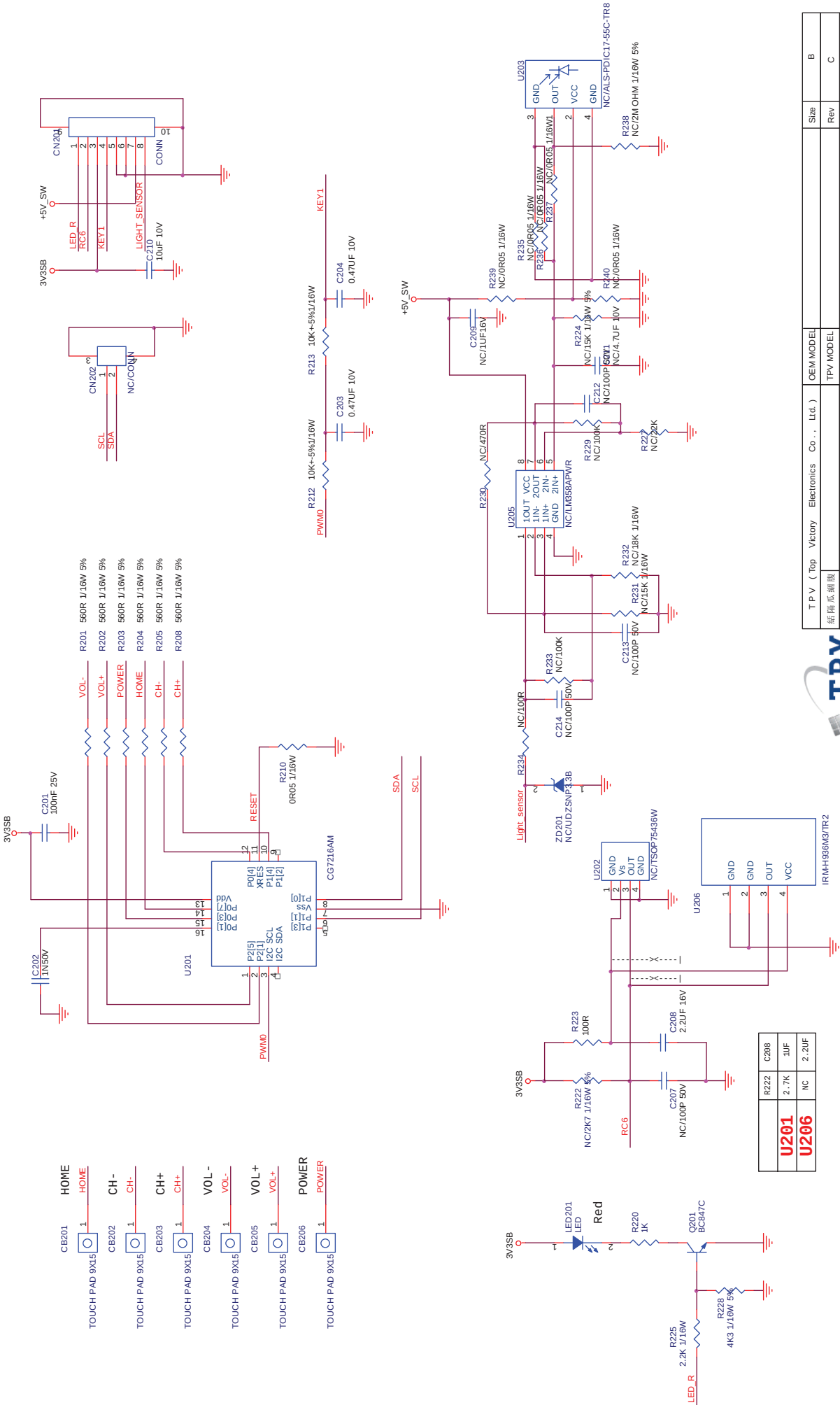
6.3 倍频板
715G4858T01000004S



TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	Size	Custom
结网瓜 網膜	TPV MODEL	Rev	A
Key Component	PCB NAME	1 of 1	HW4
Date	Monday, December 20, 2010	Sheet	

6.4 按键板

715G4496K02000004M



TPV (Top Victory Electronics Co., Ltd.)		OEM MODEL	Size	B
結構爪編號		TPV MODEL	Rev	C
Key Component		PCB NAME	715G4496	<修參>
Date		Sheet		
Tuesday, June 28, 2011		1 of 1		