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Telephone Number: +44(0) 1493 602602  
Website: [www.midascomponents.co.uk](http://www.midascomponents.co.uk)  
Email: [sales@midascomponents.co.uk](mailto:sales@midascomponents.co.uk)

## **SPECIFICATION FOR LCD MODULE**

**MODULE NO.:AFS320240TG-3.5-E300001**  
**DOC.REVISION 00**

**Customer Approval:**

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	SIGNATURE	DATE
PREPARED BY (RD ENGINEER)	FL	Aug-28-2010
PREPARED BY (QA ENGINEER)	CL	Aug-28-2010
CHECKED BY	Sean	Aug-28-2010
APPROVED BY	Rio	Aug-28-2010

## DOCUMENT REVISION HISTORY

Version	DATE	DESCRIPTION	CHANGED BY
00	Aug-28-2010	First Issue	FL

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## 1. Features & Mechanical Specifications

Item	Contents	Unit
	LCD	
LCD Type	TFT / Transmissive / Normal White	--
Viewing direction	6 O'clock	--
Backlight	White LED BackLight	--
Interface	RGB interface	--
Driver IC	NT39016D	--
Outline Dimension	77.1(W) × 64.1(H) × 4.35(T)	mm
Glass area (W×H×T)	74.1 × 56.8 / 61.9 × 0.5	mm
Active area (W×H)	70.08 × 52.56	mm
Number of Dots	320(RGB) × 240	--
Dot pitch (W×H)	0.073 × 0.219	mm
Pixel pitch (W×H)	0.219 × 0.219	mm
Operating Temperature	-20 ~ +70	☒
Storage temperature	-30 ~ +80	☒

## 2. Dimensional Outline

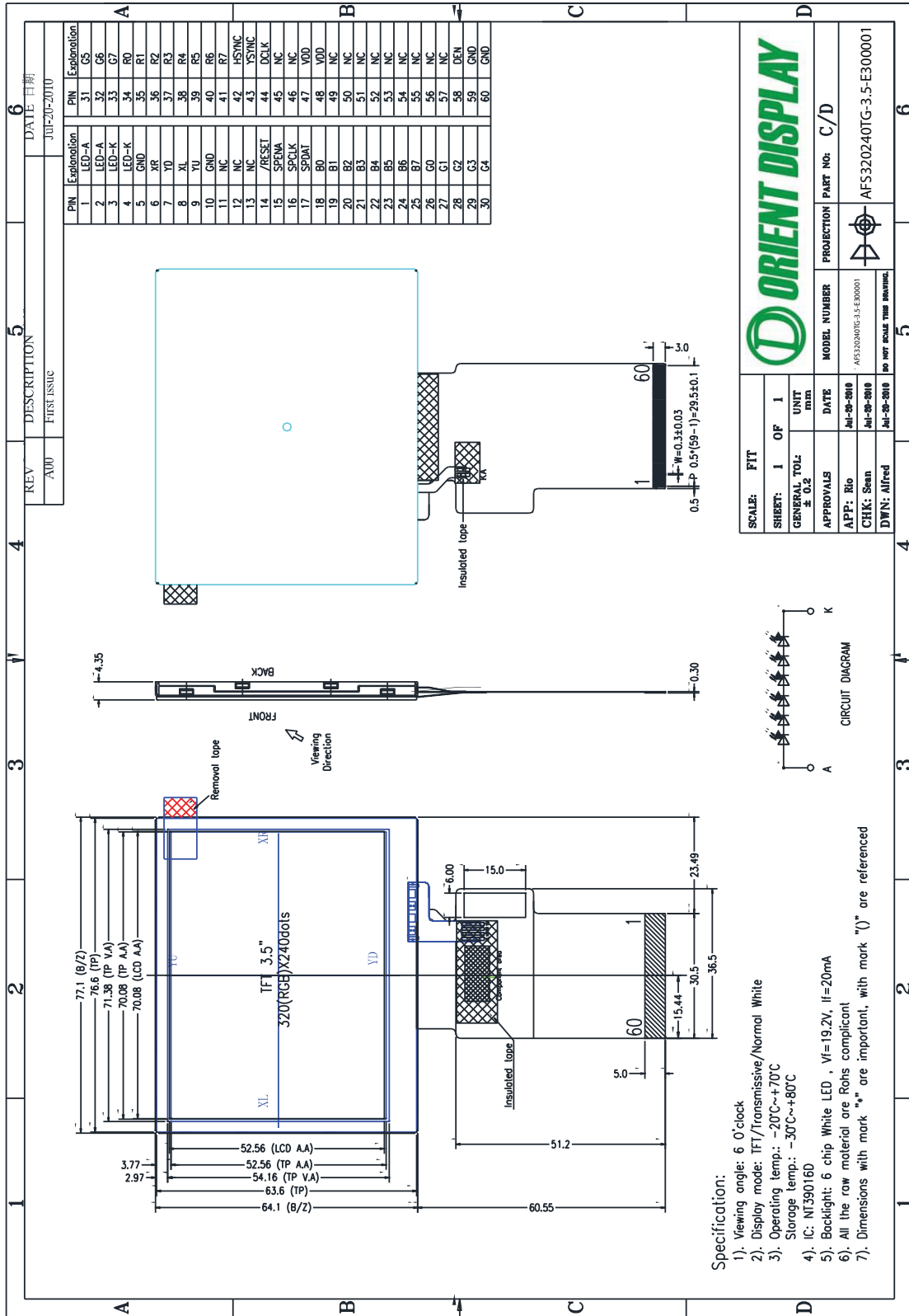


Figure 1. Dimensional outline

### 3. Block Diagram

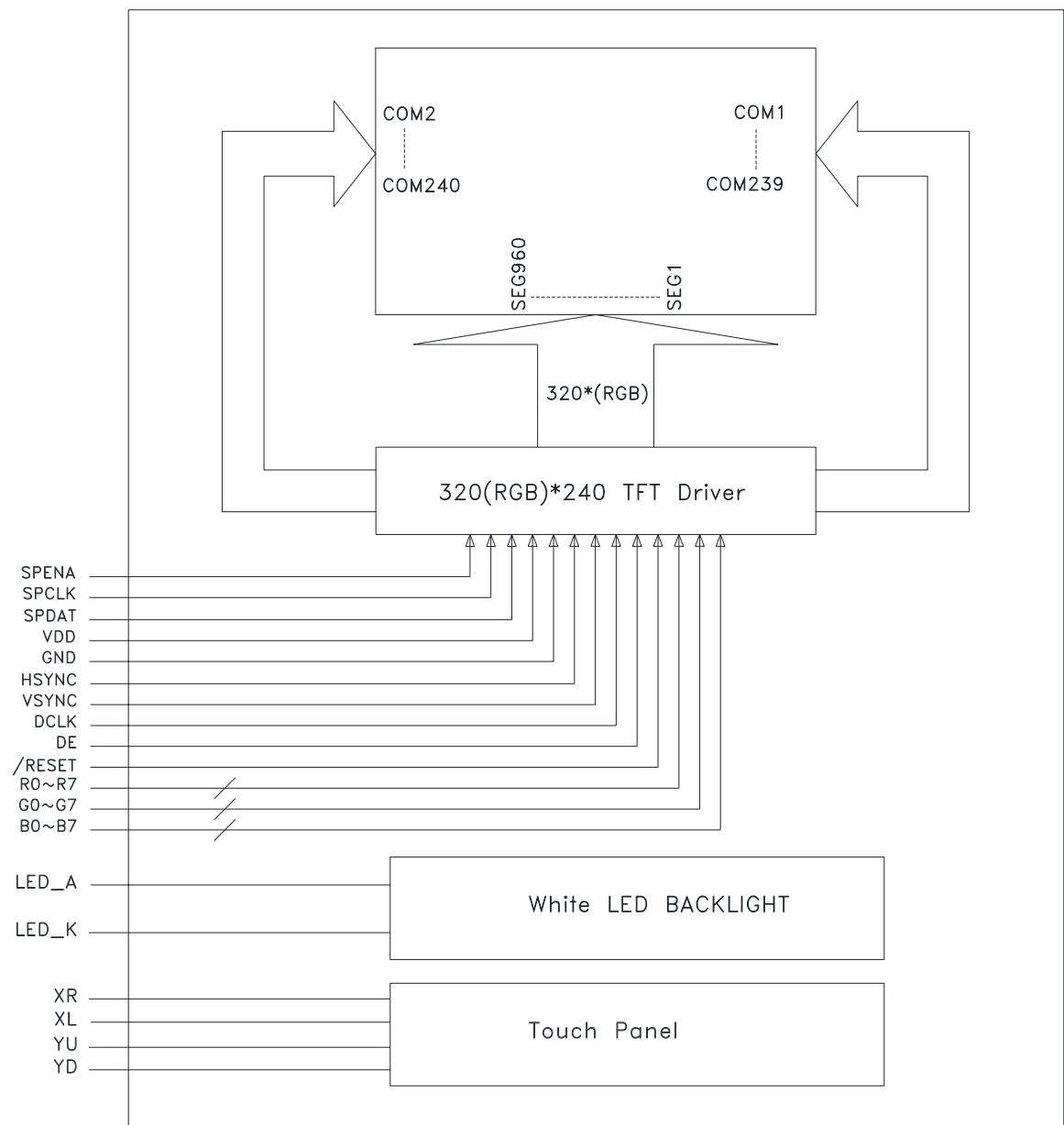


Figure 2. Block diagram

## 4. Pin Description

PIN No.	SYMBOL	Function
1,2	LED_A	Backlight LED Anode
3,4	LED_K	Backlight LED Cathode
5	GND	Ground
6	XR	Touch Panel XR Position
7	YD	Touch Panel YD Position
8	XL	Touch Panel XL Position
9	YU	Touch Panel YU Position
10	GND	Ground
11,12,13	NC	No Connection
14	/RESET	Reset pin. (Active Low)
15	SPENA	3-Wire Communication Enable. Active Low
16	SPCLK	3-Wire Communication Clock input.
17	SPDAT	3-Wire Communication Data input/output
18~25	B0~B7	B data bus
26~33	G0~G7	G data bus
34~41	R0~R7	R data bus
42	HSYNC	Horizontal Sync input
43	VSYNC	Vertical Sync input
44	DCLK	Clock for Input Data
45,46	NC	No Connection
47,48	VCC	Power supply
49~57	NC	No Connection
58	DEN	Data Input Enable.
59,60	GND	Ground

## **5. Absolute Maximum Ratings**

Item	Symbol	Rating	Unit
Supply Voltage range	VDD	-0.5 to +5.0	V
Operating Temperature range	TOP	-20 to +70	°C
Storage Temperature range	TST	-30 to +80	°C

## **6. Electrical Characteristics**

### **DC Characteristics**

Item	Symbol	Min.	Type.	Max.	Unit
Logic Supply Voltage	VDD	3.0	-	3.6	V

## **7. Backlight Characteristics**

White LED × 6

(Ta = 25°C)

Item	Symbol	Condition	Min	Typ	Max	Unit
Forward Voltage	VF	IF = 20mA	-	19.2	21.6	V
Uniformity	△Bp	-	80	-	-	%
-	Lv	IF = 20mA	3500	4000	-	cd/m <sup>2</sup>



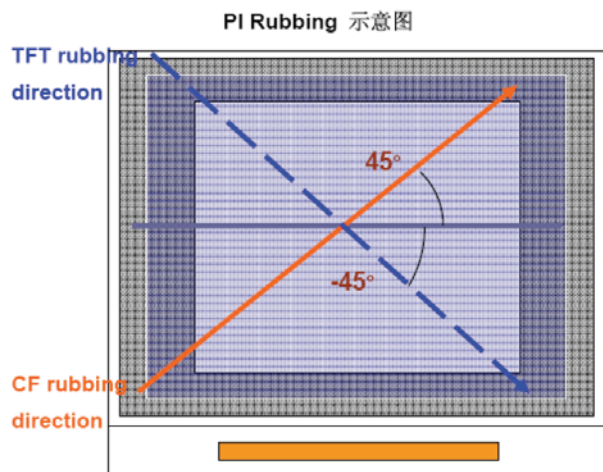
## 8. Electro-Optical Characteristics

Light source: C light

Ta=25°C

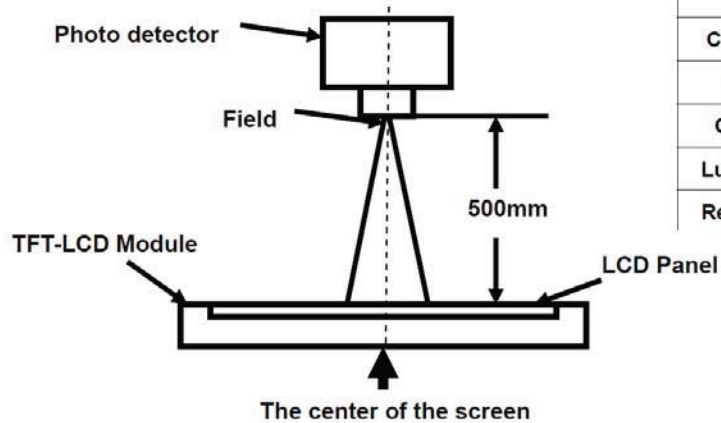
Item		Symbol	Condition	Min	Typ.	Max.	Unit	Remark
View Angles		θT	CR≧10	30	40		Degree	Note 2 With EWV polarizer
		θB		50	60			
		θL		50	60			
		θR		50	60			
Contrast Ratio		CR	θ=0°		350			Note3
Response Time		Ton	25℃		25	40	ms	Note4
		Toff						
Chromaticity	White	X		0.261	0.311	0.361		Measured by C light.
		Y		0.300	0.350	0.400		
	RED	X		0.586	0.636	0.686		
		Y		0.300	0.350	0.400		
	GREEN	X		0.261	0.311	0.361		
		Y		0.501	0.551	0.601		
	BLUE	X		0.084	0.134	0.184		
		Y		0.101	0.151	0.201		
NTSC					50		%	Note 5
Transmittance				6.9	7.5		%	With EWV polarizer, without DBEF

Rubbing Direction



**Note 1:** Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Item	Photo Detector	Field
Contrast Ratio	SR-3	1°
Luminance		
Chromaticity		
Lum Uniformity		
Response Time	BM-7A	2°

**Note 2:** Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).

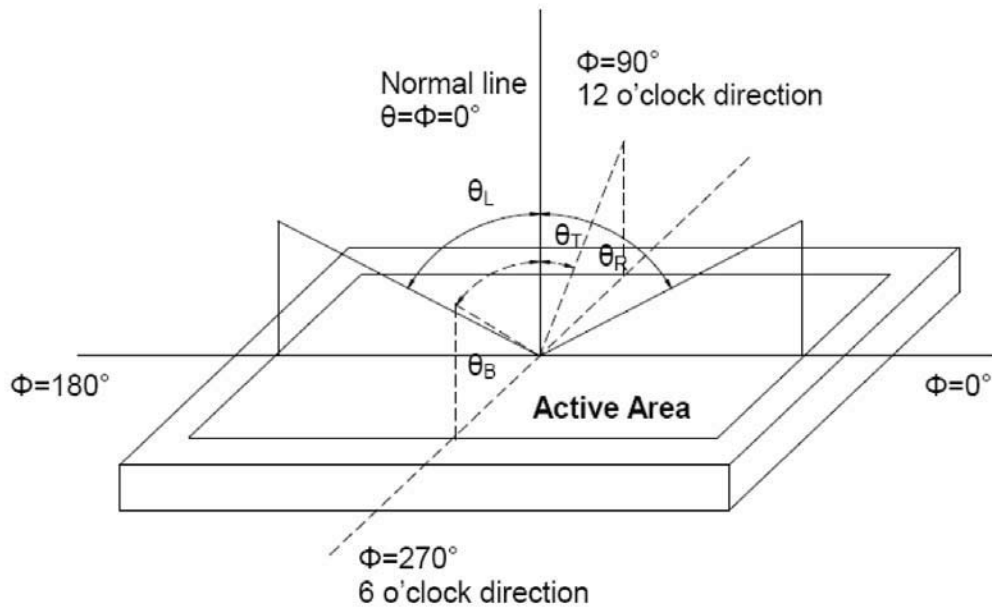


Fig. 1 Definition of viewing angle

**Note 3:** Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

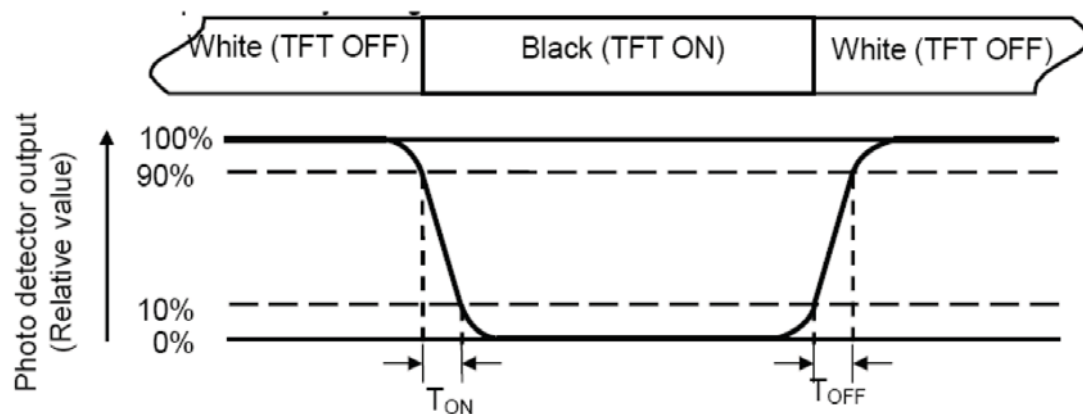
"White state ":The state is that the LCD is driven by  $V_{\text{white}}$ .

"Black state": The state is that the LCD is driven by  $V_{\text{black}}$ .

$V_{\text{white}}$ : To be determined       $V_{\text{black}}$ : To be determined.

**Note 4:** Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time ( $T_{\text{ON}}$ ) is the time between photo detector output intensity changed from 90% to 10%. And fall time ( $T_{\text{OFF}}$ ) is the time between photo detector output intensity changed from 10% to 90%.



**Note 5:** Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

## 9. Instruction Description

NT39016 3-Wire Control Register List (Default)

3-Wire Registers		Register Description		
D[15:10]	Name	Init.	R/W	Function Description
000000b	R00	07h	R/W	System control register
000001b	R01	00h	R/W	Timing Controller function register
000010b	R02	03h	R/W	Operation control register
000011b	R03	CCh	R/W	Input data Format control register
000100b	R04	46h	R/W	Source Timing delay control register
000101b	R05	0Dh	R/W	Gate Timing delay control register
000110b	R06	00h	R/W	Reserved
000111b	R07	00h	R/W	Internal function control register
001000b	R08	08h	R/W	RGB Contrast control register
001001b	R09	40h	R/W	RGB Brightness control register
001010b	R0A	88h	R/W	Hue / Saturation control register
001011b	R0B	88h	R/W	R / B Sub-Contrast control register
001100b	R0C	20h	R/W	R Sub-Brightness control register
001101b	R0D	20h	R/W	B Sub-Brightness control register
001110b	R0E	10h	R/W	VCOMDC Level Control Register
001111b	R0F	A4h	R/W	VCOMAC Level Control Register
010000b	R10	04h	R/W	VGAM2 level control register
010001b	R11	24h	R/W	VGAM3/4 level control register
010010b	R12	24h	R/W	VGAM5/6 level control register
011110b	R1E	00h	R/W	VCOMDC Trim function control register
100000b	R20	00h	R/W	Wide and narrow display mode control register

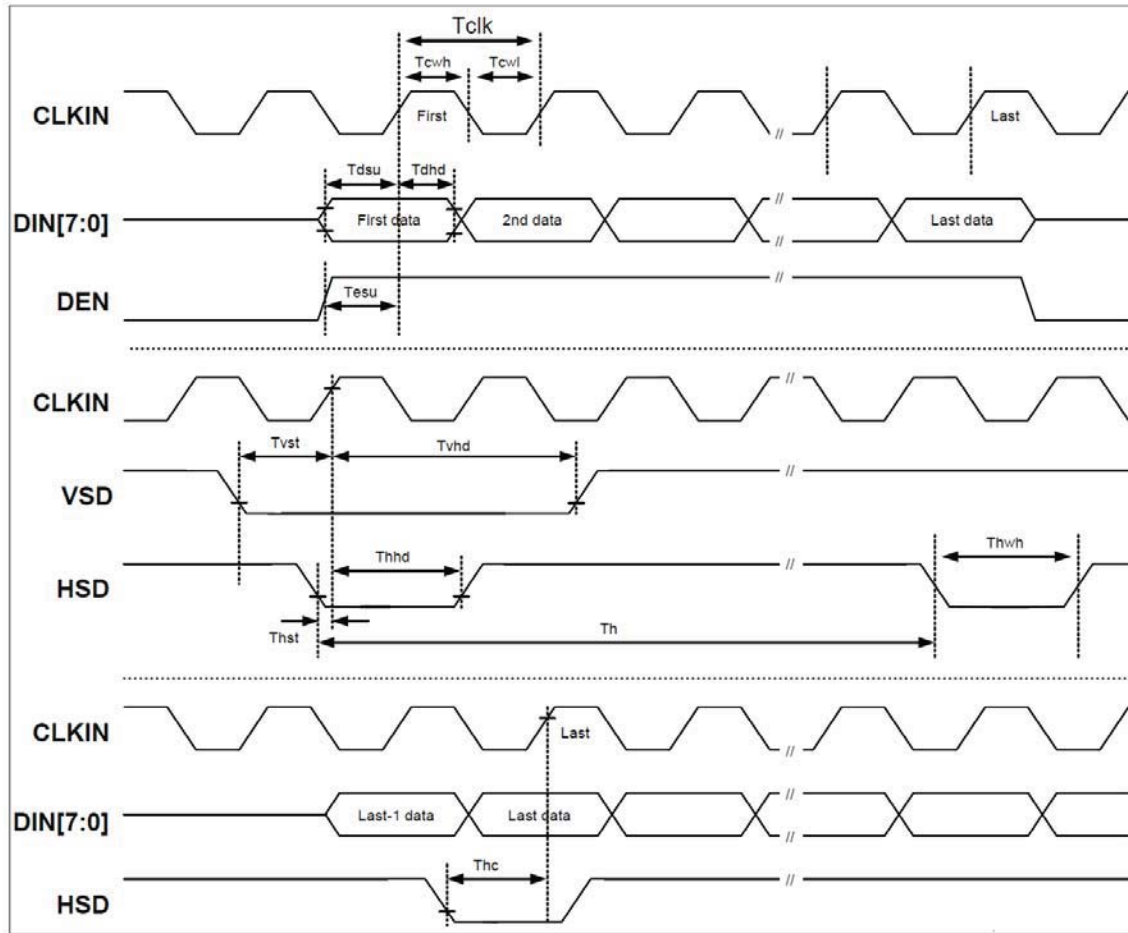
NT39016 3-Wire Register Bit Definition (Default)

3-Wire Control Register Bit Map								
Reg.	Bit [7]	Bit [6]	Bit [5]	Bit [4]	Bit [3]	Bit [2]	Bit [1]	Bit [0]
R00	PAT3	PAT2	PAT1	PAT0	PWMPDB	X	STBYB	RESETB
R01	X	X	X	SWD2	SWD1	SWD0	DITHB	CFTYP
R02	SKIPMOD	HDNC1	HDNC0	X	FPOL	VSET	UPDN	SHLR
R03	DENPOL	CLKPOL	HSDPOL	VSDPOL	SEL3	SEL2	SEL1	SEL0
R04	DDLY7	DDLY6	DDLY5	DDLY4	DDLY3	DDLY2	DDLY1	DDLY0
R05	X	HDLY6	HDLY5	HDLY4	HDLY3	HDLY2	HDLY1	HDLY0
R06	X	X	X	X	X	X	X	X
R07	FRAD1	FRAD0	INVSL1	INVSL0	PAL	PALM	-	AVGY
R08	X	X	X	CON4	CON3	CON2	CON1	CON0
R09	X	BRI6	BRI5	BRI4	BRI3	BRI2	BRI1	BRI0
R0A	HUE3	HUE2	HUE1	HUE0	SAT3	SAT2	SAT1	SAT0
R0B	SCONB1	SCONB0			SCONR1	SCONR0		
R0C	X	X	SBRIR5	SBRIR4	SBRIR3	SBRIR2	SBRIR1	SBRIR0
R0D	X	X	SBRIB5	SBRIB4	SBRIB3	SBRIB2	SBRIB1	SBRIB0
R0E	X	OTP_BYPS	VCDCSL5	VCDCSL4	VCDCSL3	VCDCSL2	VCDCSL1	VCDCSL0
R0F	VGLSL1	VGLSL0	VGHSL1	VGHSL0	VCACSL3	VCACSL2	VCACSL1	VCACSL0
R10	X	X	X	GAMEN	X	V2GAM2	V2GAM1	V2GAM0
R11	X	X	V4GAM2	V4GAM1	V4GAM0	V3GAM2	V3GAM1	V3GAM0
R12	X	X	V6GAM2	V6GAM1	V6GAM0	V5GAM2	V5GAM1	V5GAM0
R1E	TRMEN7	TRMEN6	TRMEN5	TRMEN4	TRMEN3	TRMEN2	TRMEN1	TRMEN0
R20	X	X	X	X	X	X	WNSEL1	WNSEL0

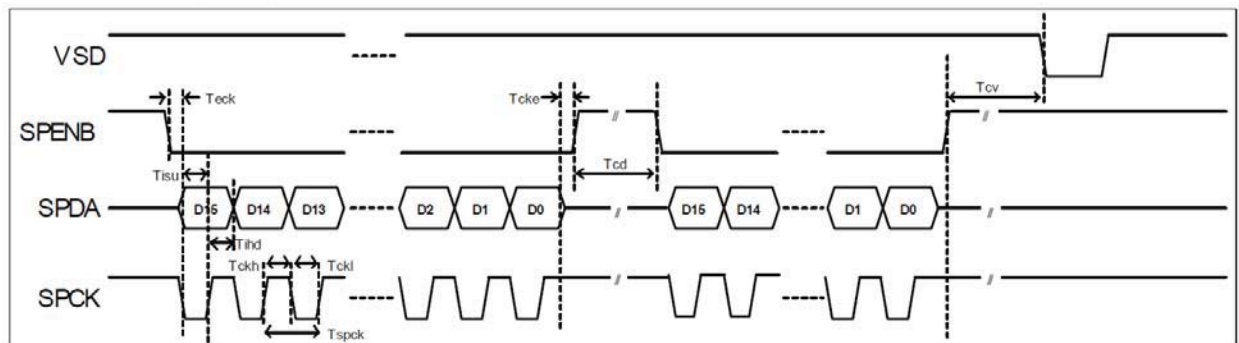
Note: Register function active at the falling edge of VSD except STBYB, RESETB register bits.

## 10. AC Characteristics

### Clock and Data Input Timing Diagram



### 3-Wire Timing Diagram





**Test Condition: (VDD=VDDP=3.3V, VDDA=5.0V, GND=GNDA=GNDP=0V, TA= 25°C)**

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
<b>System Operation Timing</b>						
VDD power source slew time	T <sub>POR</sub>			1000	us	From 0V to 90% VDD
RSTB active pulse width	T <sub>RSTB</sub>	40			us	VDD = 3.3V
<b>Input Output Timing</b>						
CLKIN clock time	T <sub>clk</sub>	-		35.7	ns	CLKIN = 28MHz
HSD to CLKIN	T <sub>hc</sub>	-	-	1	CLKIN	
HSD width	T <sub>hwh</sub>	1	-	-	CLKIN	
VSD width	T <sub>vwh</sub>	1	-	-	Th	
HSD period time	T <sub>h</sub>	60	63.56	67	us	
VSD setup time	T <sub>vst</sub>	12	-	-	ns	
VSD hold time	T <sub>vhd</sub>	12	-	-	ns	
HSD setup time	T <sub>hst</sub>	12	-	-	ns	
HSD hold time	T <sub>hhd</sub>	12	-	-	ns	
Data set-up time	T <sub>dsu</sub>	12	-	-	ns	DIN[23:0] to CLKIN
Data hold time	T <sub>dhd</sub>	12	-	-	ns	DIN[23:0] to CLKIN
DEN setup time	T <sub>esd</sub>	12	-		ns	DEN to CLKIN
Time that VSD to 1 <sup>st</sup> line data input	T <sub>vs</sub>	2	13	127	Th	@CCIR601 / 8bit RGB HV mode Control by HDLY[6:0] setting T <sub>vs</sub> = HDLY[6:0]
Time that CCIR_V to 1 <sup>st</sup> line data input	T <sub>vs</sub>	12	20	28	Th	@CCIR656 NTSC mode Control by HDLY[6:0] setting T <sub>vs</sub> = HDLY[6:0]
Time that CCIR_V to 1 <sup>st</sup> line data input	T <sub>vs</sub>	17	25	33	Th	@CCIR656 PAL mode Control by HDLY[6:0] setting T <sub>vs</sub> = HDLY[6:0]
Time that VSD to 1 <sup>st</sup> line data input	T <sub>vs</sub>	2	13	127	Th	@24bit RGB HV mode Control by HDLY[6:0] setting T <sub>vs</sub> = HDLY[6:0]
Source output stable time 1	T <sub>st</sub>	-	25	30	us	96% final, CL=30pF, RL=2K
Gate output stable time	T <sub>gst</sub>	-	500	1000	ns	96% final, CL=40pF
VCOMOUT output stable time	T <sub>cst</sub>	-	4	8	us	96% final, CL=33nF, RL=100ohm
<b>3-wire serial communication AC timing</b>						
Serial clock	T <sub>spck</sub>	320	-	-	ns	
SPCK pulse duty	T <sub>scdut</sub>	40	50	60	%	
Serial data setup time	T <sub>isu</sub>	120	-	-	ns	
Serial data hold time	T <sub>ihd</sub>	120	-	-	ns	
Serial clock high/low	T <sub>ssw</sub>	120	-	-	ns	
Chip select distinguish	T <sub>cd</sub>	1	-	-	us	
SPENA to VSD	T <sub>cv</sub>	1	-	-	us	

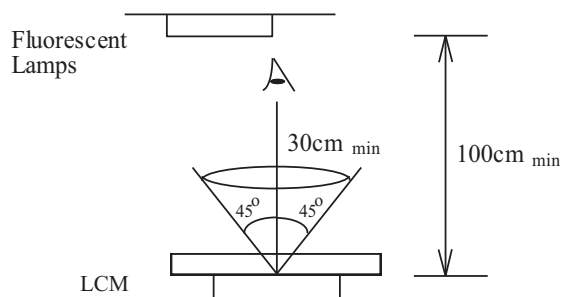
## **11.Quality Specifications**

**All The raw material are Rohs complicant.**

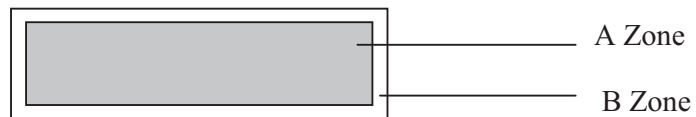
### **11.1 Standard of the product appearance test**

Manner of appearance test: The inspection should be performed in using 20W x 2 fluorescent lamps. Distance between LCM and fluorescent lamps should be 100 cm or more. Distance between LCM and inspector eyes should be 30 cm or more.

Viewing direction for inspection is 45° from vertical against LCM.



Definition of zone:



A Zone: viewing area

B Zone: outside viewing area

## 11.2 Specification of quality assurance

AQL inspection standard

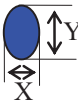
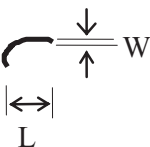
Sampling method: MIL-STD-105E, Level II, single sampling

**Defect classification (Note: \* is not including)**

Classify	Item		Note	AQL
Major	Display state	Short or open circuit	1	0.65 12
		LC leakage		
		Flickering		
		No display		
		Wrong viewing direction		
		Contrast defect (dim, ghost)	2	
		Back-light	1,8	
	Non-display	Flat cable or pin reverse	10	
		Wrong or missing component	11	
Minor	Display state	Background color deviation	2	1.0
		Black spot and dust	3	
		Line defect, Scratch	4	
		Rainbow	5	
		Chip	6	
		Pin hole	7	
	Polarizer	Protruded	12	
		Bubble and foreign material	3	
	Soldering	Poor connection	9	
	Wire	Poor connection	10	
	TAB	Position, Bonding strength	13	



**Note on defect classification**

No.	Item	Criterion																				
1	Short or open circuit	Not allow																				
	LC leakage																					
	Flickering																					
	No display																					
	Wrong viewing direction																					
	Wrong Back-light																					
2	Contrast defect	Refer to approval sample																				
	Background color deviation																					
3	Point defect, Black spot, dust (including Polarizer)  $\Phi = (X+Y)/2$	<div></div> <table><tr><th>Point Size</th><th>Acceptable Qty.</th></tr><tr><td><math>\Phi \leq 0.10</math></td><td>Disregard</td></tr><tr><td><math>0.10 &lt; \Phi \leq 0.20</math></td><td>3</td></tr><tr><td><math>0.20 &lt; \Phi \leq 0.25</math></td><td>2</td></tr><tr><td><math>0.25 &lt; \Phi \leq 0.30</math></td><td>1</td></tr><tr><td><math>\Phi &gt; 0.30</math></td><td>0</td></tr></table> <div>Unit : mm</div>	Point Size	Acceptable Qty.	$\Phi \leq 0.10$	Disregard	$0.10 < \Phi \leq 0.20$	3	$0.20 < \Phi \leq 0.25$	2	$0.25 < \Phi \leq 0.30$	1	$\Phi > 0.30$	0								
Point Size	Acceptable Qty.																					
$\Phi \leq 0.10$	Disregard																					
$0.10 < \Phi \leq 0.20$	3																					
$0.20 < \Phi \leq 0.25$	2																					
$0.25 < \Phi \leq 0.30$	1																					
$\Phi > 0.30$	0																					
4	Line defect, Scratch	<div></div> <table><tr><th colspan="2">Line</th><th>Acceptable Qty.</th></tr><tr><th>L</th><th>W</th><th></th></tr><tr><td>---</td><td><math>0.015 \geq W</math></td><td>Disregard</td></tr><tr><td><math>3.0 \geq L</math></td><td><math>0.03 \geq W</math></td><td rowspan="2">2</td></tr><tr><td><math>2.0 \geq L</math></td><td><math>0.05 \geq W</math></td></tr><tr><td><math>1.0 \geq L</math></td><td><math>0.1 &gt; W</math></td><td>1</td></tr><tr><td>---</td><td><math>0.05 &gt; W</math></td><td>Applied as point defect</td></tr></table> <div>Unit: mm</div>	Line		Acceptable Qty.	L	W		---	$0.015 \geq W$	Disregard	$3.0 \geq L$	$0.03 \geq W$	2	$2.0 \geq L$	$0.05 \geq W$	$1.0 \geq L$	$0.1 > W$	1	---	$0.05 > W$	Applied as point defect
Line		Acceptable Qty.																				
L	W																					
---	$0.015 \geq W$	Disregard																				
$3.0 \geq L$	$0.03 \geq W$	2																				
$2.0 \geq L$	$0.05 \geq W$																					
$1.0 \geq L$	$0.1 > W$	1																				
---	$0.05 > W$	Applied as point defect																				
5	Rainbow	Not more than two color changes across the viewing area.																				

No	Item	Criterion
----	------	-----------

6	Chip	
Remark:		
X: Length direction		
Y: Short direction		
Z: Thickness direction		
t: Glass thickness		
W: Terminal Width		

	Acceptable criterion						
	<table><tr><td>X</td><td>Y</td><td>Z</td></tr><tr><td>≤ 2</td><td>0.5mm</td><td>≤ t/2</td></tr></table>	X	Y	Z	≤ 2	0.5mm	≤ t/2
X	Y	Z					
≤ 2	0.5mm	≤ t/2					

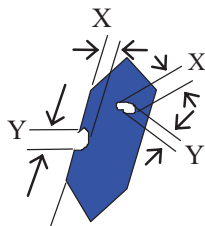
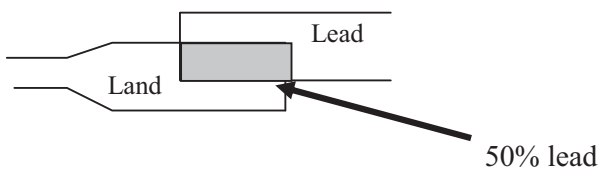
	Acceptable criterion						
	<table><tr><td>X</td><td>Y</td><td>Z</td></tr><tr><td>≤ 2</td><td>0.5mm</td><td>≤ t</td></tr></table>	X	Y	Z	≤ 2	0.5mm	≤ t
X	Y	Z					
≤ 2	0.5mm	≤ t					

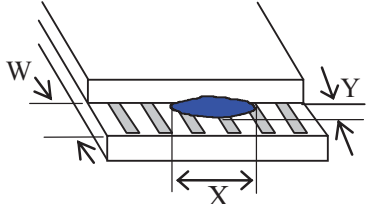
	Acceptable criterion								
	<table><tr><td>X</td><td>Y</td><td>Z</td></tr><tr><td>≤ 3</td><td>≤ 2</td><td rowspan="2">≤ t</td></tr><tr><td colspan="2">shall not reach to ITO</td></tr></table>	X	Y	Z	≤ 3	≤ 2	≤ t	shall not reach to ITO	
X	Y	Z							
≤ 3	≤ 2	≤ t							
shall not reach to ITO									

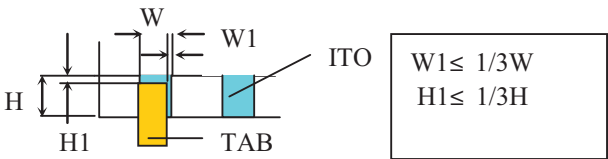
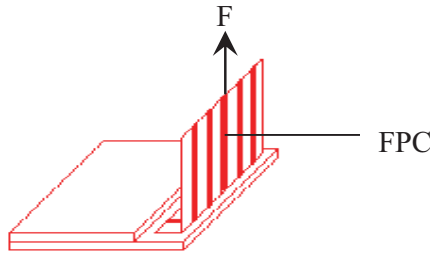
	Acceptable criterion						
	<table><tr><td>X</td><td>Y</td><td>Z</td></tr><tr><td>Disregard</td><td>≤ 0.2</td><td>≤ t</td></tr></table>	X	Y	Z	Disregard	≤ 0.2	≤ t
X	Y	Z					
Disregard	≤ 0.2	≤ t					

	Acceptable criterion						
	<table><tr><td>X</td><td>Y</td><td>Z</td></tr><tr><td>≤ 5</td><td>≤ 2</td><td>≤ t/3</td></tr></table>	X	Y	Z	≤ 5	≤ 2	≤ t/3
X	Y	Z					
≤ 5	≤ 2	≤ t/3					

No.	Item	Criterion
7	<p>Segment pattern</p> <p>W = Segment width</p>	<p>(1) Pin hole</p> <p><math>\Phi &lt; 0.10\text{mm}</math> is acceptable.</p>

	$\Phi = (X+Y)/2$	 <table data-bbox="868 356 1302 486"> <tr> <th>Point Size</th> <th>Acceptable Qty</th> </tr> <tr> <td><math>\Phi \leq 1/4 W</math></td> <td>Disregard</td> </tr> <tr> <td><math>1/4 W &lt; \Phi \leq 1/2 W</math></td> <td>1</td> </tr> </table>	Point Size	Acceptable Qty	$\Phi \leq 1/4 W$	Disregard	$1/4 W < \Phi \leq 1/2 W$	1
Point Size	Acceptable Qty							
$\Phi \leq 1/4 W$	Disregard							
$1/4 W < \Phi \leq 1/2 W$	1							
8	Back-light	(1) The color of backlight should correspond its specification. (2) Not allow flickering						
9	Soldering	(1) Not allow heavy dirty and solder ball on PCB. (The size of dirty refer to point and dust defect) (2) Over 50% of lead should be soldered on Land.  						
10	Wire	(1) Copper wire should not be rusted (2) Not allow crack on copper wire connection. (3) Not allow reversing the position of the flat cable. (4) Not allow exposed copper wire inside the flat cable.						
11*	PCB	(1) Not allow screw rust or damage. (2) Not allow missing or wrong putting of component.						

No	Item	Criterion
12	Protruded W: Terminal Width	 Acceptable criteria: $Y \Phi 0.4$

13	TAB	<p>1. Position</p>  <p>2 FPC bonding strength test</p>  <p><math>P (=F/\text{FPC bonding width}) \geq 650\text{gf/cm}</math> ,(speed rate: 1mm/min) 5pcs per SOA (shipment)</p>
14	Total no. of acceptable Defect	<p>A. Zone</p> <p>Maximum 2 minor non-conformities per one unit. Defect distance: each point to be separated over 10mm</p> <p>B. Zone</p> <p>It is acceptable when it is no trouble for quality and assembly in customer's end product.</p>

### 11.3 Reliability of LCM

Reliability test condition:

Item	Condition	Time (hrs)	Assessment
High temp. Storage	60°C	48	No abnormalities in functions
High temp. Operating	50°C	48	
Low temp. Storage	-20°C	48	
Low temp. Operating	-10°C	48	

Humidity	40°C/ 90%RH	48	and appearance
Temp. Cycle	-20°C ← 25°C → 60°C (60 min ← 5 min → 60min)	10cycles	

Recovery time should be 24 hours minimum. Moreover, functions, performance and appearance shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature ( $20 \pm 8^{\circ}\text{C}$ ), normal humidity (below 65% RH), and in the area not exposed to direct sun light.

## 11.4 Precaution for using LCD/LCM

LCD/LCM is assembled and adjusted with a high degree of precision. Do not attempt to make any alteration or modification. The followings should be noted.

### General Precautions:

1. LCD panel is made of glass. Avoid excessive mechanical shock or applying strong pressure onto the surface of display area.
2. The polarizer used on the display surface is easily scratched and damaged. Extreme care should be taken when handling. To clean dust or dirt off the display surface, wipe gently with cotton, or other soft material soaked with isopropyl alcohol, ethyl alcohol or trichlorotrifluoroethane, do not use water, ketone or aromatics and never scrub hard.
3. Do not tamper in any way with the tabs on the metal frame.
4. Do not make any modification on the PCB without consulting OD.
5. When mounting a LCM, make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
6. Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels and also cause rainbow on the display.
7. Be careful not to touch or swallow liquid crystal that might leak from a damaged cell. Any liquid crystal adheres to skin or clothes, wash it off immediately with soap and water.

### Static Electricity Precautions:

1. CMOS-LSI is used for the module circuit; therefore operators should be grounded whenever he/she comes into contact with the module.
2. Do not touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.
3. Do not touch the connection terminals of the display with bare hand; it will cause disconnection or defective insulation of terminals.
4. The modules should be kept in anti-static bags or other containers resistant to static for storage.
5. Only properly grounded soldering irons should be used.
6. If an electric screwdriver is used, it should be grounded and shielded to prevent sparks.
7. The normal static prevention measures should be observed for work clothes and working benches.
8. Since dry air is inductive to static, a relative humidity of 50-60% is recommended.

### Soldering Precautions:

1. Soldering should be performed only on the I/O terminals.
2. Use soldering irons with proper grounding and no leakage.
3. Soldering temperature:  $280^{\circ}\text{C} \pm 10^{\circ}\text{C}$
4. Soldering time: 3 to 4 second.
5. Use eutectic solder with resin flux filling.
6. If flux is used, the LCD surface should be protected to avoid spattering flux.
7. Flux residue should be removed.

**Operation Precautions:**

1. The viewing angle can be adjusted by varying the LCD driving voltage  $V_o$ .
2. Since applied DC voltage causes electro-chemical reactions, which deteriorate the display, the applied pulse waveform should be a symmetric waveform such that no DC component remains. Be sure to use the specified operating voltage.
3. Driving voltage should be kept within specified range; excess voltage will shorten display life.
4. Response time increases with decrease in temperature.
5. Display color may be affected at temperatures above its operational range.
6. Keep the temperature within the specified range usage and storage. Excessive temperature and humidity could cause polarization degradation, polarizer peel-off or generate bubbles.
7. For long-term storage over 40°C is required, the relative humidity should be kept below 60%, and avoid direct sunlight.

**Limited Warranty**

Orient Display LCDs and modules are not consumer products, but may be incorporated by Orient Display's customers into consumer products or components thereof, Orient Display does not warrant that its LCDs and components are fit for any such particular purpose.

1.The liability of Orient Display is limited to repair or replacement on the terms set forth below. Orient Display will not be responsible for any subsequent or consequential events or injury or damage to any personnel or user including third party personnel and/or user. Unless otherwise agreed in writing between Orient Display and the customer. Orient Display will only replace or repair any of its LCD which is found defective electrically or visually when inspected in accordance with Orient Display general LCD inspection standard (copies available on request).

2.No warranty can be granted if any of the precautions state in handling liquid crystal display above has been disregarded. Broken glass, scratches on polarizer mechanical damages as well as defects that are caused accelerated environment tests are excluded from warranty.

3.In returning the LCD/LCM, they must be properly packaged; there should be detailed description of the failures or defect.