

DS26LS31C/DS26LS31M Quad High Speed Differential Line Driver

 Check for Samples: [DS26LS31C](#), [DS26LS31M](#)

FEATURES

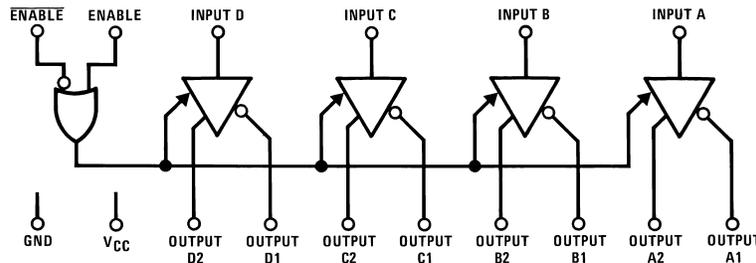
- Output skew—2.0 ns typical
- Input to output delay—10 ns typical
- Operation from single 5V supply
- Outputs won't load line when $V_{CC} = 0V$
- Four line drivers in one package for maximum package density
- Output short-circuit protection
- Complementary outputs
- Meets the requirements of EIA Standard RS-422
- Pin compatible with AM26LS31
- Available in military and commercial temperature range

DESCRIPTION

The DS26LS31 is a quad differential line driver designed for digital data transmission over balanced lines. The DS26LS31 meets all the requirements of EIA Standard RS-422 and Federal Standard 1020. It is designed to provide unipolar differential drive to twisted-pair or parallel-wire transmission lines.

The circuit provides an enable and disable function common to all four drivers. The DS26LS31 features TRI-STATE outputs and logically ANDed complementary outputs. The inputs are all LS compatible and are all one unit load.

Logic and Connection Diagrams



Dual-In-Line Package

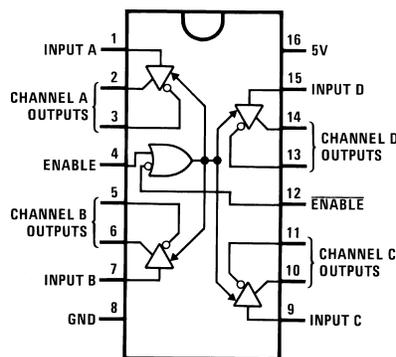


Figure 1. Top View



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These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

Absolute Maximum Ratings ⁽¹⁾

Supply Voltage	7V
Input Voltage	7V
Output Voltage	5.5V
Output Voltage (Power OFF)	-0.25 to 6V
Maximum Power Dissipation ⁽²⁾ at 25°C	
Cavity Package	1509 mW
Molded DIP Package	1476 mW
SO Package	1051 mW

- (1) "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The [Electrical Characteristics](#) provide conditions for actual device operation.
- (2) Derate cavity package 10.1 mW/°C above 25°C; derate molded DIP package 11.9 mW/°C above 25°C; derate SO package 8.41 mW/°C above 25°C.

Operating Conditions

	Min	Max	Units
Supply Voltage, V_{CC}			
DS26LS31M	4.5	5.5	V
DS26LS31	4.75	5.25	V
Temperature, T_A			
DS26LS31M	-55	+125	°C
DS26LS31	0	+70	°C

Electrical Characteristics ⁽¹⁾ ⁽²⁾ ⁽³⁾

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_{OH}	Output High Voltage	$I_{OH} = -20$ mA	2.5			V
V_{OL}	Output Low Voltage	$I_{OL} = 20$ mA			0.5	V
V_{IH}	Input High Voltage		2.0			V
V_{IL}	Input Low Voltage				0.8	V
I_{IL}	Input Low Current	$V_{IN} = 0.4$ V		-40	-200	μA
I_{IH}	Input High Current	$V_{IN} = 2.7$ V			20	μA
I_I	Input Reverse Current	$V_{IN} = 7$ V			0.1	mA
I_O	TRI-STATE Output Current	$V_O = 2.5$ V			20	μA
		$V_O = 0.5$ V			-20	μA
V_{CL}	Input Clamp Voltage	$I_{IN} = -18$ mA			-1.5	V
I_{SC}	Output Short-Circuit Current		-30		-150	mA
I_{CC}	Power Supply Current	All Outputs Disabled		35	60	mA
		or Active				

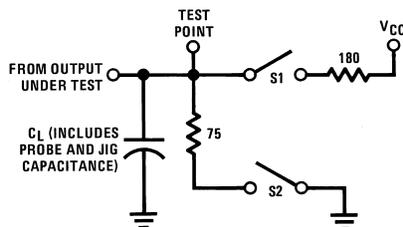
- (1) Unless otherwise specified min/max limits apply across the -55°C to +125°C temperature range for the DS26LS31M and across the 0°C to +70°C range for the DS26LS31. All typicals are given for $V_{CC} = 5$ V and $T_A = 25$ °C.
- (2) All currents into device pins are positive; all currents out of device pins are negative. All voltages are referenced to ground unless otherwise specified.
- (3) Only one output at a time should be shorted.

Switching Characteristics

$V_{CC} = 5V, T_A = 25^\circ C$

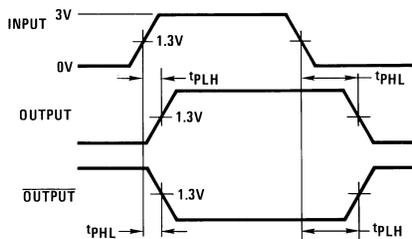
Symbol	Parameter	Conditions	Min	Typ	Max	Units
t_{PLH}	Input to Output	$C_L = 30 \text{ pF}$		10	15	ns
t_{PHL}	Input to Output	$C_L = 30 \text{ pF}$		10	15	ns
Skew	Output to Output	$C_L = 30 \text{ pF}$		2.0	6.0	ns
t_{LZ}	Enable to Output	$C_L = 10 \text{ pF}, S2 \text{ Open}$		15	35	ns
t_{HZ}	Enable to Output	$C_L = 10 \text{ pF}, S1 \text{ Open}$		15	25	ns
t_{ZL}	Enable to Output	$C_L = 30 \text{ pF}, S2 \text{ Open}$		20	30	ns
t_{ZH}	Enable to Output	$C_L = 30 \text{ pF}, S1 \text{ Open}$		20	30	ns

AC Test Circuit and Switching Time Waveforms



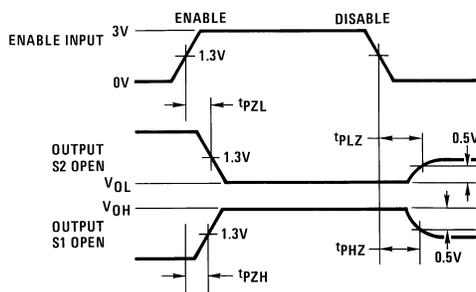
S1 and S2 of load circuit are closed except where shown.

Figure 2. AC Test Circuit



$f = 1 \text{ MHz}, t_r \leq 15 \text{ ns}, t_f \leq 6 \text{ ns}$

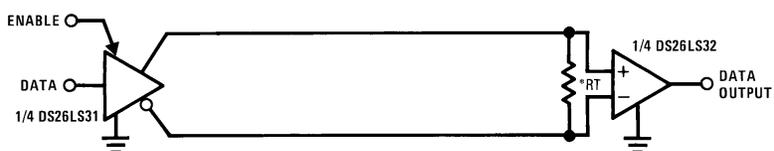
Figure 3. Propagation Delays



$f = 1 \text{ MHz}, t_r \leq 15 \text{ ns}, t_f \leq 6 \text{ ns}$

Figure 4. Enable and Disable Times

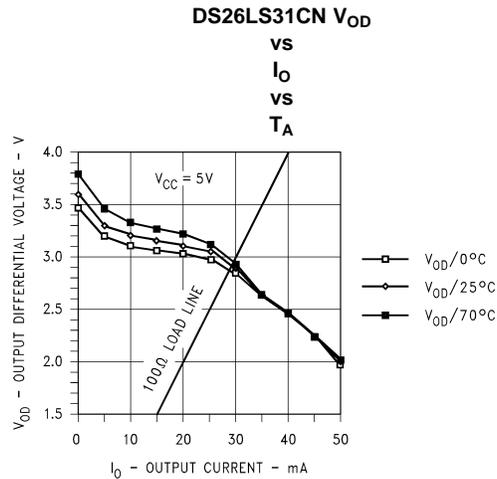
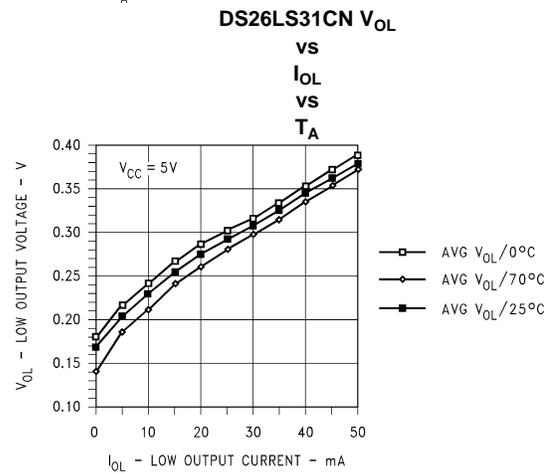
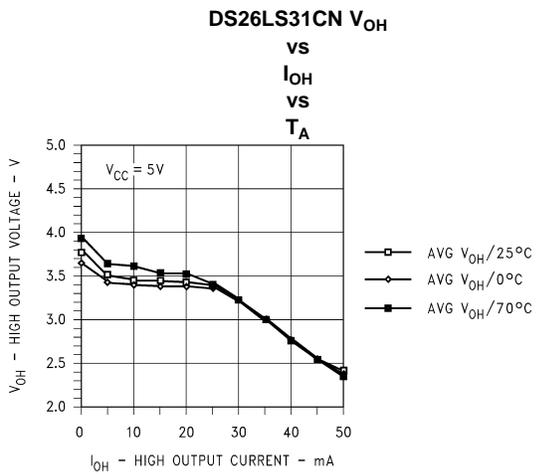
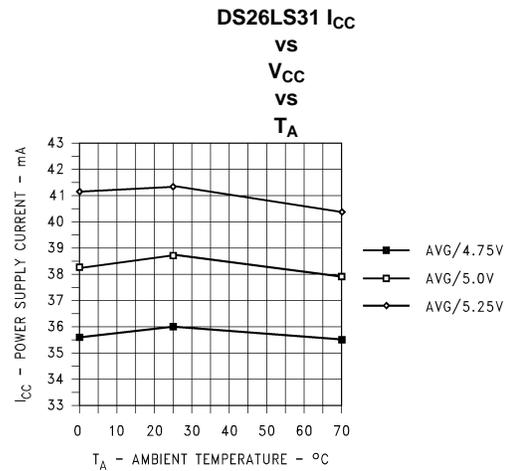
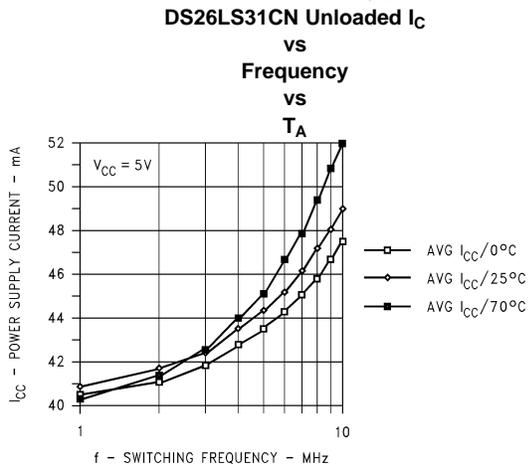
Typical Applications



R_T is optional although highly recommended to reduce reflection.

Figure 5. Two-Wire Balanced System, RS-422

Typical Performance Characteristics



PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp (3)	Samples (Requires Login)
AM26LS31PC	ACTIVE	PDIP	NFG	16	25	TBD	Call TI	Level-1-NA-UNLIM	
DS26LS31CM	ACTIVE	SOIC	D	16	48	TBD	CU SNPB	Level-1-235C-UNLIM	
DS26LS31CM/NOPB	ACTIVE	SOIC	D	16	48	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	
DS26LS31CMX	ACTIVE	SOIC	D	16	2500	TBD	CU SNPB	Level-1-235C-UNLIM	
DS26LS31CMX/NOPB	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	
DS26LS31CN	ACTIVE	PDIP	NFG	16	25	TBD	Call TI	Level-1-NA-UNLIM	
DS26LS31CN/NOPB	ACTIVE	PDIP	NFG	16	25	Pb-Free (RoHS)	CU SN	Level-1-NA-UNLIM	
DS26LS31N	ACTIVE	PDIP	NFG	16	25	TBD	Call TI	Level-1-NA-UNLIM	

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

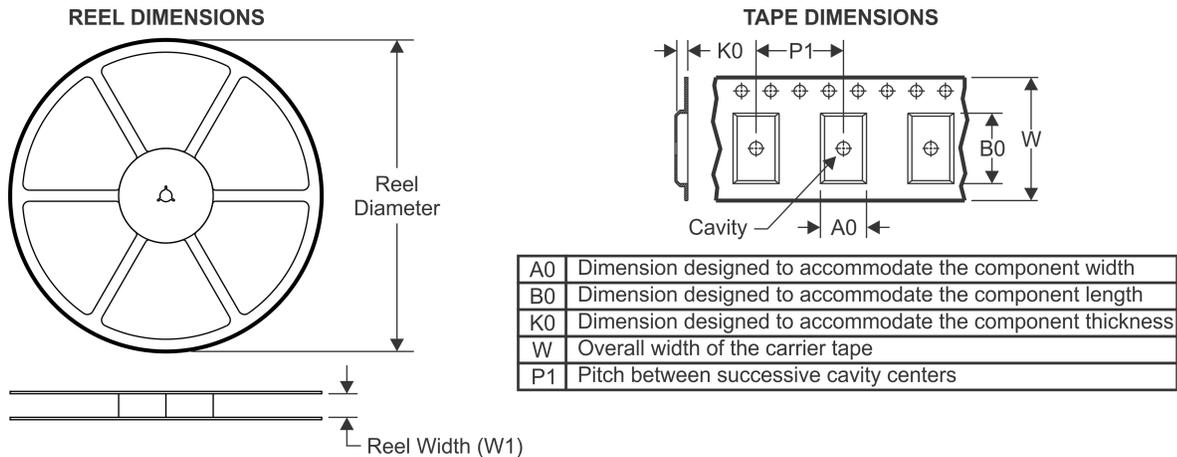
Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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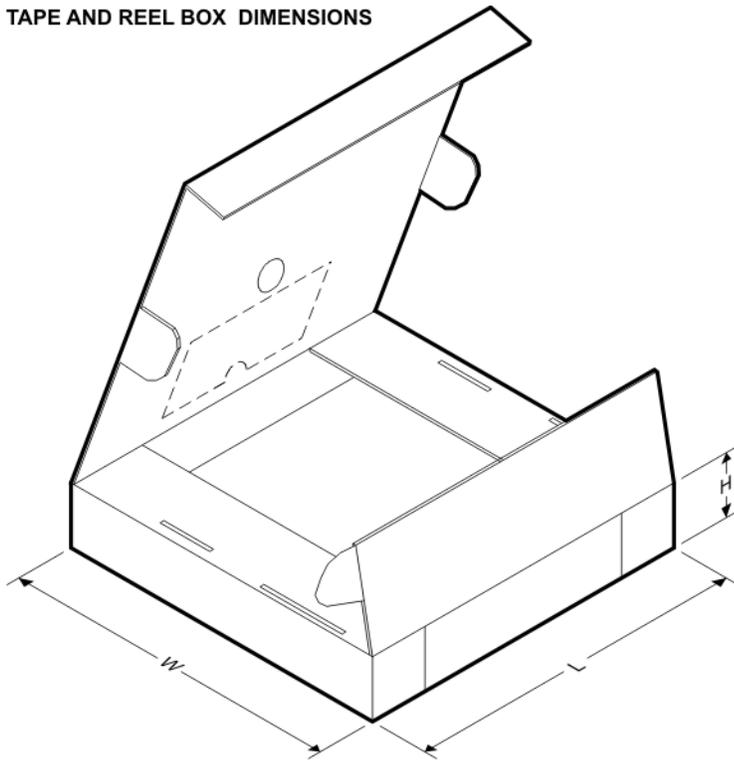
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TAPE AND REEL INFORMATION

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
DS26LS31CMX	SOIC	D	16	2500	330.0	16.4	6.5	10.3	2.3	8.0	16.0	Q1
DS26LS31CMX/NOPB	SOIC	D	16	2500	330.0	16.4	6.5	10.3	2.3	8.0	16.0	Q1

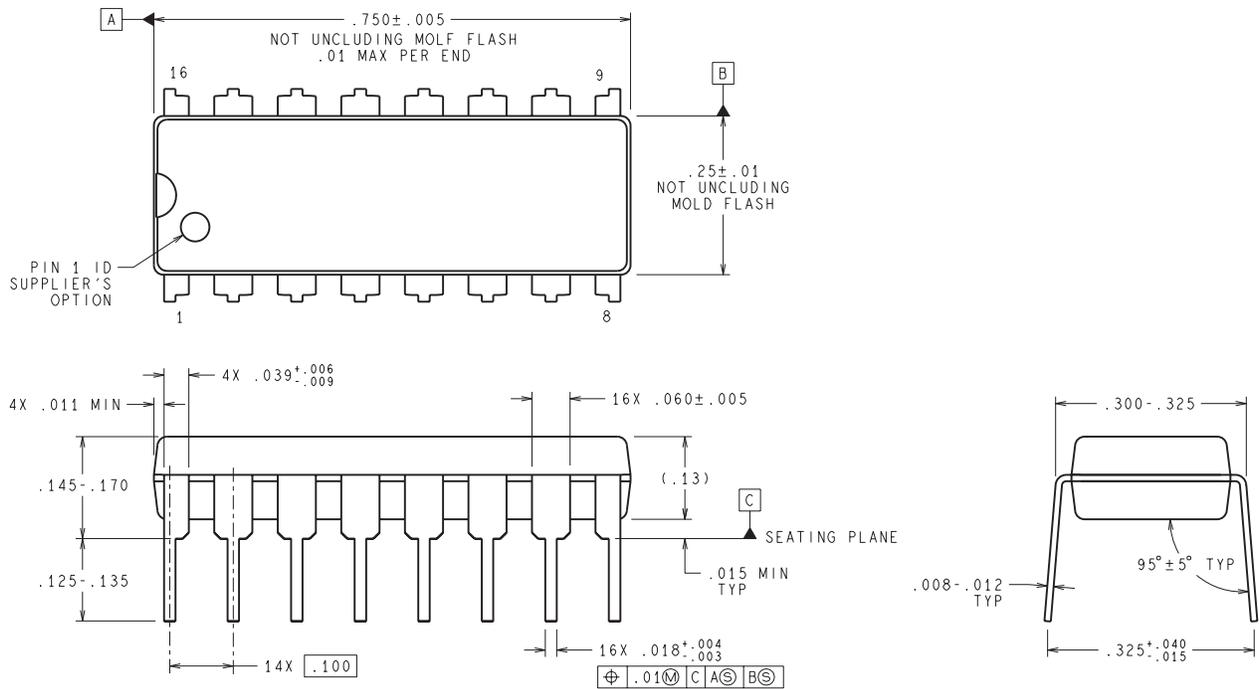
TAPE AND REEL BOX DIMENSIONS



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
DS26LS31CMX	SOIC	D	16	2500	349.0	337.0	45.0
DS26LS31CMX/NOPB	SOIC	D	16	2500	349.0	337.0	45.0

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