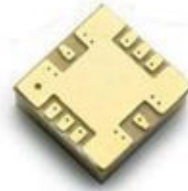


AMGP-6432

28-31 GHz 2W SMT Packaged Power Amplifier



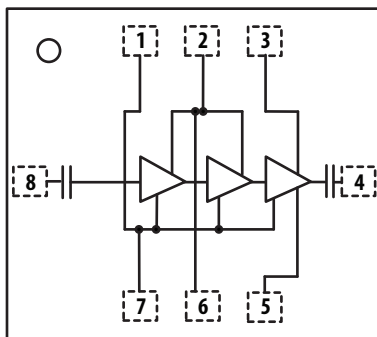
Data Sheet



Description

The AMGP-6432 is a surface mount packaged 2-Watt power amplifier that operates from frequencies between 28 to 31 GHz. In the operational frequency band from 29.25 to 30 GHz, it provides 33 dBm of typical output power (P_{1dB})/ 34 dBm P_{sat} and 21.5dB of small-signal gain. This PA is also suitable for high linear application where the PA demonstrates greater than -38dBc of third order output inter modulation (OIM3) at +18dBm/ tone output power level.

Functional Block Diagram



Pin	Function
1	Vg
2	Vd1
3	Vd2
4	RF_OUT
5	Vd2
6	Vd1
7	Vg
8	RF_IN



Attention: Observe Precautions for handling electrostatic sensitive devices.
ESD Machine Model (Class A): 60 V
ESD Human Body Model (Class 1A): 200 V
Refer to Avago Application Note A004R:
Electrostatic Discharge Damage and Control.

Features

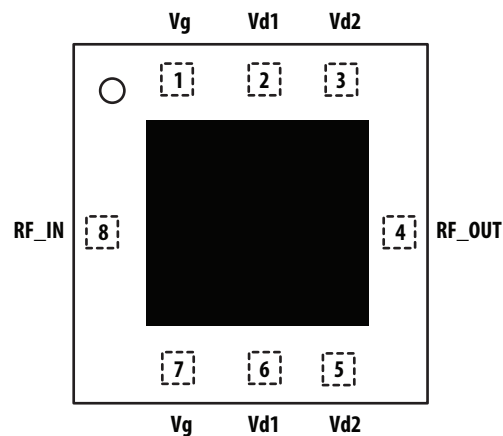
- 5 x 5 mm surface mount package
- > +33 dBm Output Power from 28.5 to 31 GHz
- 50 Ω input and output match
- -40° C to +85° C operation

Applications

- VSAT
- Microwave Radio System
- Satellite Up/Down Link

Note: This part is not rated for high-moisture environments.

Package Diagram



ELECTRICAL SPECIFICATIONS

Table 1. Absolute Minimum and Maximum ^[1] Ratings

Parameter		Specifications			Comments
Description	Pin	Min.	Max.	Unit	
Drain Supply Voltage	V _{d1} V _{d2}		6.5	V	
Gate Supply Voltage	V _g	-2	0	V	
RF Input Power (P _{in}) ^[2]	RFIN		24	dBm	CW
Power Dissipation (P _{diss})			10	W	P _{diss} = V _{d1} × I _{d1} + V _{d2} × I _{d2} + P _{in} - P _{out}
MSL			MSL2		
T _{CH}			150	°C	Channel Temperature
T _{STG}		-65	150	°C	Storage Temperature

Notes:

1. Operation of this device above any one of these maximum parameters may cause permanent damage
2. With the DC (typical bias) and RF applied to the device at board temperature T_b = 25° C

Table 2. Recommended Operating Range

Parameter		Specifications				Comments
Description	Pin	Min.	Typical	Max.	Unit	
Drain Supply Voltage	V _{d1} V _{d2}		6.0		V	
Gate Supply Voltage	V _g	-1	-0.68	-0.6	V	
Maximum Gate Current	I _{g, max}	-3	-2.5		mA	I _{g, max} occurs at highest RF P _{out} condition.
Quiescent Drain Supply Current (I _{dq})	V _{d1} V _{d2}		300 400		mA	I _{dq} = I _{d1} + I _{d2}
RF Output Power (P _{out})	RFOUT		33		dBm	CW
Frequency Range		28		31	GHz	
Thermal Resistance, θ _{ch-b}			7.6		°C/W	Channel to board
Base Plate Temperature		-40		+85	°C	

Electrical Specifications

All data measured on a 2.4 mm connectorized production contactor board (Rogers 4350B) at $V_{dd1} = V_{dd2} = 6\text{ V}$, $I_{dq} = 0.7\text{ A}$ ($I_{dq1} + I_{dq2}$), $T_c = 25\text{ }^\circ\text{C}$, and $50\text{ }\Omega$ at all ports, unless otherwise stated.

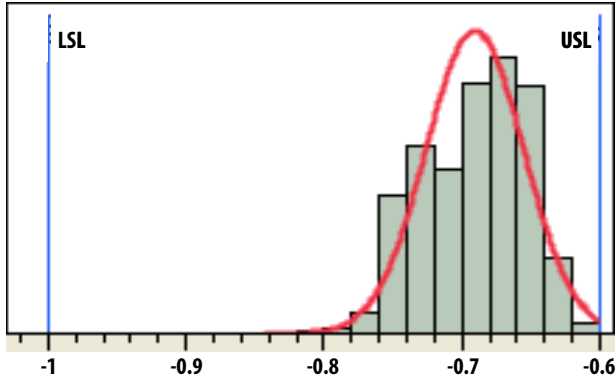
Table 3. RF Electrical Characteristics

Parameter	Performance			Unit	Comments
	Min.	Typical	Max.		
Frequency Range (GHz)	28		31	GHz	
Input Return Loss (dB)		-15		dB	Small Signal
Output Return Loss (dB)		-12		dB	Small Signal
Gain (dB) ^[1] @ Freq = 29.25 GHz	18	21.5	25	dB	$V_{dd} = 6\text{ V}$, $I_{dq} = 0.7\text{ A}$
($P_{in} = 0\text{ dBm}$) @ Freq = 30 GHz	18	21.3	25		$V_{dd} = 6\text{ V}$, $I_{dq} = 0.7\text{ A}$
Reverse Isolation (dB)		-50		dB	Small Signal
P_{out} ^[1] @ Freq = 29.25 GHz	33	34.8		dBm	$V_{dd} = 6\text{ V}$, $I_{dq} = 0.7\text{ A}$
($P_{in} = 17\text{ dBm}$) @ Freq = 30 GHz	33	34.3			$V_{dd} = 6\text{ V}$, $I_{dq} = 0.7\text{ A}$
P_{1dB} @ Freq = 29.25 GHz		33.9			$V_{dd} = 6\text{ V}$, $I_{dq} = 0.7\text{ A}$
@ Freq = 30 GHz		32.9			$V_{dd} = 6\text{ V}$, $I_{dq} = 0.7\text{ A}$
IM3 Level		-38		dBc	$\Delta f = 20\text{ MHz}$, $P_{out} = 20\text{ dBm/tone}$
Total Drain Current		0.7		A	I_{dq}

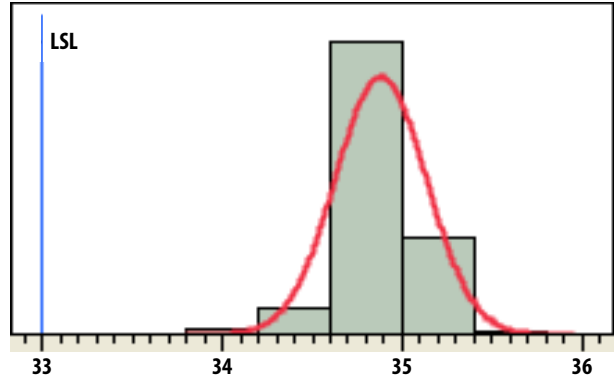
Note:

1. P_{out} and Gain measurement accuracy is subjected to the tolerance of $\pm 0.5\text{ dBm}$, respectively.

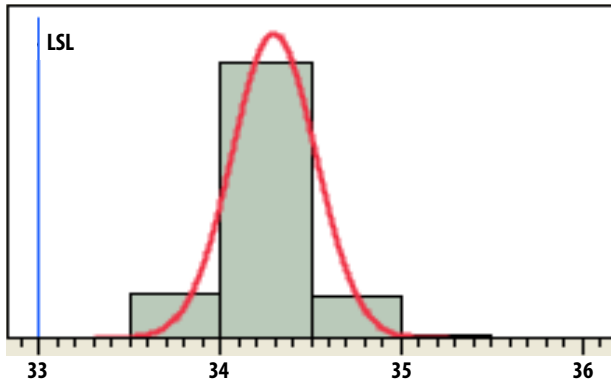
Product Consistency Distribution Charts at 29.25 GHz and 30 GHz, $V_{dd} = 6\text{ V}$, $I_{dq} = 0.7\text{ A}$



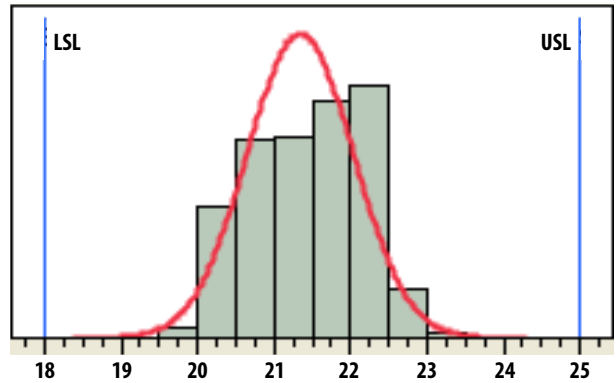
V_g @ $V_{dd} = 6\text{ V}$, $I_{dq} = 0.7\text{ A}$, Mean = -0.68 V , LSL = -1 V , USL = -0.6 V



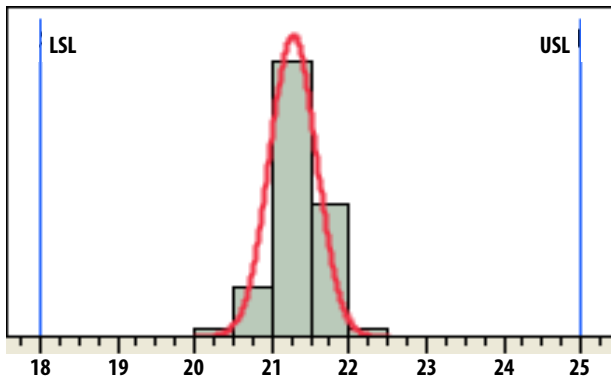
P_{out} @ 29.25 GHz ($P_{in} = 17\text{ dBm}$), Mean = 34.8 dBm , LSL = 33 dBm



P_{out} @ 30 GHz ($P_{in} = 17\text{ dBm}$), Mean = 34.3 dBm , LSL = 33 dBm



Gain @ 29.25 GHz, Mean = 21.5 dB , LSL = 18 dB , USL = 25 dB



Gain @ 30 GHz, Mean = 21.2 dB , LSL = 18 dB , USL = 25 dB

Selected Performance Plots

All data measured on a 2.4 mm connector based evaluation board at $V_{dd1} = V_{dd2} = 6\text{ V}$, $I_{dq} = 0.7\text{ A}$ ($I_{d1} + I_{d2}$), $T_A = 25^\circ\text{C}$, and $50\ \Omega$ at all ports.

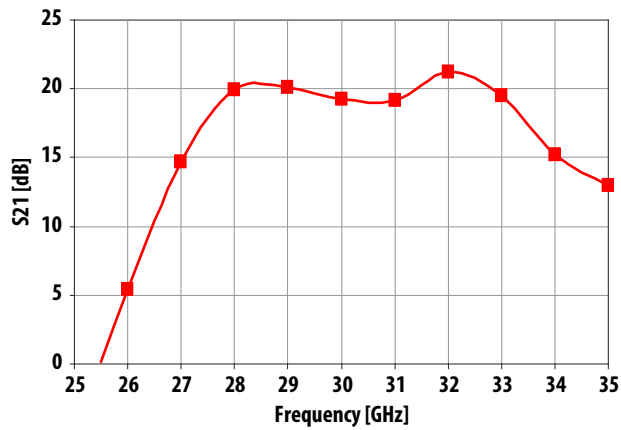


Figure 1. S_{21} (dB) Frequency Sweep

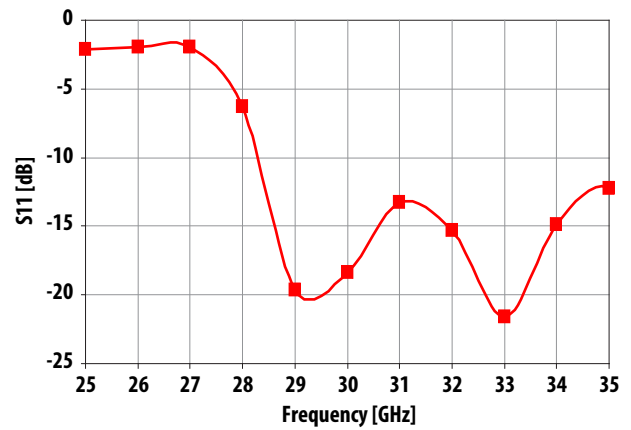


Figure 2. S_{11} (dB) Frequency Sweep

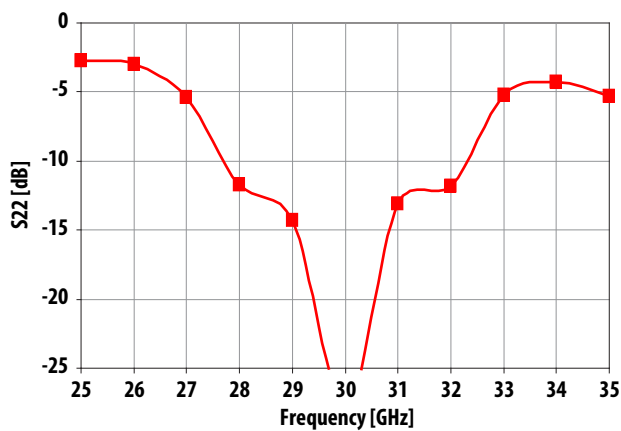


Figure 3. S_{22} (dB) Frequency Sweep

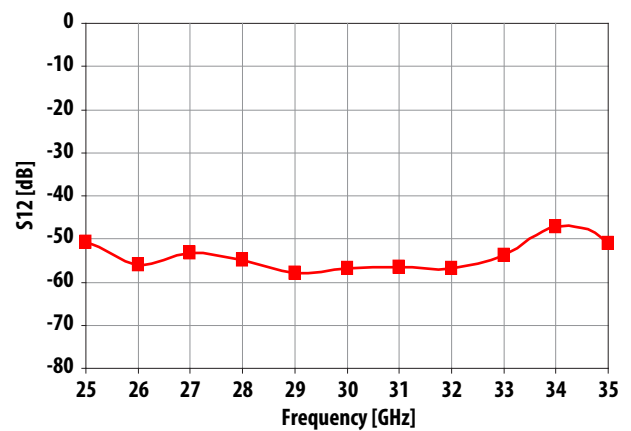


Figure 4. S_{12} (dB) Frequency Sweep

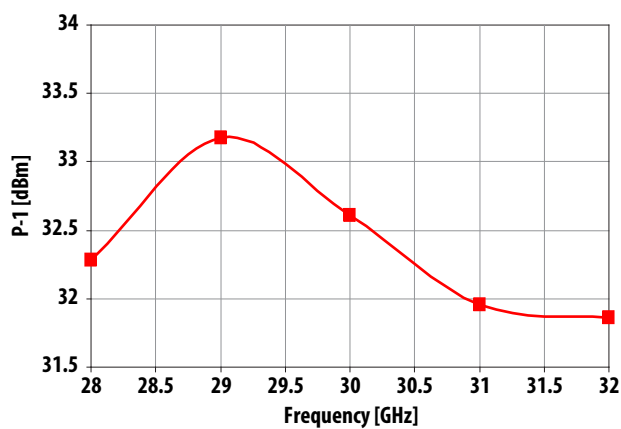


Figure 5. P_{1dB} (dBm) Frequency Sweep

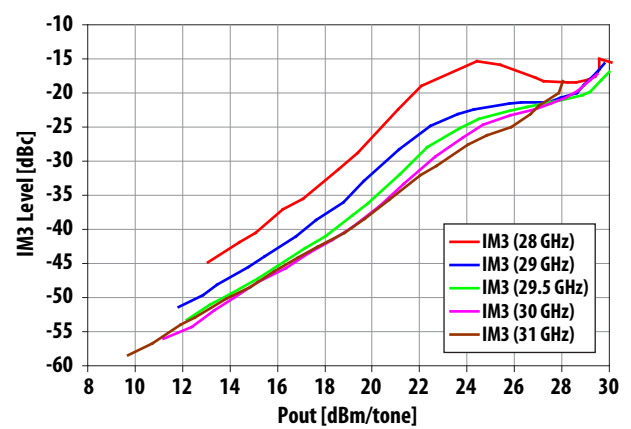


Figure 6. IM3 level (dBc) vs. Output power/tone

Selected Performance Plots over Operating Temperature Range

All data measured on a 2.4 mm connector based evaluation board at $V_{dd1} = V_{dd2} = 6\text{ V}$, $I_{dq} = 0.7\text{ A}$ ($I_{d1} + I_{d2}$), and $50\ \Omega$ at all ports. I_{dg} has been maintained at 0.7 A under different temperature conditions. Plotted data shown this page includes evaluation board loss and connector loss approximately 0.5 dB /one side.

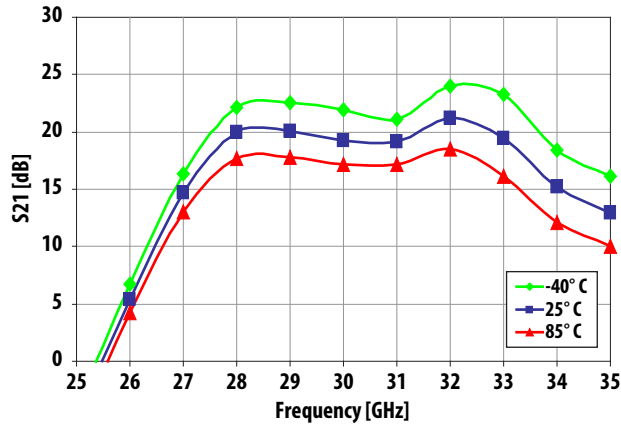


Figure 7. S_{21} (dB) Frequency Sweep over Temperature

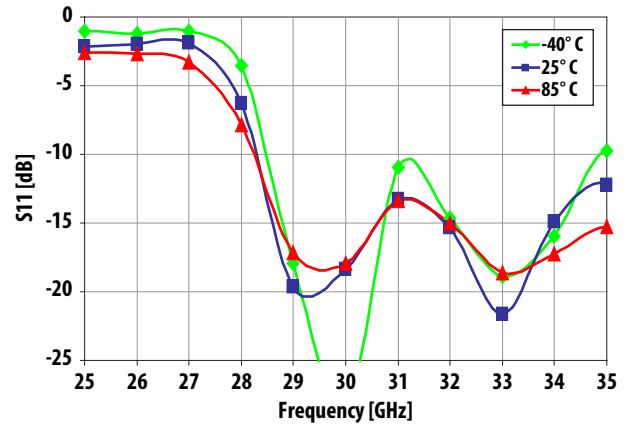


Figure 8. S_{11} (dB) Frequency Sweep over Temperature

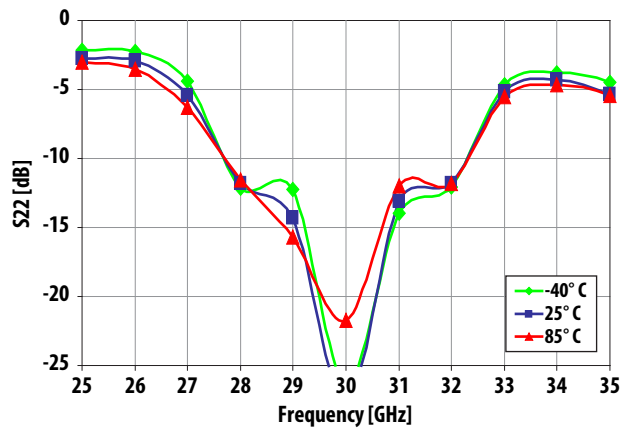


Figure 9. S_{22} (dB) Frequency Sweep over Temperature

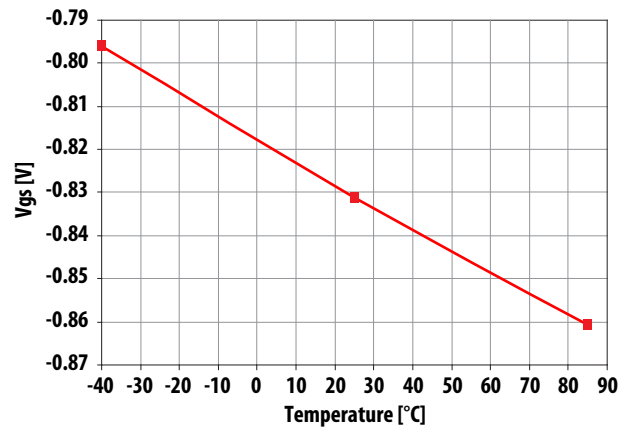


Figure 10. Typical V_{gs} for $I_{dq} = 0.7\text{ A}$ over Temperature

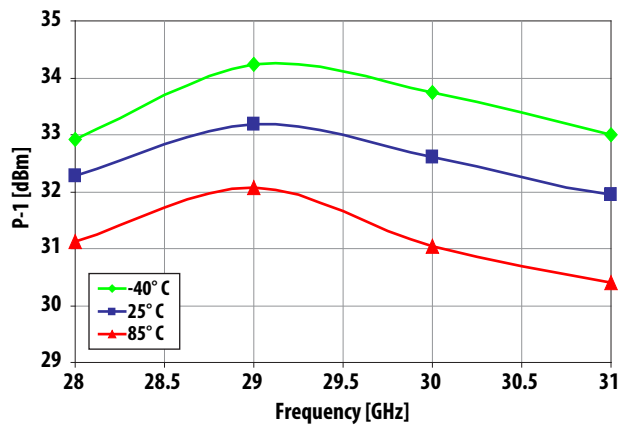


Figure 11. P_{1dB} (dBm) Frequency Sweep over Temperature

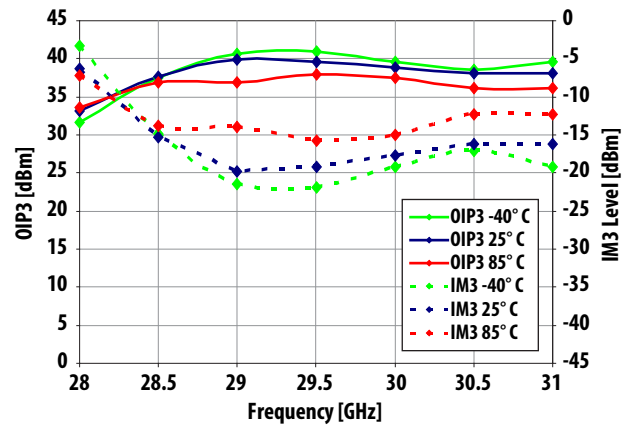


Figure 12. $OIP3$ (dBm) and $IM3$ level (dBm) Frequency Sweep over Temperature @ $P_o = 20\text{ dBm/tone}$

Selected Performance Plots Over Operating Supply Voltage Range

All data measured on a 2.4 mm connector based evaluation board at $T_A = 25^\circ\text{C}$, and $50\ \Omega$ at all ports. Plotted data shown this page includes evaluation board loss and connector loss approximately 0.5 dB/one side.

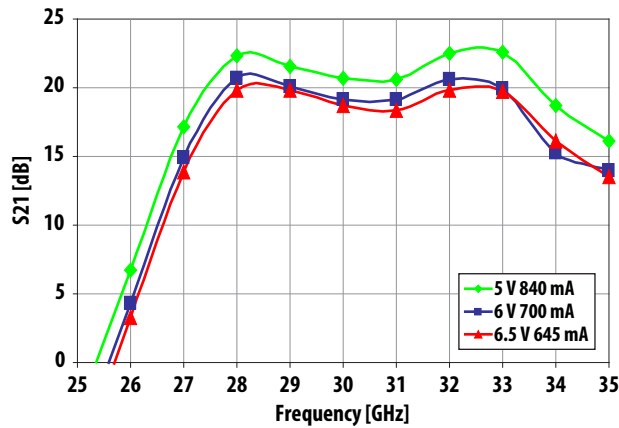


Figure 13. S_{21} (dB) Frequency Sweep

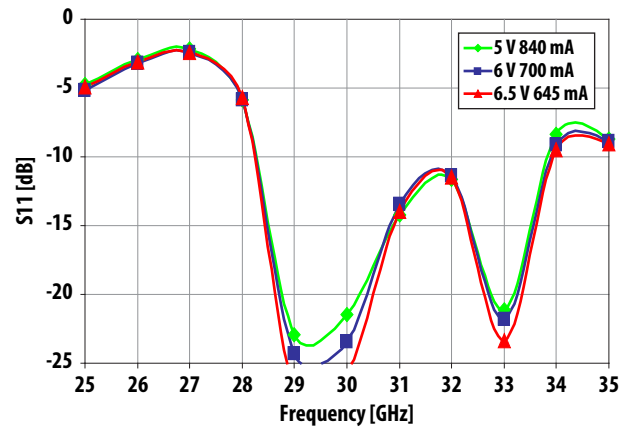


Figure 14. S_{11} (dB) Frequency Sweep

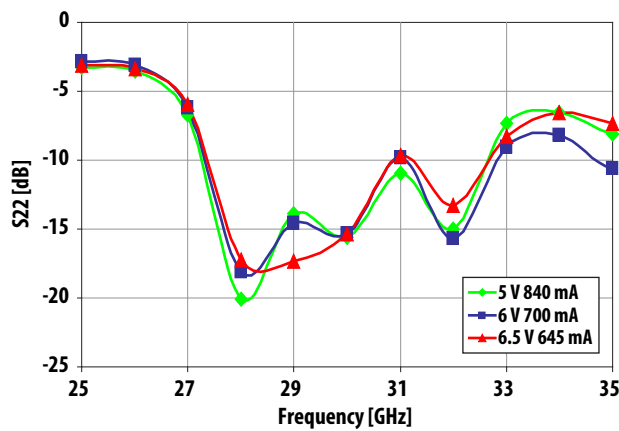


Figure 15. S_{22} (dB) Frequency Sweep

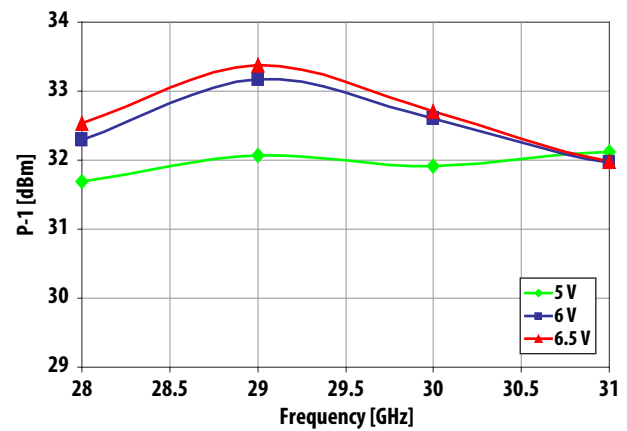


Figure 16. P_{1dB} (dBm) Frequency Sweep

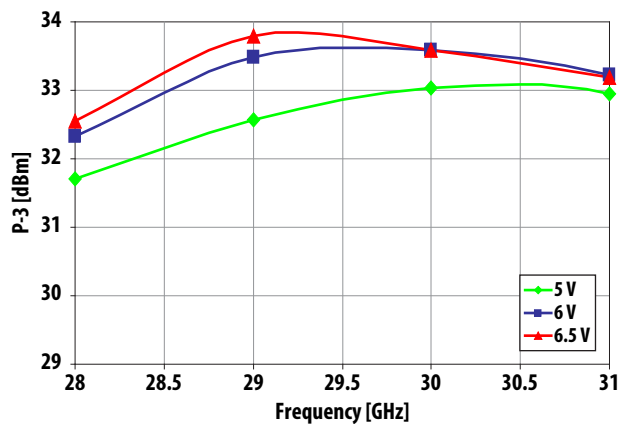


Figure 17. P_{3dB} (dBm) Frequency Sweep

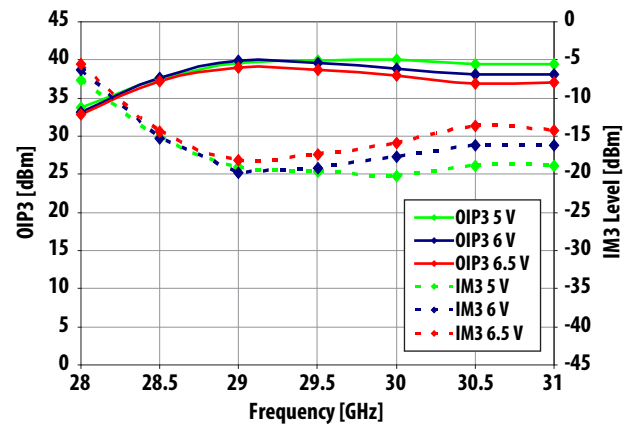


Figure 18. OIP3 and IM3 level vs. Frequency Sweep @ $P_o = 20\ \text{dVm/ton}$

Application Circuit

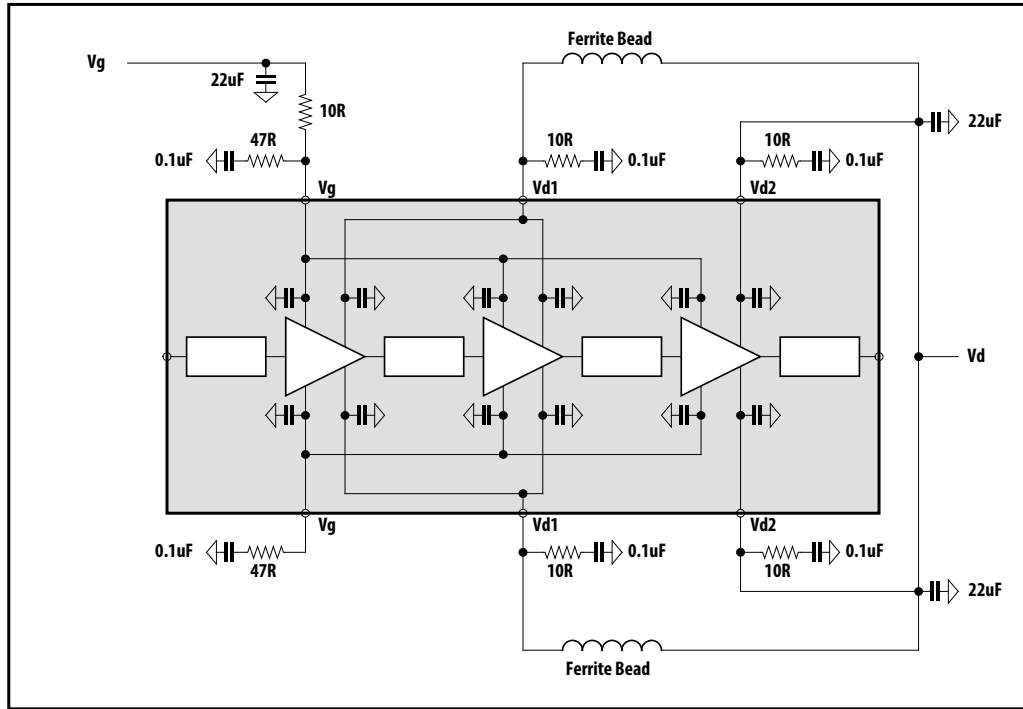


Figure 19. Application Circuit

Table 3. Typical Test Conditions:

PIN		
Vd1, 2	6V	Drain Supply Voltage
Idsq=Id1+Id2	700mA	Quiescent Drain Current
Vg	-0.83	Gate Supply Voltage

Table 4. Pin Description

Pin No.	Function
1	Vg
2	Vd1
3	Vd2
4	RF_OUT
5	Vd2
6	Vd1
7	Vg
8	RF_IN

Demo-board

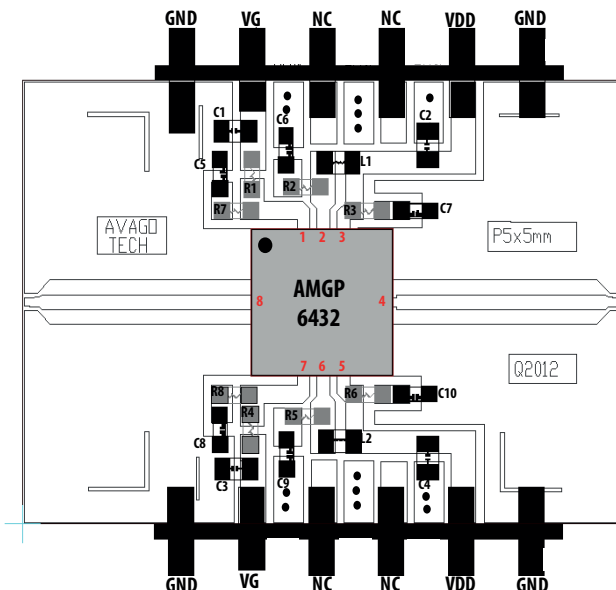


Figure 20. Demo-board

Table 5. Recommended Passive Components

Ref Designator	Value	Part Number
C1-C4	22uF	TDK C1608X5R0J226M DigiKey 445-8028-1-ND
C5-C10	0.1uF	0402 ANY
L1, L2	Ferrite Bead	Murata BLM18HG471SN1
R1-R6	10Ω	0402 ANY
R7, R8	47	0402 ANY

Bias Sequence:

- Apply Vg [Pin 1 or Pin 7] = -1.5V
- Apply Vdd [Pin 3 and Pin 5] = 0V
- Increase Vdd to 6V
- Increase Vg obtain Idsq = 0.7A
- Apply RF Input
- Turn off in reverse order

Reliability Data

Please contact Avago Technical and/or Customer supports for more detail www.avagotech.com

Package Dimension, PCB Layout and Tape and Reel information

Please refer to Avago Technologies Application Note 5521, AMxP-xxxx production Assembly Process (Land Pattern B).

Part Number Ordering Information

Part Number	Devices per Container	Container
AMGP-6432-BLKG	10	antistatic bag
AMGP-6432-TR1G	100	7" Reel
AMGP-6432-TR2G	500	7" Reel

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