



ALPHA & OMEGA
SEMICONDUCTOR

AO7405

P-Channel Enhancement Mode Field Effect Transistor



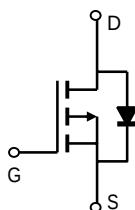
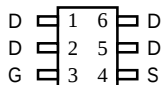
General Description

The AO7405 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge, and operation with gate voltages as low as 2.5V, in the small SOT363 footprint. It can be used for a wide variety of applications, including load switching, low current inverters and low current DC-DC converters. *Standard Product AO7405 is Pb-free (meets ROHS & Sony 259 specifications). AO7405L is a Green Product ordering option. AO7405 and AO7405L are electrically identical.*

Features

$V_{DS} (V) = -30V$
 $I_D = -1.6A$ ($V_{GS} = -10V$)
 $R_{DS(ON)} < 150m\Omega$ ($V_{GS} = -10V$)
 $R_{DS(ON)} < 200m\Omega$ ($V_{GS} = -4.5V$)
 $R_{DS(ON)} < 280m\Omega$ ($V_{GS} = -2.5V$)

SC-70-6
(SOT-363)
Top View



Absolute Maximum Ratings $T_A=25^\circ C$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current ^A	I_D	-1.6	A
		-1.3	
Pulsed Drain Current ^B	I_{DM}	-10	
Power Dissipation ^A	P_D	0.625	W
		0.4	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$R_{\theta JA}$	160	200	$^\circ C/W$
Maximum Junction-to-Ambient ^A		180	220	$^\circ C/W$
Maximum Junction-to-Lead ^C	$R_{\theta JL}$	130	160	$^\circ C/W$

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Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D=-250\mu\text{A}$, $V_{GS}=0\text{V}$	-30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=-24\text{V}$, $V_{GS}=0\text{V}$ $T_J=55^{\circ}\text{C}$			-1 -5	μA
I_{GSS}	Gate-Body leakage current	$V_{DS}=0\text{V}$, $V_{GS}=\pm 12\text{V}$			± 100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_D=-250\mu\text{A}$	-0.6	-1	-1.4	V
$I_{D(ON)}$	On state drain current	$V_{GS}=-4.5\text{V}$, $V_{DS}=-5\text{V}$	-10			A
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10\text{V}$, $I_D=-1.6\text{A}$ $T_J=125^{\circ}\text{C}$		115 195	150 240	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}$, $I_D=-1\text{A}$		135	200	$\text{m}\Omega$
		$V_{GS}=-2.5\text{V}$, $I_D=-1\text{A}$		190	280	$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{DS}=-5\text{V}$, $I_D=-1.6\text{A}$	3	4		S
V_{SD}	Diode Forward Voltage	$I_S=-1\text{A}$, $V_{GS}=0\text{V}$		-0.85	-1	V
I_S	Maximum Body-Diode Continuous Current				-0.5	A
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	$V_{GS}=0\text{V}$, $V_{DS}=-15\text{V}$, $f=1\text{MHz}$		409		pF
C_{oss}	Output Capacitance			55		pF
C_{rss}	Reverse Transfer Capacitance			42		pF
R_g	Gate resistance	$V_{GS}=0\text{V}$, $V_{DS}=0\text{V}$, $f=1\text{MHz}$		12		Ω
SWITCHING PARAMETERS						
Q_g	Total Gate Charge	$V_{GS}=-4.5\text{V}$, $V_{DS}=-15\text{V}$, $I_D=-1\text{A}$		5.06		nC
Q_{gs}	Gate Source Charge			0.72		nC
Q_{gd}	Gate Drain Charge			1.58		nC
$t_{D(on)}$	Turn-On DelayTime	$V_{GS}=-10\text{V}$, $V_{DS}=-15\text{V}$, $R_L=15\Omega$, $R_{GEN}=3\Omega$		6.2		ns
t_r	Turn-On Rise Time			3.2		ns
$t_{D(off)}$	Turn-Off DelayTime			41.2		ns
t_f	Turn-Off Fall Time			14.5		ns
t_{rr}	Body Diode Reverse Recovery Time	$I_F=-1\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$		13.2		ns
Q_{rr}	Body Diode Reverse Recovery Charge	$I_F=-1\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$		5.4		nC

A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The value in any given application depends on the user's specific board design. The current rating is based on the $t \leq 10\text{s}$ thermal resistance rating.

B: Repetitive rating, pulse width limited by junction temperature.

C: The $R_{\theta JA}$ is the sum of the thermal impedance from junction to lead $R_{\theta JL}$ and lead to ambient.

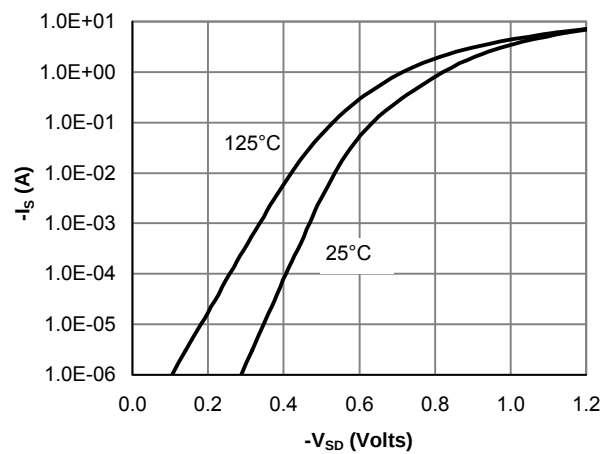
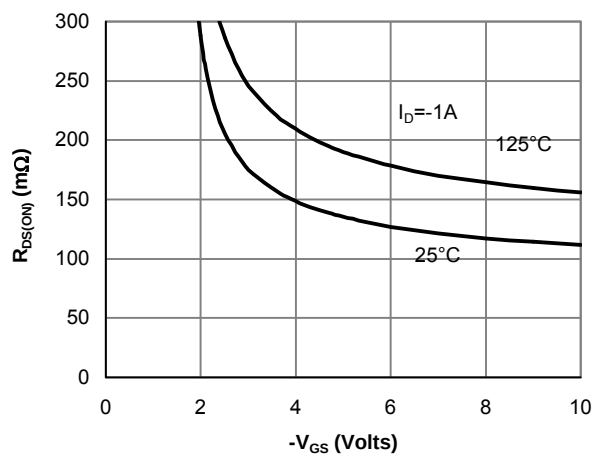
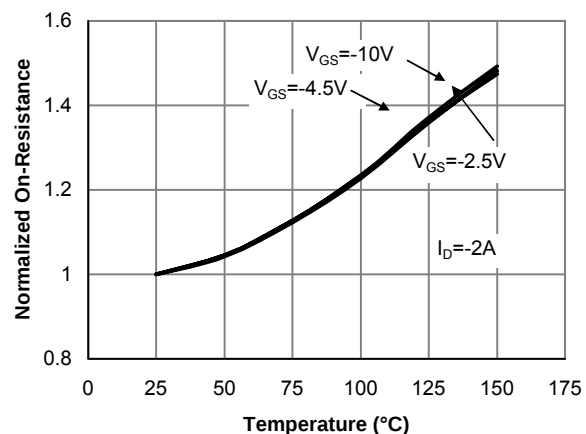
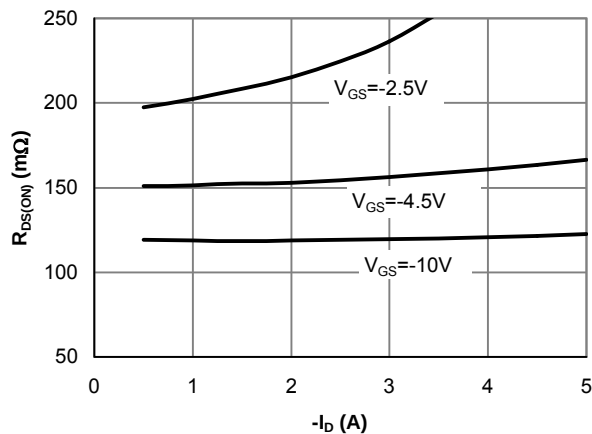
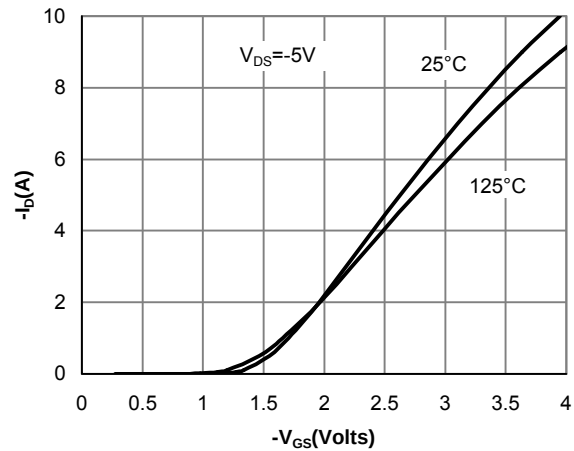
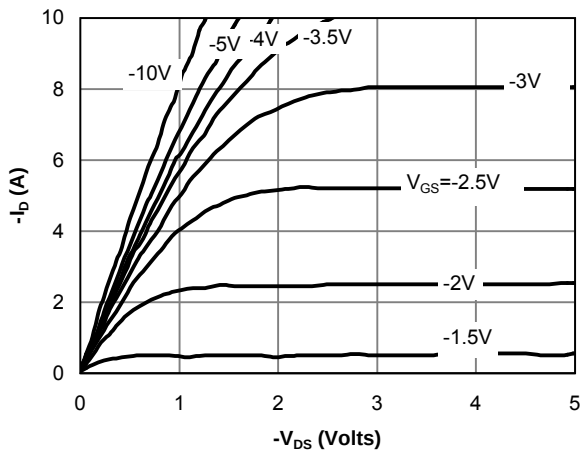
D: The static characteristics in Figures 1 to 6, 12, 14 are obtained using 80 μs pulses, duty cycle 0.5% max.

E: These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The SOA curve provides a single pulse rating.

Rev 2 : July 2005

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS



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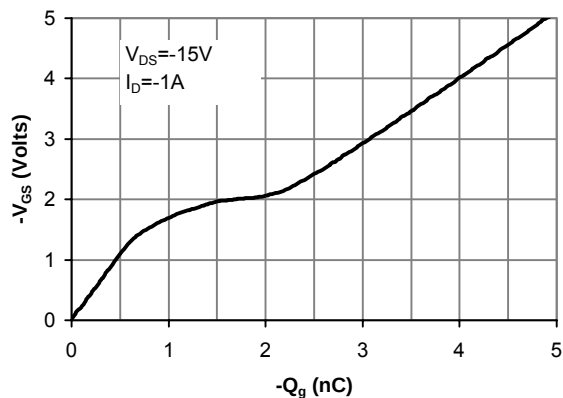


Figure 7: Gate-Charge Characteristics

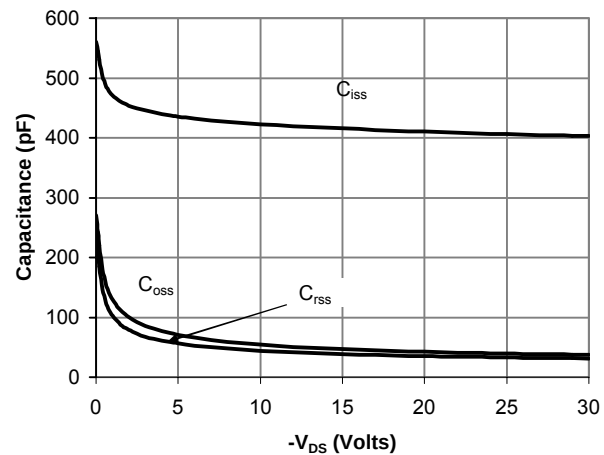


Figure 8: Capacitance Characteristics

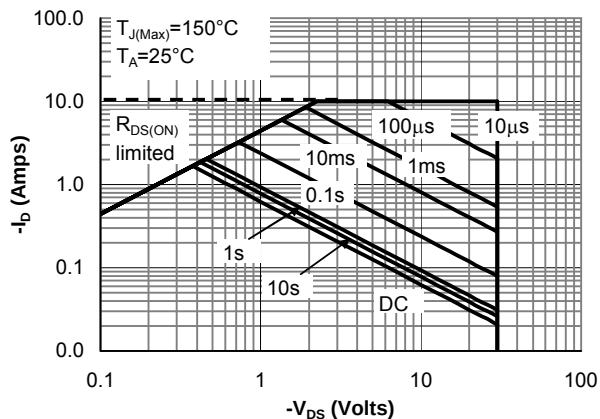


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

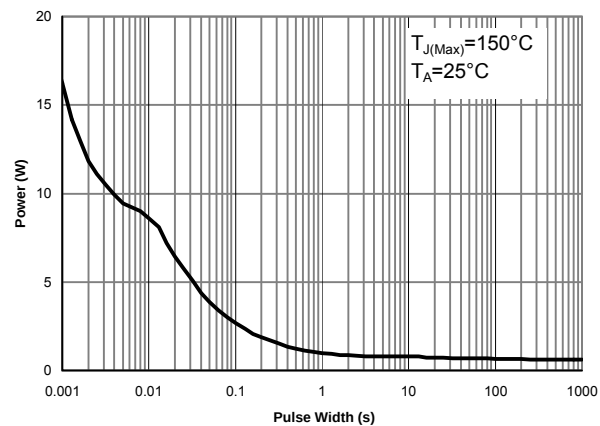


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

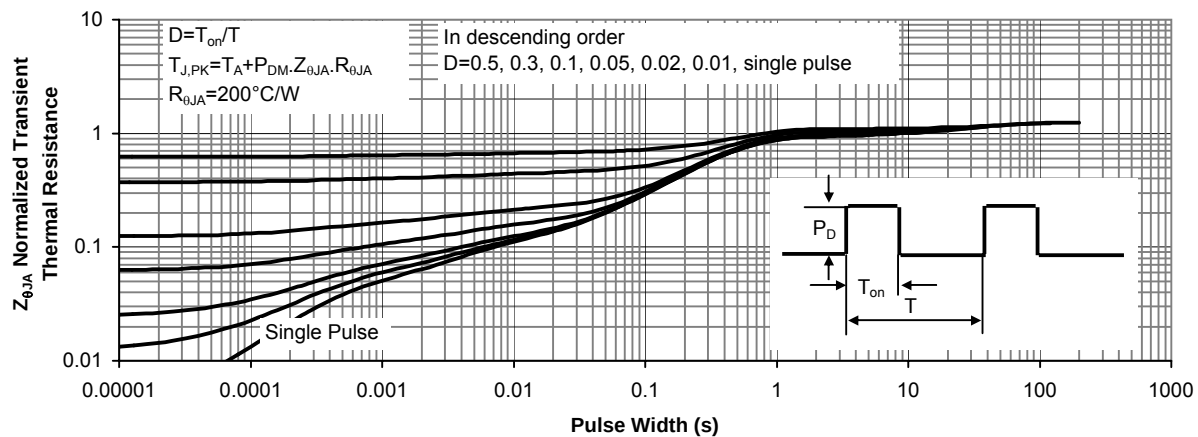


Figure 11: Normalized Maximum Transient Thermal Impedance