ADVANCED POWER TECHNOLOGY®

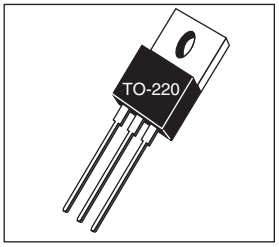
600V

APT30GT60KR

APT30GT60KRG*

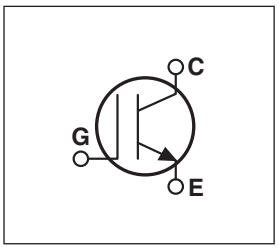
***G Denotes RoHS Compliant, Pb Free Terminal Finish.**

Thunderbolt IGBT®



The Thunderblot IGBT® is a new generation of high voltage power IGBTs. Using Non- Punch Through Technology, the Thunderblot IGBT® offers superior ruggedness and ultrafast switching speed.

- **Low Forward Voltage Drop**
- **High Freq. Switching to 100KHz**
- **Low Tail Current**
- **Ultra Low Leakage Current**
- **RBSOA and SCSOA Rated**



MAXIMUM RATINGS

All Ratings: T_C = 25°C unless otherwise specified.

Symbol	Parameter	APT30GT60KR(G)	UNIT
V_{CES}	Collector-Emitter Voltage	600	Volts
V_{GE}	Gate-Emitter Voltage	±30	
I_{C1}	Continuous Collector Current @ T_C = 25°C	64	Amps
I_{C2}	Continuous Collector Current @ T_C = 110°C	30	
I_{CM}	Pulsed Collector Current ①	110	
SSOA	Switching Safe Operating Area @ T_J = 150°C	110A @ 600V	
P_D	Total Power Dissipation	250	Watts
T_J,T_{STG}	Operating and Storage Junction Temperature Range	-55 to 150	°C
T_L	Max. Lead Temp. for Soldering: 0.063" from Case for 10 Sec.	300	

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	Units
$V_{(BR)CES}$	Collector-Emitter Breakdown Voltage (V_{GE} = 0V, I_C = 250µA)	600			Volts
$V_{GE(TH)}$	Gate Threshold Voltage (V_{CE} = V_{GE}, I_C = 700µA, T_j = 25°C)	3	4	5	
$V_{CE(ON)}$	Collector-Emitter On Voltage (V_{GE} = 15V, I_C = 30A, T_j = 25°C)	1.6	2.0	2.5	
	Collector-Emitter On Voltage (V_{GE} = 15V, I_C = 30A, T_j = 125°C)		2.8		
I_{CES}	Collector Cut-off Current (V_{CE} = 600V, V_{GE} = 0V, T_j = 25°C) ②			50	µA
	Collector Cut-off Current (V_{CE} = 600V, V_{GE} = 0V, T_j = 125°C) ②			1000	
I_{GES}	Gate-Emitter Leakage Current (V_{GE} = ±20V)			±100	nA

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

APT Website - http://www.advancedpower.com

052-6204 Rev F 6 - 2008

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DYNAMIC CHARACTERISTICS

APT30GT60KR(G)

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C_{ies}	Input Capacitance	**Capacitance**		1600		pF
C_{oes}	Output Capacitance	V_{GE} = 0V, V_{CE} = 25V		150		
C_{res}	Reverse Transfer Capacitance	f = 1 MHz		92		
V_{GEP}	Gate-to-Emitter Plateau Voltage	Gate Charge		7.5		V
Q_g	Total Gate Charge ③	V_{GE} = 15V		145		nC
Q_{ge}	Gate-Emitter Charge	V_{CE} = 300V		10		
Q_{gc}	Gate-Collector ("Miller") Charge	I_C = 30A		60		
SSOA	Switching Safe Operating Area	T_J = 150°C, R_G = 10Ω, V_{GE} = 15V, L = 100µH, V_{CE} = 600V	110			A
$t_{d(on)}$	Turn-on Delay Time	**Inductive Switching (25°C)**		12		ns
t_r	Current Rise Time	V_{CC} = 400V		20		
$t_{d(off)}$	Turn-off Delay Time	V_{GE} = 15V		225		
t_f	Current Fall Time	I_C = 30A		80		
E_{on1}	Turn-on Switching Energy ④	R_G = 10Ω		525		µJ
E_{on2}	Turn-on Switching Energy (Diode) ⑤	T_J = +25°C		605		
E_{off}	Turn-off Switching Energy ⑥			600		
$t_{d(on)}$	Turn-on Delay Time	**Inductive Switching (125°C)**		12		ns
t_r	Current Rise Time	V_{CC} = 400V		20		
$t_{d(off)}$	Turn-off Delay Time	V_{GE} = 15V		245		
t_f	Current Fall Time	I_C = 30A		100		
E_{on1}	Turn-on Switching Energy ④	R_G = 10Ω		570		µJ
E_{on2}	Turn-on Switching Energy (Diode) ⑤	T_J = +125°C		965		
E_{off}	Turn-off Switching Energy ⑥			830		

THERMAL AND MECHANICAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to Case **(IGBT)**			.50	°C/W
$R_{\theta JC}$	Junction to Case **(DIODE)**			N/A	
W_T	Package Weight	1.9			gm

① Repetitive Rating: Pulse width limited by maximum junction temperature.

② For Combi devices, I_{ces} includes both IGBT and FRED leakages

③ See MIL-STD-750 Method 3471.

④ E_{on1} is the clamped inductive turn-on energy of the IGBT only, without the effect of a commutating diode reverse recovery current adding to the IGBT turn-on loss. Tested in inductive switching test circuit shown in figure 21, but with a Silicon Carbide diode.

⑤ E_{on2} is the clamped inductive turn-on energy that includes a commutating diode reverse recovery current in the IGBT turn-on switching loss. (See Figures 21, 22.)

⑥ E_{off} is the clamped inductive turn-off energy measured in accordance with JEDEC standard JESD24-1. (See Figures 21, 23.)

APT Reserves the right to change, without notice, the specifications and information contained herein.

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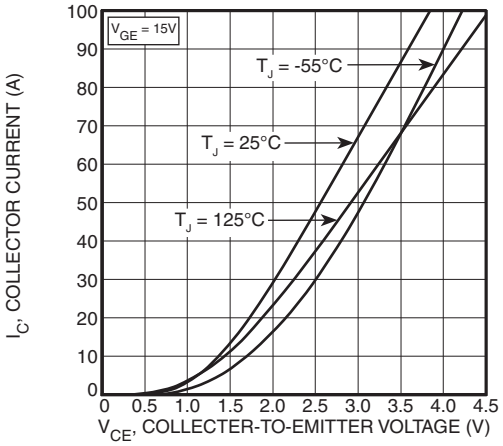

FIGURE 1, Output Characteristics(T_J = 25°C)

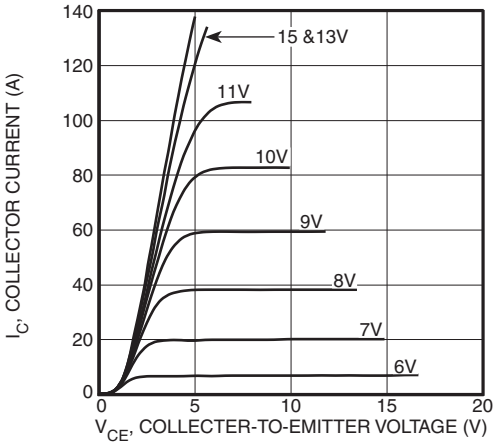

FIGURE 2, Output Characteristics (T_J = 125°C)

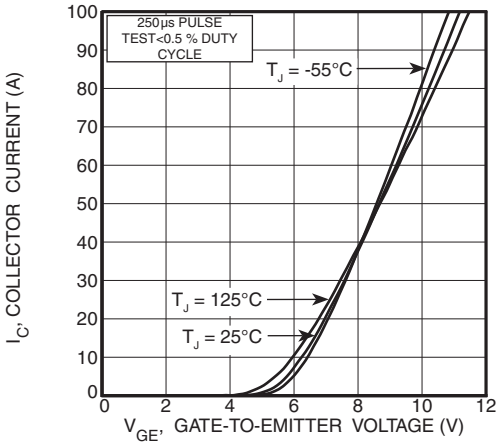

FIGURE 3, Transfer Characteristics

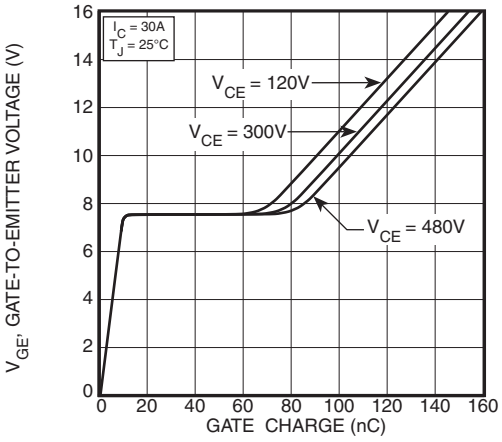

FIGURE 4, Gate Charge

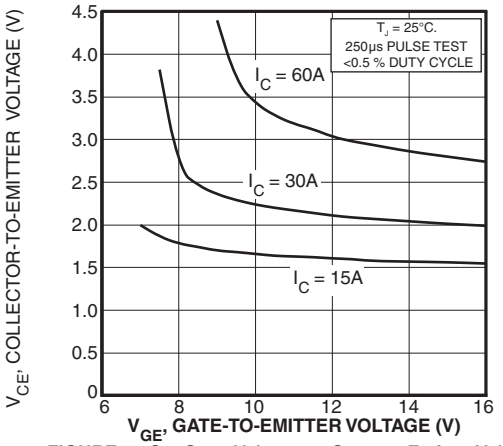

FIGURE 5, On State Voltage vs Gate-to- Emitter Voltage

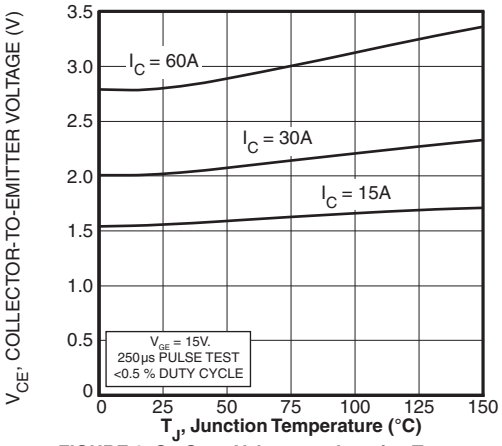

FIGURE 6, On State Voltage vs Junction Temperature

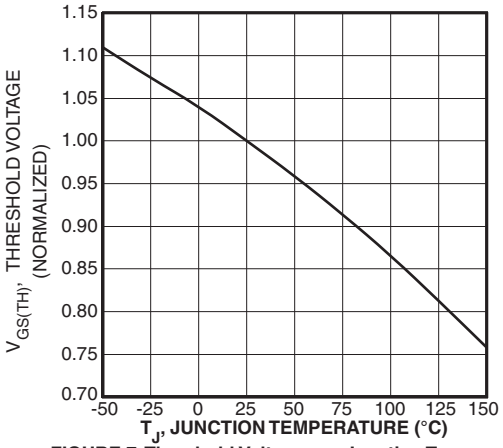

FIGURE 7, Threshold Voltage vs. Junction Temperature

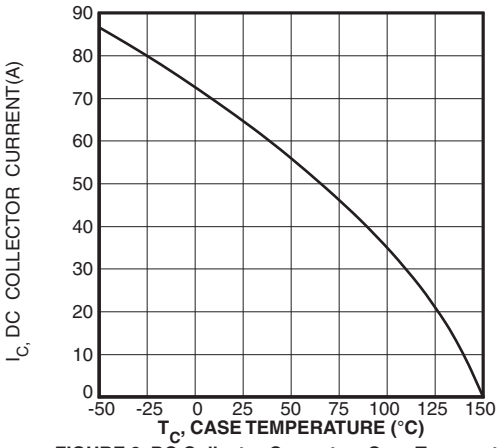

FIGURE 8, DC Collector Current vs Case Temperature

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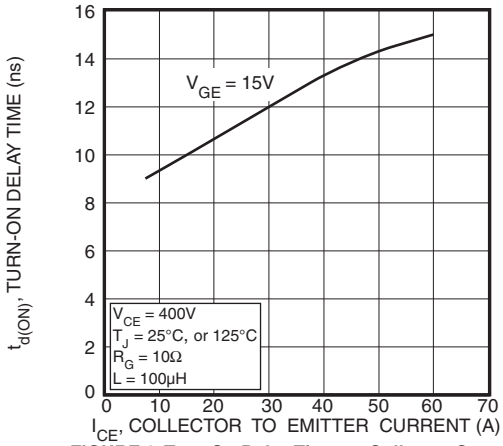

FIGURE 9, Turn-On Delay Time vs Collector Current

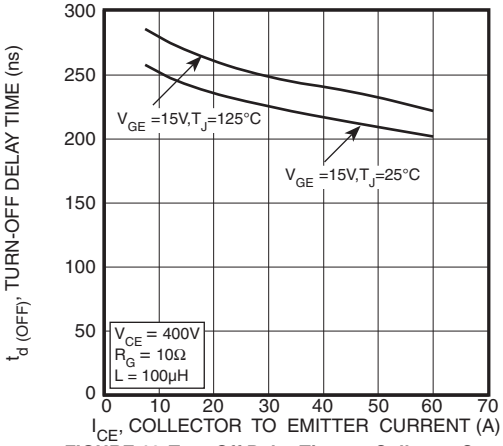

FIGURE 10, Turn-Off Delay Time vs Collector Current

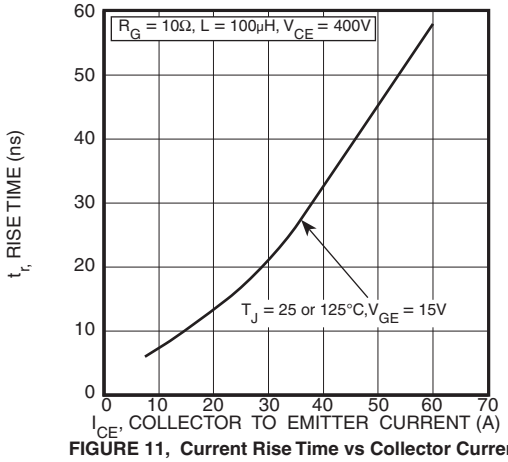

FIGURE 11, Current Rise Time vs Collector Current

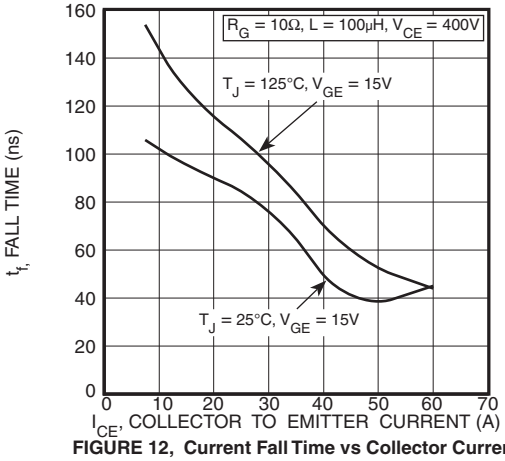

FIGURE 12, Current Fall Time vs Collector Current

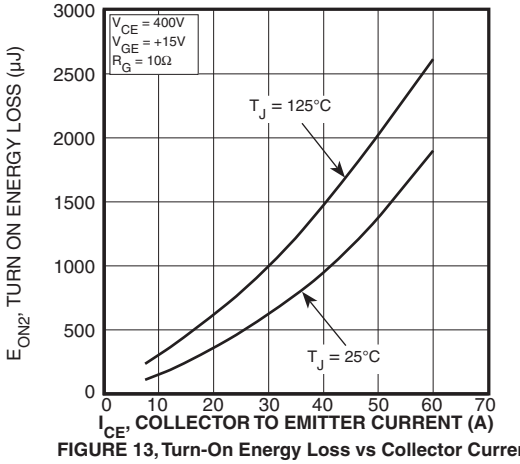

FIGURE 13, Turn-On Energy Loss vs Collector Current

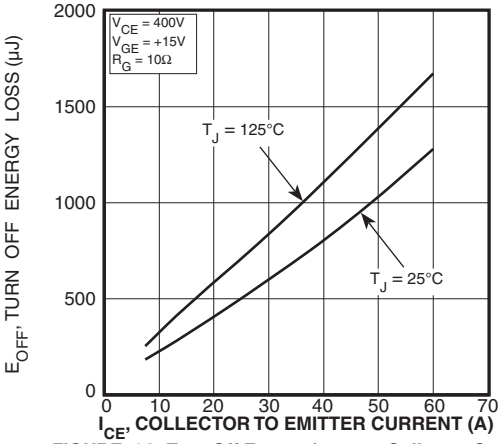

FIGURE 14, Turn Off Energy Loss vs Collector Current

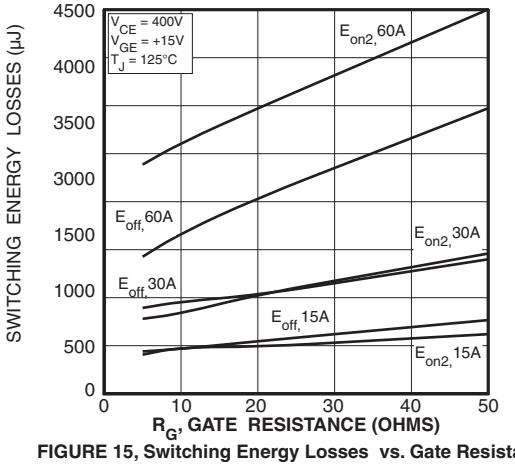

FIGURE 15, Switching Energy Losses vs. Gate Resistance

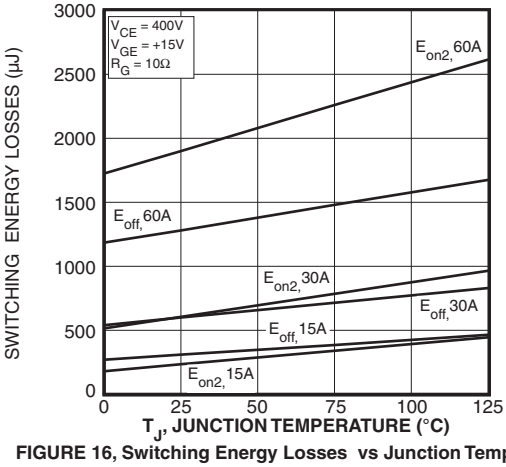

FIGURE 16, Switching Energy Losses vs Junction Temperature

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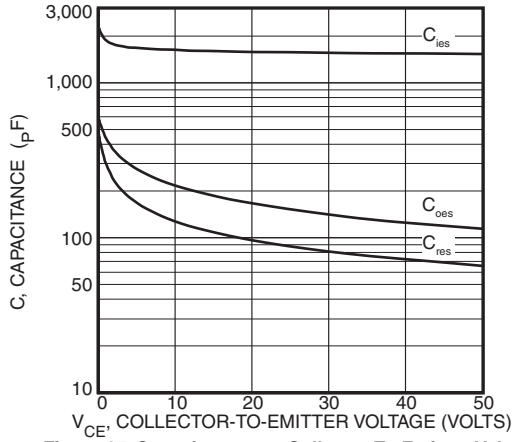

Figure 17, Capacitance vs Collector-To-Emitter Voltage

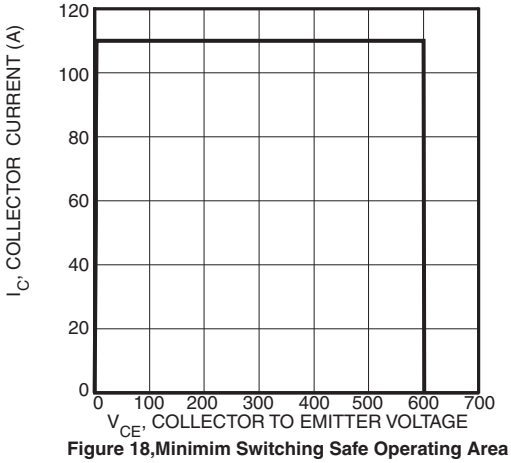

Figure 18,Minimim Switching Safe Operating Area

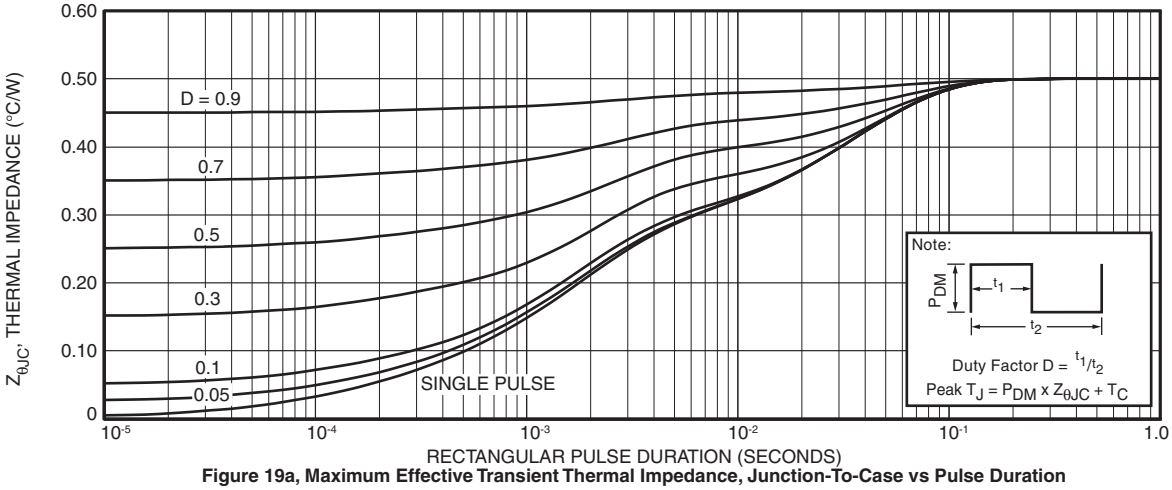

Figure 19a, Maximum Effective Transient Thermal Impedance, Junction-To-Case vs Pulse Duration

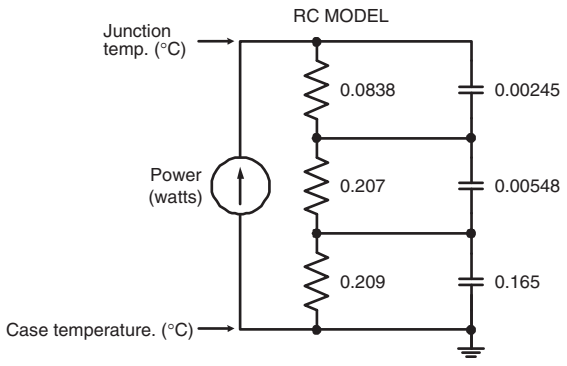

FIGURE 19b, TRANSIENT THERMAL IMPEDANCE MODEL

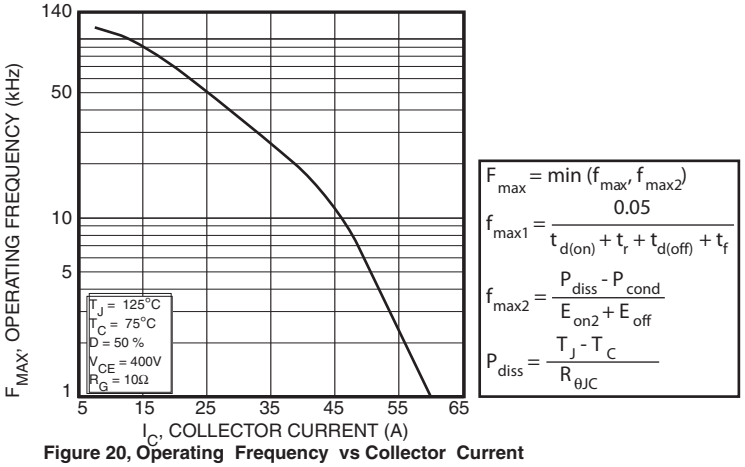

Figure 20, Operating Frequency vs Collector Current

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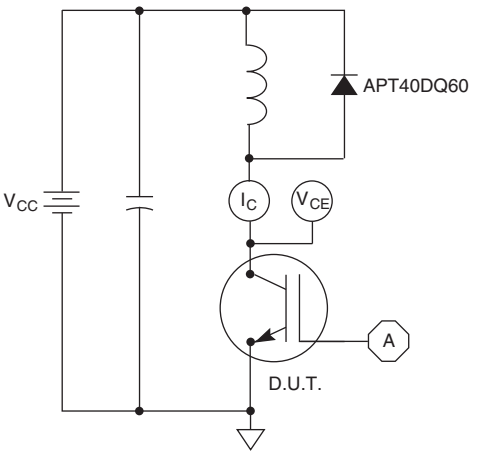


Figure 21, Inductive Switching Test Circuit

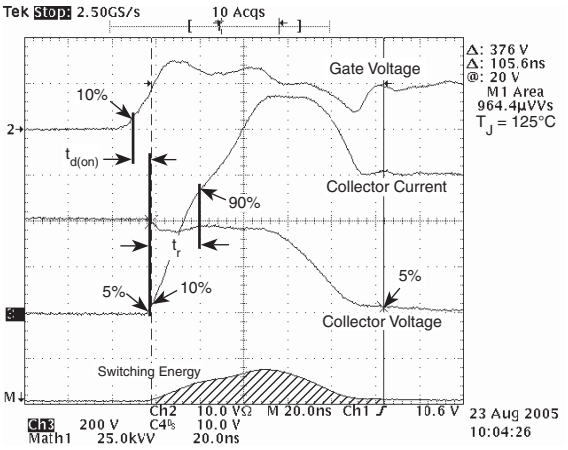


Figure 22, Turn-on Switching Waveforms and Definitions

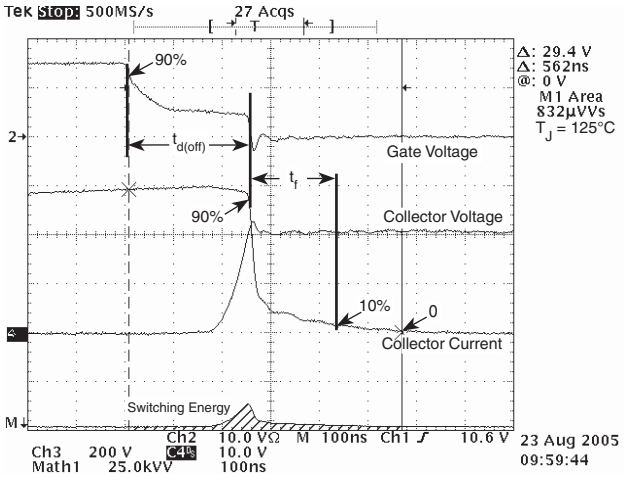
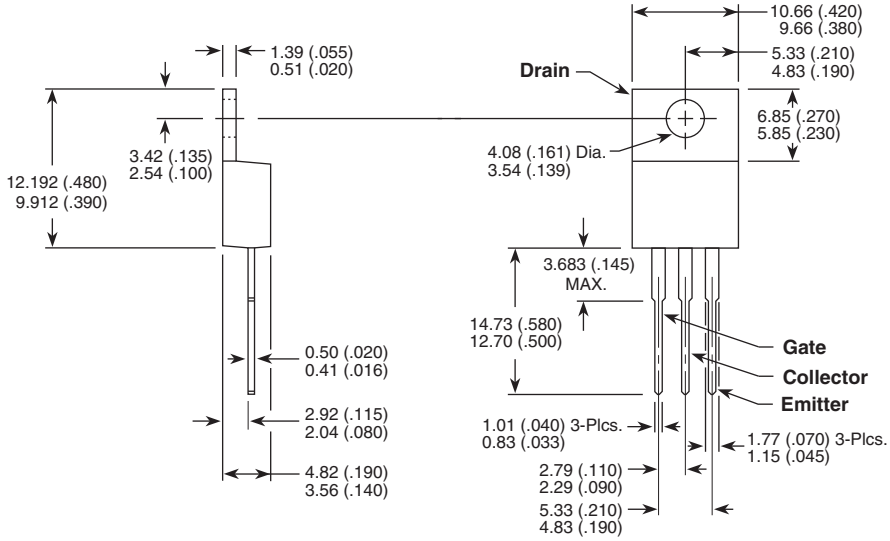


Figure 23, Turn-off Switching Waveforms and Definitions

TO-220 (K) Package Outline

e3 100% Sn



Dimensions in Millimeters and (Inches)

APT's products are covered by one or more of U.S.patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. US and Foreign patents pending. All Rights Reserved.

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