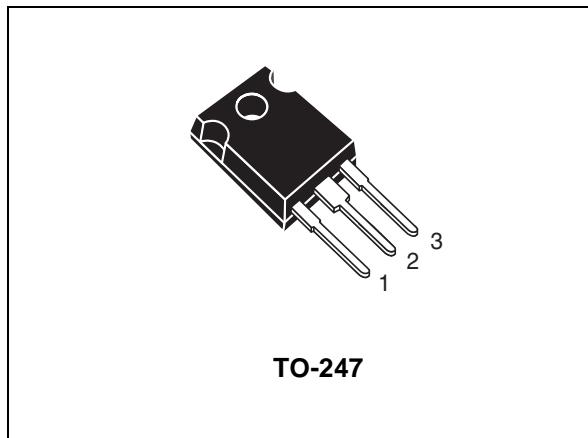
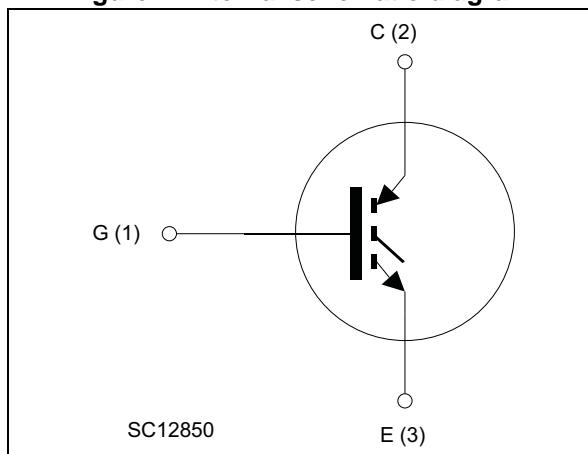


**Trench gate field-stop IGBT, H series
1200 V, 25 A high speed**
Datasheet - production data

Figure 1. Internal schematic diagram

Features

- Maximum junction temperature: $T_J = 175 \text{ }^{\circ}\text{C}$
- High speed switching series
- Minimized tail current
- $V_{CE(\text{sat})} = 2.1 \text{ V (typ.)} @ I_C = 25 \text{ A}$
- 5 μs minimum short circuit withstand time at $T_J=150 \text{ }^{\circ}\text{C}$
- Tight parameters distribution
- Safe paralleling
- Low thermal resistance
- Lead free package

Applications

- Uninterruptible power supply
- Welding machines
- Photovoltaic inverters
- Power factor correction
- High frequency converters

Description

This device is an IGBT developed using an advanced proprietary trench gate field stop structure. The device is part of the improved H series of IGBTs, which represent an optimum compromise between conduction and switching losses to maximize the efficiency of high frequency converters. Furthermore, a slightly positive $V_{CE(\text{sat})}$ temperature coefficient and very tight parameter distribution result in safer paralleling operation.

Table 1. Device summary

Order codes	Marking	Package	Packaging
STGW25H120F2	GW25H120F2	TO-247	Tube

Contents

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2.1	Electrical characteristics (curves)	6
3	Test circuits	11
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1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{CES}	Collector-emitter voltage ($V_{GE} = 0$)	1200	V
I_C	Continuous collector current at $T_C = 25^\circ\text{C}$	50	A
I_C	Continuous collector current at $T_C = 100^\circ\text{C}$	25	A
$I_{CP}^{(1)}$	Pulsed collector current	100	A
V_{GE}	Gate-emitter voltage	± 20	V
P_{TOT}	Total dissipation at $T_C = 25^\circ\text{C}$	375	W
T_{STG}	Storage temperature range	-55 to 150	$^\circ\text{C}$
T_J	Operating junction temperature	-55 to 175	$^\circ\text{C}$

1. Pulse width limited by maximum junction temperature

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R_{thJC}	Thermal resistance junction-case	0.4	$^\circ\text{C}/\text{W}$
R_{thJA}	Thermal resistance junction-ambient	50	$^\circ\text{C}/\text{W}$

2 Electrical characteristics

$T_J = 25^\circ\text{C}$ unless otherwise specified.

Table 4. Static characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{(\text{BR})\text{CES}}$	Collector-emitter breakdown voltage ($V_{\text{GE}} = 0$)	$I_C = 2 \text{ mA}$	1200			V
$V_{\text{CE}(\text{sat})}$	Collector-emitter saturation voltage	$V_{\text{GE}} = 15 \text{ V}, I_C = 25 \text{ A}$		2.1	2.6	V
		$V_{\text{GE}} = 15 \text{ V}, I_C = 25 \text{ A}$ $T_J = 125^\circ\text{C}$		2.4		
		$V_{\text{GE}} = 15 \text{ V}, I_C = 25 \text{ A}$ $T_J = 175^\circ\text{C}$		2.5		
$V_{\text{GE}(\text{th})}$	Gate threshold voltage	$V_{\text{CE}} = V_{\text{GE}}, I_C = 1 \text{ mA}$	5	6	7	V
I_{CES}	Collector cut-off current ($V_{\text{GE}} = 0$)	$V_{\text{CE}} = 1200 \text{ V}$			25	μA
I_{GES}	Gate-emitter leakage current ($V_{\text{CE}} = 0$)	$V_{\text{GE}} = \pm 20 \text{ V}$			250	nA

Table 5. Dynamic characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
C_{ies}	Input capacitance	$V_{\text{CE}} = 25 \text{ V}, f = 1 \text{ MHz},$ $V_{\text{GE}} = 0$	-	2010	-	pF
C_{oes}	Output capacitance		-	146	-	pF
C_{res}	Reverse transfer capacitance		-	49	-	pF
Q_g	Total gate charge	$V_{\text{CC}} = 960 \text{ V}, I_C = 25 \text{ A},$ $V_{\text{GE}} = 15 \text{ V}$, see Figure 23	-	100	-	nC
Q_{ge}	Gate-emitter charge		-	11	-	nC
Q_{gc}	Gate-collector charge		-	52	-	nC

Table 6. Switching characteristics (inductive load)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on delay time	$V_{CE} = 600 \text{ V}, I_C = 25 \text{ A}, R_G = 10 \Omega, V_{GE} = 15 \text{ V},$ see Figure 22	-	29	-	ns
t_r	Current rise time		-	12	-	ns
$(di/dt)_{on}$	Turn-on current slope		-	1774	-	A/μs
$t_{d(off)}$	Turn-off delay time		-	130	-	ns
t_f	Current fall time		-	106	-	ns
$E_{on}^{(1)}$	Turn-on switching losses		-	0.6	-	mJ
$E_{off}^{(2)}$	Turn-off switching losses		-	0.7	-	mJ
E_{ts}	Total switching losses		-	1.3	-	mJ
$t_{d(on)}$	Turn-on delay time	$V_{CE} = 600 \text{ V}, I_C = 25 \text{ A}, R_G = 10 \Omega, V_{GE} = 15 \text{ V}, T_J = 175 \text{ }^\circ\text{C}$, see Figure 22	-	27.5	-	ns
t_r	Current rise time		-	13.5	-	ns
$(di/dt)_{on}$	Turn-on current slope		-	1522	-	A/μs
$t_{d(off)}$	Turn-off delay time		-	139	-	ns
t_f	Current fall time		-	200	-	ns
$E_{on}^{(1)}$	Turn-on switching losses		-	1.05	-	mJ
$E_{off}^{(2)}$	Turn-off switching losses		-	1.65	-	mJ
E_{ts}	Total switching losses		-	2.7	-	mJ
t_{sc}	Short-circuit withstand time	$V_{CE} = 600 \text{ V}, V_{GE} = 15 \text{ V}, T_J = 150 \text{ }^\circ\text{C}$	5		-	μJ

1. Energy losses include reverse recovery of the external diode. The diode is the same of the co-packed STGW25H120DF2
2. Turn-off losses include also the tail of the collector current.

2.1 Electrical characteristics (curves)

Figure 2. Power dissipation vs. case temperature

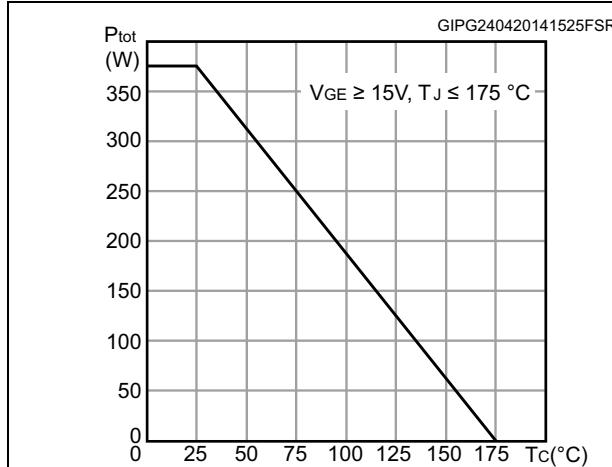


Figure 3. Collector current vs. case temperature

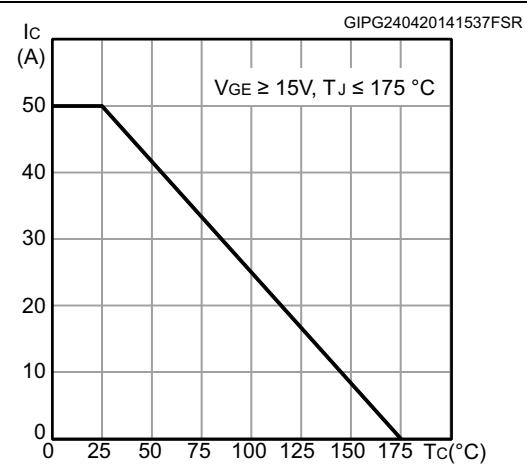


Figure 4. Output characteristics ($T_J = 25^{\circ}C$)

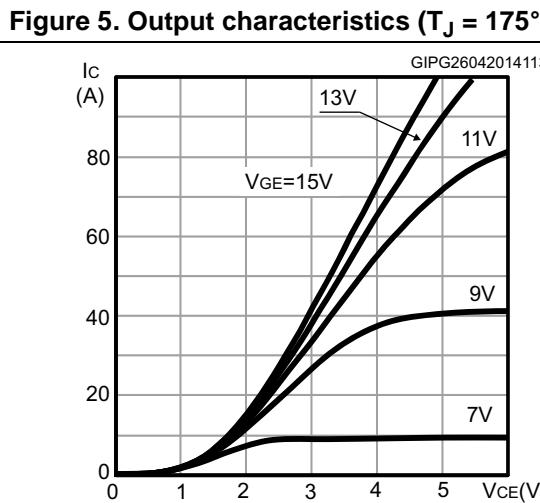
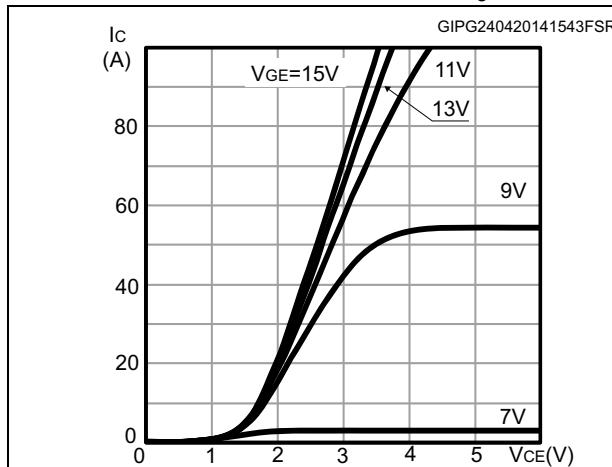


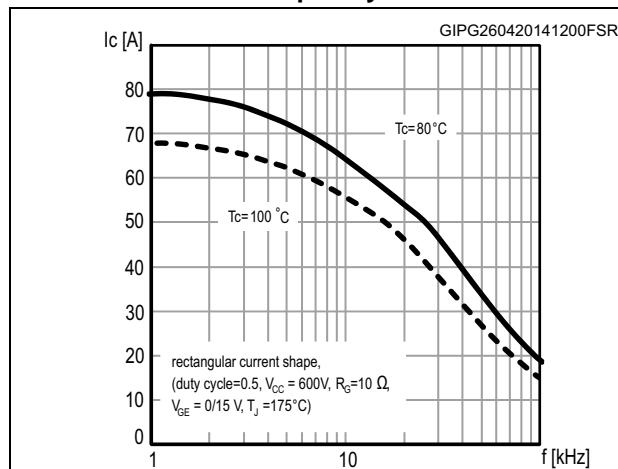
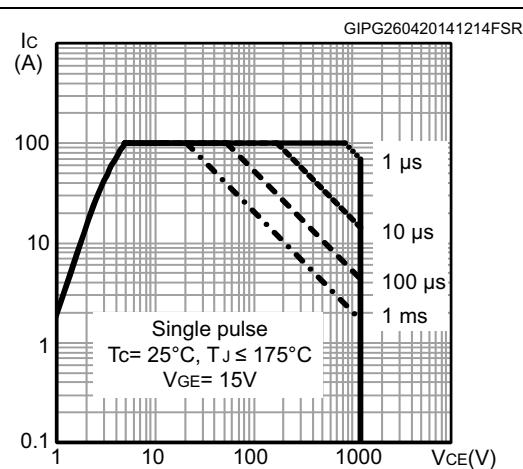
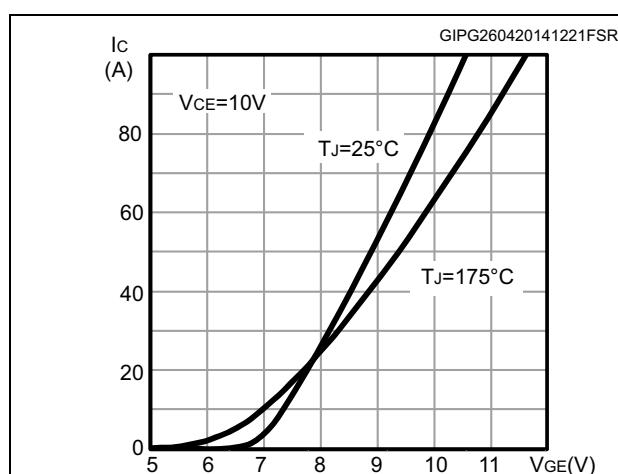
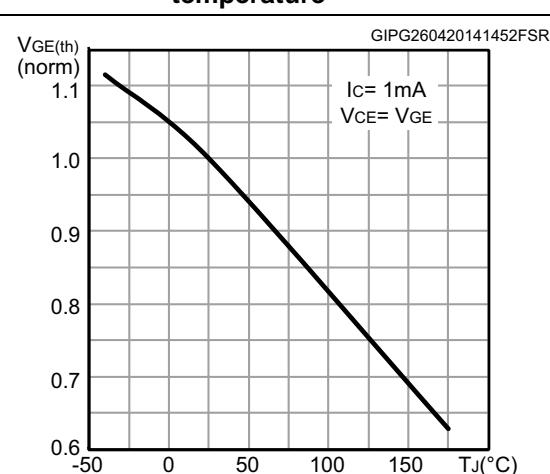
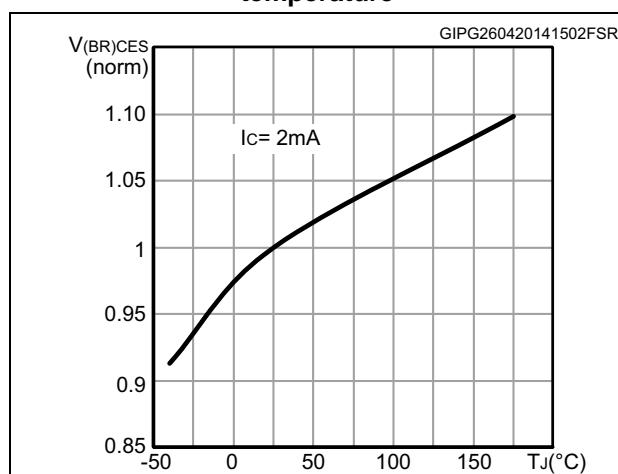
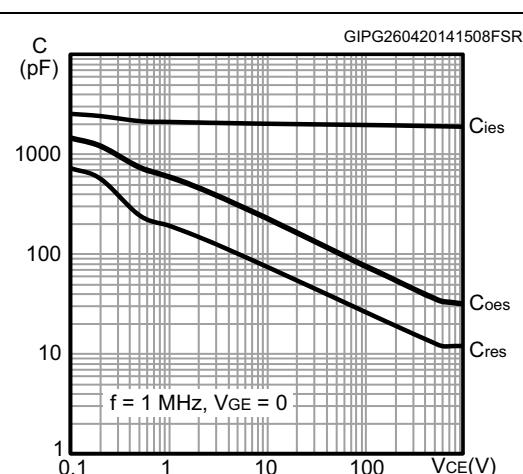
Figure 8. Collector current vs. switching frequency**Figure 9. Forward bias safe operating area****Figure 10. Transfer characteristics****Figure 11. Normalized VGE(th) vs junction temperature****Figure 12. Normalized V(BR)CES vs. junction temperature****Figure 13. Capacitance variation**

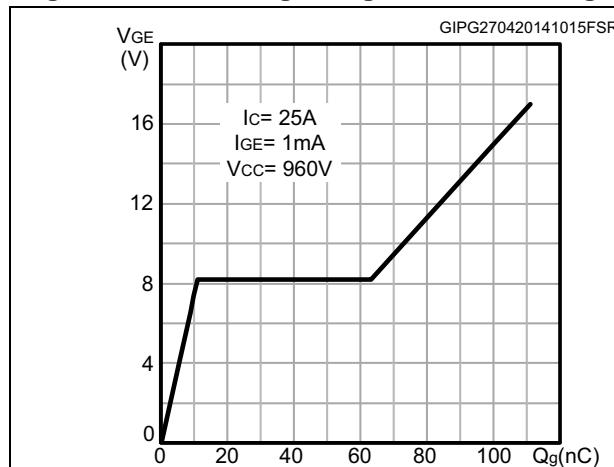
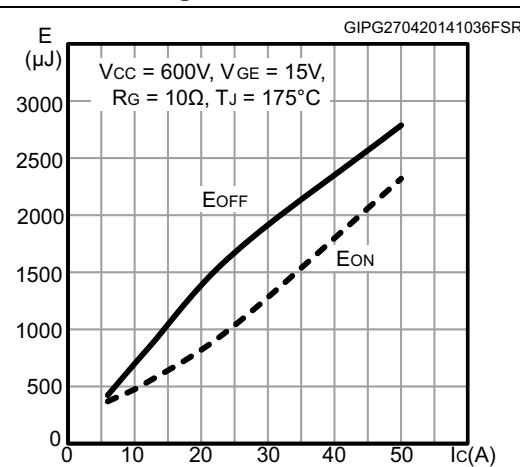
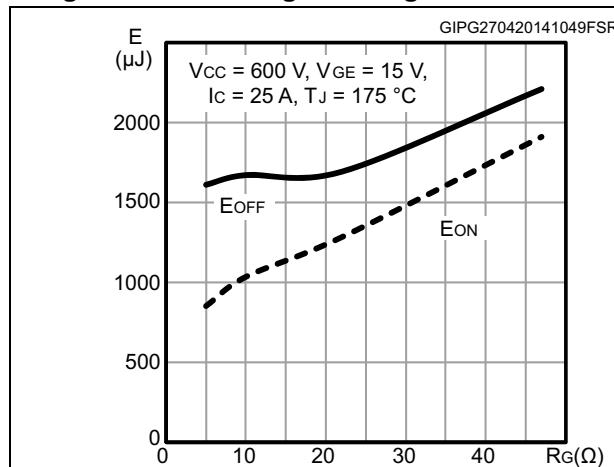
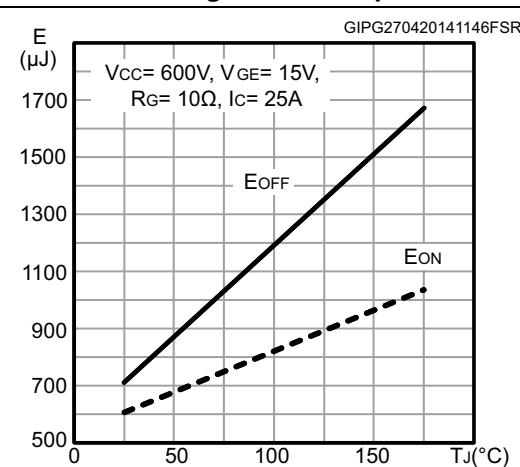
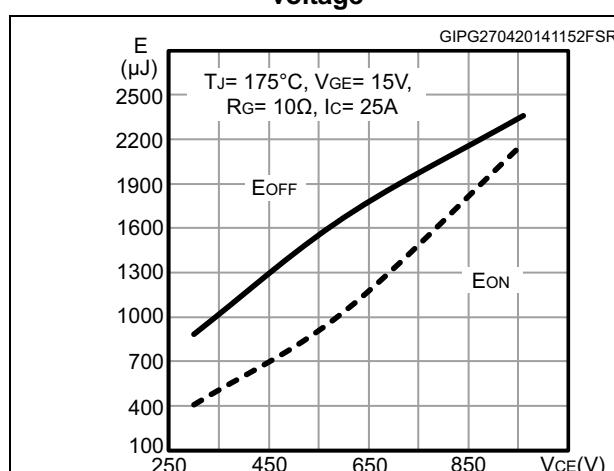
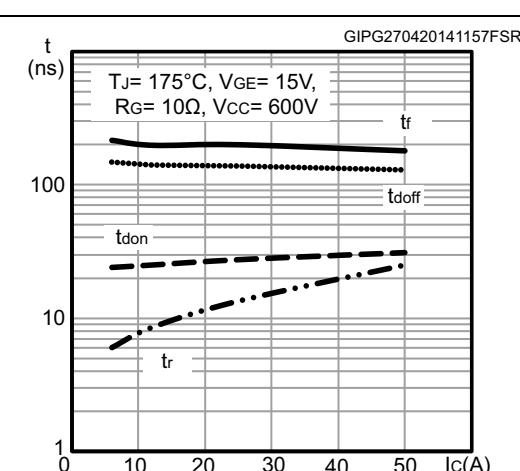
Figure 14. Gate charge vs. gate-emitter voltage**Figure 15. Switching loss vs collector current****Figure 16. Switching loss vs gate resistance****Figure 17. Switching loss vs temperature****Figure 18. Switching loss vs collector-emitter voltage****Figure 19. Switching times vs. collector current**

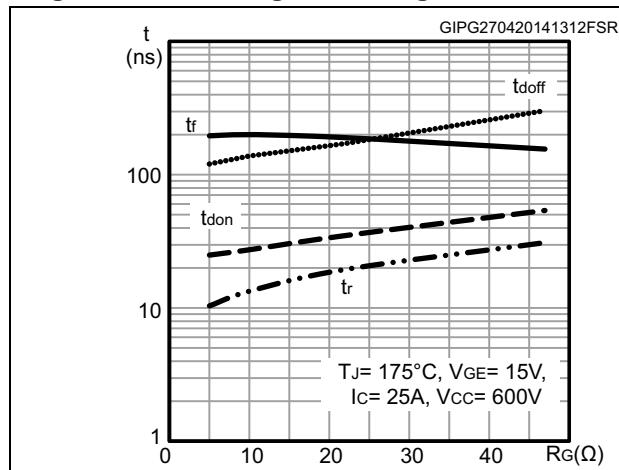
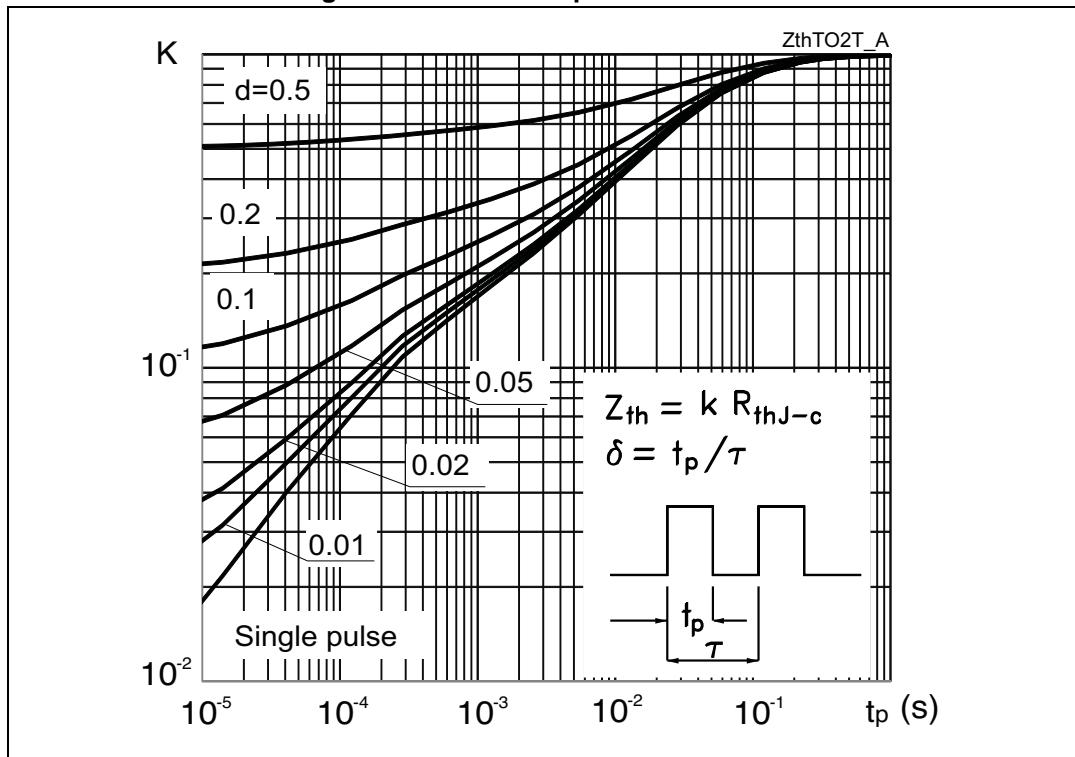
Figure 20. Switching times vs. gate resistance

Figure 21. Thermal impedance for IGBT



3 Test circuits

Figure 22. Test circuit for inductive load switching

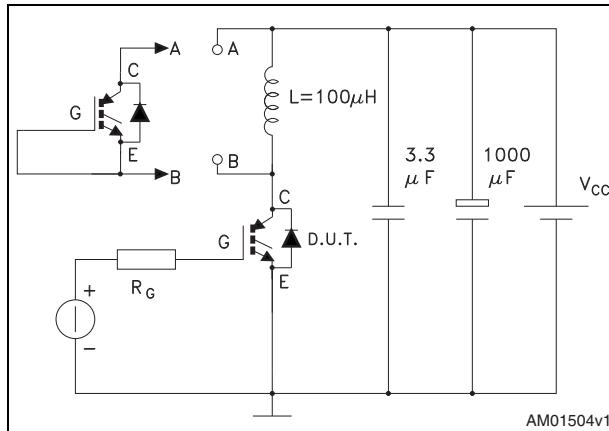


Figure 23. Gate charge test circuit

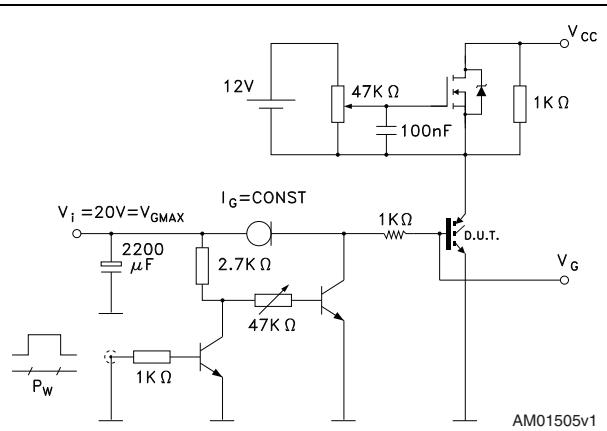
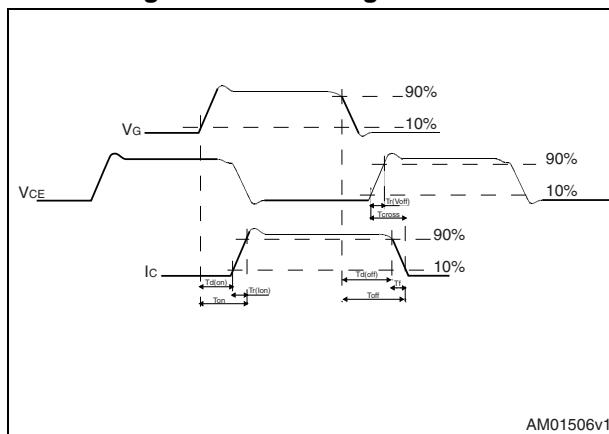


Figure 24. Switching waveform



4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK® is an ST trademark.

Figure 25. TO-247 drawing

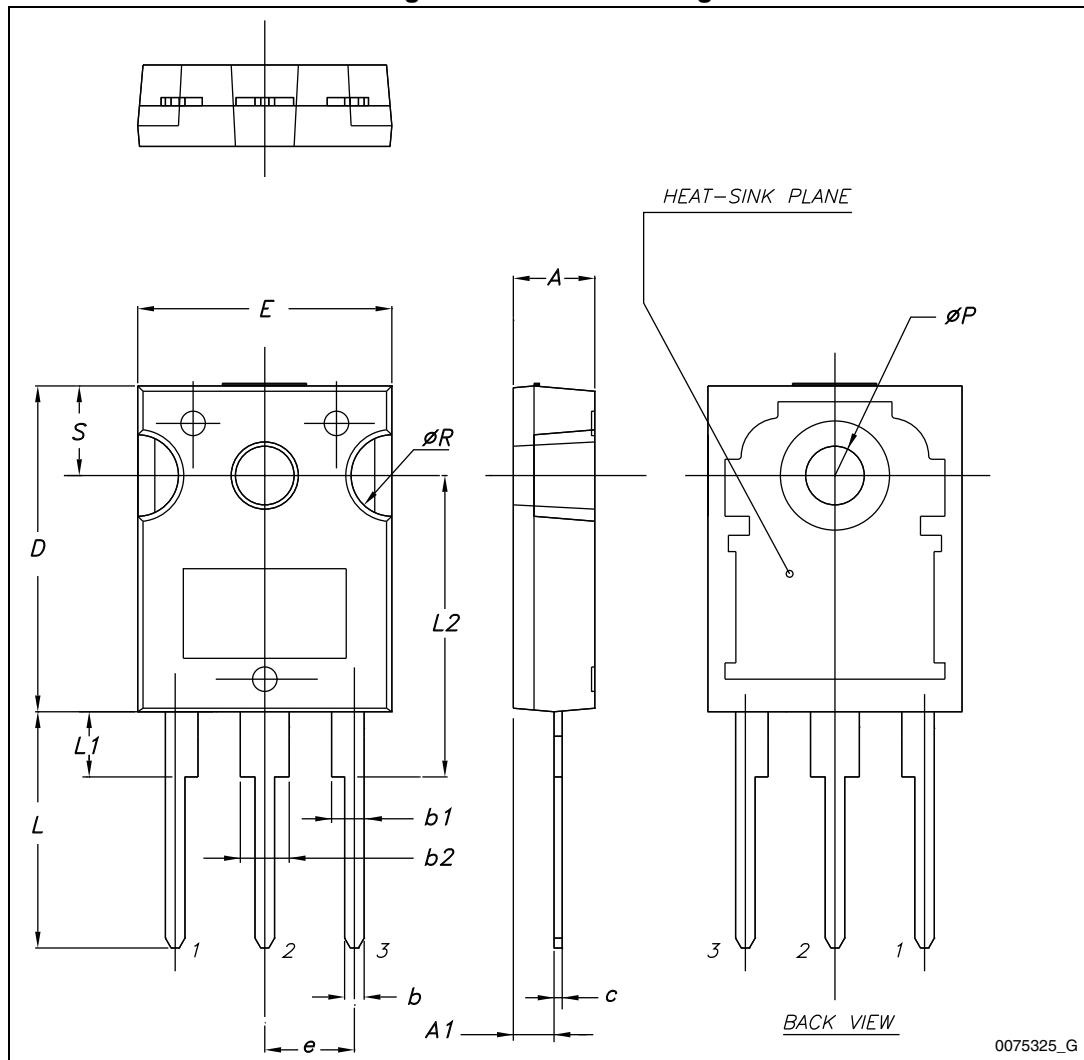


Table 7. TO-247 mechanical data

Dim.	mm.		
	Min.	Typ.	Max.
A	4.85		5.15
A1	2.20		2.60
b	1.0		1.40
b1	2.0		2.40
b2	3.0		3.40
c	0.40		0.80
D	19.85		20.15
E	15.45		15.75
e	5.30	5.45	5.60
L	14.20		14.80
L1	3.70		4.30
L2		18.50	
ØP	3.55		3.65
ØR	4.50		5.50
S	5.30	5.50	5.70

5 Revision history

Table 8. Document revision history

Date	Revision	Changes
28-Feb-2014	1	Initial release.
31-Mar-2014	2	Document status changed from preliminary to production data. Updated Table 4: Static characteristics and Table 6: Switching characteristics (inductive load) . Added Section 2.1: Electrical characteristics (curves) .

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