

AUIPS1031(S)(R)

INTELLIGENT POWER LOW SIDE SWITCH

Features

- Over temperature shutdown
- Over current shutdown
- Active clamp
- Low current & logic level input
- ESD protection
- Optimized Turn On/Off for EMI
- Diagnostic on the input current

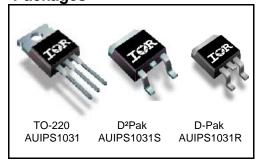
Description

The AUIPS1031(S)(R)PbF is a three terminal Intelligent Power Switch (IPS) that features a low side MOSFET with over-current, over-temperature, ESD protection and drain to source active clamp. This device offers protections and the high reliability required in harsh environments. The switch provides efficient protection by turning OFF the power MOSFET when the temperature exceeds 165°C or when the drain current reaches 18A. The device restarts once the input is cycled. A serial resistance connected to the input provides the diagnostic. The avalanche capability is significantly enhanced by the active clamp and covers most inductive load demagnetizations.

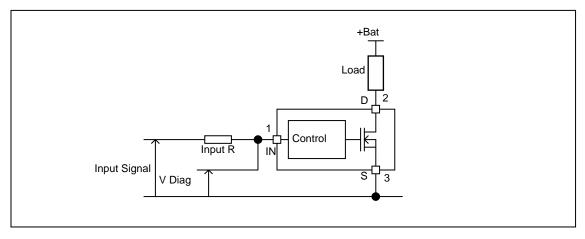
Product Summary

 $\begin{array}{ll} Rds(on) & 50m\Omega\,(max.) \\ Vclamp & 39V \\ Ishutdown & 18A\,(typ.) \end{array}$

Packages



Typical Connection





Qualification Information[†]

			Automotive			
			(per AEC-Q100 ^{††})			
Qualification Level		Comments: This family of ICs has passed an Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.				
Moisture Sensitivity Level		DPAK-3L	MSL1, 260°C (per IPC/JEDEC J-STD-020)			
		D2PAK-3L	MSL1, 260°C (per IPC/JEDEC J-STD-020)			
		TO220-5L	Not applicable			
	Machine Model	Class M4 (+/-450V) (per AEC-Q100-003)				
ESD	Human Body Model	Class H2 (+/-2500V) (per AEC-Q100-002)				
Charged Device Model		Class C3B (+/-1000V) (per AEC-Q100-011)				
IC Latc	h-Up Test	Class II, Level A (per AEC-Q100-004)				
RoHS (Compliant	Yes				

[†] Qualification standards can be found at International Rectifier's web site http://www.irf.com/

^{††} Exceptions to AEC-Q100 requirements are noted in the qualification report.



Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. (Tj= -40°C..150°C,

Vcc=6..36V unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
Vds	Maximum drain to source voltage	-0.3	36	V
Vds cont.	Maximum continuous drain to source voltage	-	28	V
Vin	Maximum input voltage	-0.3	6	V
Isd cont.	Max. diode continuous current (limited by thermal dissipation)		4	Α
	Maximum power dissipation (internally limited by thermal protection)			
	Rth=5°C/W AUIPS1031		25	W
Pd	Rth=40°C/W AUIPS1031S 1" sqr.		3.1	V V
	_ Footprint		3.1	
	Rth=50C/W AUIPS1031R 1" sqr. footprint		2.5	
Tj max.	Max. storage & operating temperature junction temperature	-40	150	°C

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Units
Rth1	Thermal resistance junction to ambient AUIPS1031 TO-220 free air	50	_	
Rth2	Thermal resistance junction to case AUIPS1031 TO-220	3.9	_	
Rth1	Thermal resistance junction to ambient AUIPS1031S D2Pak std. footprint	60	_	
Rth2	Thermal resistance junction to ambient AUIPS1031S D2Pak 1" sqr. footprint	40	_	
Rth3	Thermal resistance junction to case AUIPS1031S D2Pak	3.9	_	°C/W
Rth1	Thermal resistance junction to ambient AUIPS1031R D-Pak std. footprint	70	_	
Rth2	Thermal resistance junction to ambient AUIPS1031R D-Pak 1" sqr. Footprint	50	_	
Rth3	Thermal resistance junction to case AUIPS1031R D-Pak	3.9		

Recommended Operating Conditions

These values are given for a quick design. For operation outside these conditions, please consult the application notes.

Symbol	Parameter	Min.	Max.	Units
VIH	High level input voltage	4.5	5.5	
VIL	Low level input voltage	0	0.5	
Ids	Continuous drain current, Tambient=85°C, Tj=125°C, Vin=5V Rth=5°C/W AUIPS1031	_	9.5	Α
	Rth=40°C/W AUIPS1031S 1" sqr. footprint Rth=50C/W AUIPS1031R 1" sqr. footprint		3.3	
Rin	Recommended resistor in series with IN pin to generate a diagnostic	0.5	10	kΩ
Max L	Max recommended load inductance (including line inductance) (1)	_	50	μH
Max F	Max. frequency (switching losses = conduction losses)	_	1.5	kHz
Max. t rise	Max. input rising time	_	1	μs

⁽¹⁾ Higher inductance is possible if maximum load current is limited - see figure 11



Static Electrical Characteristics

Tj= -40..150°C, Vcc=6..28V (unless otherwise specified), typical value are given for Tj=25°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Rds(on)	ON state resistance Tj=25°C	_	40	50	mΩ	Vin=5V. Ids=8A
	ON state resistance Tj=150°C (2)	_	76	95	1112.2	VIII=5V, IUS=6A
ldss1	Drain to source leakage current	_	0.1	2	۸	Vcc=14V, Tj=25°C
ldss2	Drain to source leakage current	_	0.2	4	μA	Vcc=28V, Tj=25°C
V clamp1	Drain to source clamp voltage 1	36	39	_		Id=20mA
V clamp2	Drain to source clamp voltage 2	_	40	42	\/	Id=1A
Vin clamp	IN to source pin clamp voltage	5.5	6.5	7.5	V	lin=1mA
Vth	Input threshold voltage	_	1.7	_		Id=10mA

Switching Electrical Characteristics

Vcc=14V, Resistive load=1.5Ω, Rinput=0Ω, Vin=5V, Tj=25°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Tdon	Turn-on delay time to 20%	3	10	30		
Tr	Rise time 20% to 80%	6	20	40		See figure 2
Tdoff	Turn-off delay time to 80%	20	70	200	μs	See ligure 2
Tf	Fall time 80% to 20%	6	15	30		
Eon + Eoff	Turn on and off energy	_	0.7	_	mJ	

Protection Characteristics

Ti= -40..150°C, Vcc=6..28V (unless otherwise specified), typical value are given for Ti=25°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Tsd	Over temperature threshold	150(2)	165	_	°C	See figure 1
Isd	Over current threshold	9.5	18	27	Α	See figure 1
OV	Over voltage protection (not active when the device is ON)	34	37	_	V	
Vreset	IN protection reset threshold		1.7	_	V	
Treset	Time to reset protection	15(2)	50	200	μs	Vin=0V

Diagnostic

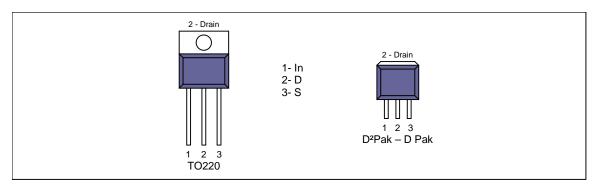
Tj= -40..150°C, Vcc=6..28V (unless otherwise specified), typical value are given for Tj=25°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
lin, on	ON state IN positive current	10	32	80		Vin=5V
lin, off	OFF state IN positive current	120	230	350	μΑ	Vin=5V
	(after protection latched)					

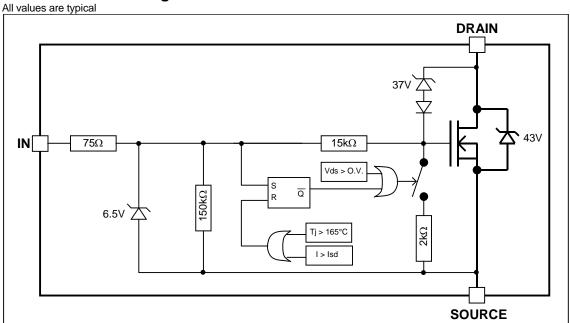
(2) Guaranteed by design



Lead Assignments



Functional Block Diagram





All curves are typical values. Operating in the shaded area is not recommended.

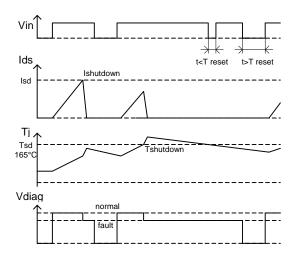


Figure 1 - Timing diagram

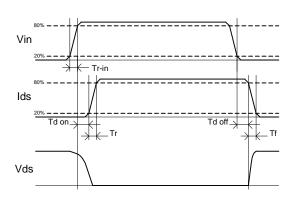


Figure 2 - IN rise time & switching definitions

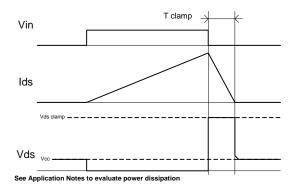


Figure 3 - Active clamp waveforms

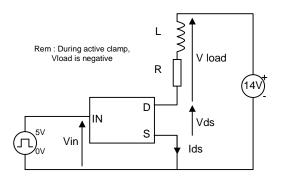


Figure 4 - Active clamp test circuit

25

20

15

10

5

0

ds, output current

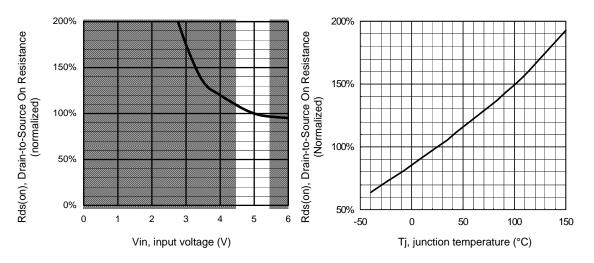


Figure 5 – Normalized Rds(on) (%) Vs Input voltage (V)

140% 120% lsd, normalized I shutdown (%) 100% 80% 60% 40% 20% 0% -50 0 50 100 150

Figure 6 - Normalized Rds(on) (%) Vs Tj (°C)

Figure 7 - Current limitation and current shutdown Vs Input voltage (V)

3

Vin, input voltage (V)

2

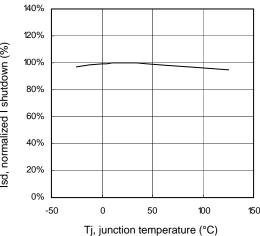


Figure 8 - Normalized I shutdown (%) Vs junction temperature (°C)

7 www.irf.com

- ،I limit

4

I shutdown

5

6

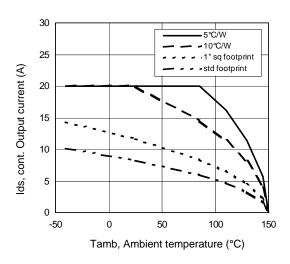
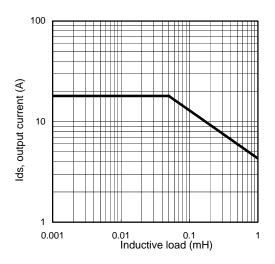


Figure 9 – Max. continuous output current (A) Vs Ambient temperature (°C)

Figure 10 – Ids (A) Vs over temperature protection response time (s)



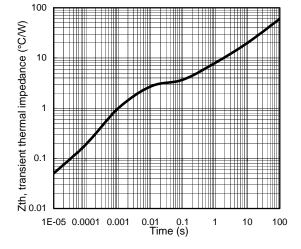
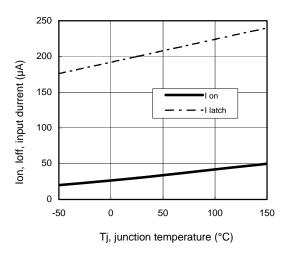


Figure 11 – Max. ouput current (A) Vs Inductive load (mH)

Figure 12 – Transient thermal impedance (°C/W) Vs time (s)



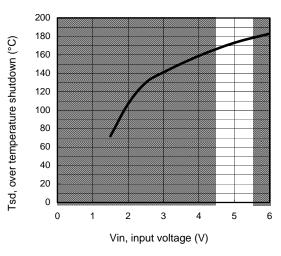
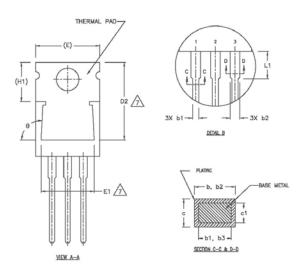


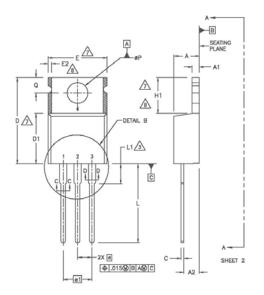
Figure 13 – Input current (μA) On and Off Vs junction temperature (°C)

Figure 14 – Over temperature shutdown (°C) Vs input voltage (V)



Case Outline - TO-220 AB - Automotive Q100 PbF qualified





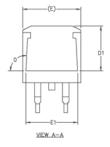
SYMBOL	MILLIM	ETERS	INC	HES	1
	MIN.	MAX.	MIN.	MAX.	NOTES
Α	3.56	4.82	.140	.190	
A1	0.51	1.40	.020	.055	
A2	2.04	2.92	.080	.115	
ь	0.38	1.01	.015	.040	
ь1	0.38	0.96	.015	.038	5
b2	1.15	1.77	.045	.070	
b3	1.15	1.73	.045	.068	
с	0.36	0.61	.014	.024	
c1	0.36	0.56	.014	.022	5
D	14.22	16.51	.560	.650	4
D1	8.38	9.02	.330	.355	4
D2	12.19	12.88	.480	.507	7
E	9.66	10.66	.380	.420	4,7
E1	8.38	8.89	.330	.350	7
e	2.54		.100		· ′
e1	5.0	08	.200	BSC	1
H1	5.85	6.55	.230	.270	7.8
L	12.70	14.73	.500	.580	7,0
L1	-	6.35	.500	.250	3
øP	3.54	4.08	.139	.161	,
Q	2.54	3.42	.100	.135	
ø	90'-93'			-93*	1
L					1

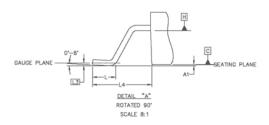
NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5 M- 1994.
- DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS].
- LEAD DIMENSION AND FINISH UNCONTROLLED IN L1.
- DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005" (0.127) PER SIDE, THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BOOY.
- DIMENSION 61 & c1 APPLY TO BASE METAL ONLY. CONTROLLING DIMENSION: INCHES.
- THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS E,H1,D2 & E1
- DIMENSION E2 X H1 DEFINE A ZONE WHERE STAMPING AND SINGULATION IRREGULARITIES ARE ALLOWED.
- LEADS AND DRAIN ARE PLATED WITH 100% Sn



Case Outline - D²Pak (SMD-220) - Automotive Q100 PbF MSL1 qualified





MILLIMETERS MIN MAX MIN.

4.06 4.83 .160 .190

0.51 0.99 .020 .039

0.51 0.89 .020 .035

1.14 1.78 .045 .070

0.38 0.74 .015 .029

1.14 1.65 .045 .065

6.86

6.22

14.61 15.88

1.78 2.79 .070 .110

1.27 1.78 .050 .070

4.78

17.78

8.89

11.43

3.81

0.51

90"

n 2.08

0

R

2.54 BSC

0.25 BSC

1.65

0.71

93* 90° 93°

.335 .380

.270

.380 .420

.245

.010

.188

.700

.350

.450

.082

.150

.020

.100 BSC

.625

.065

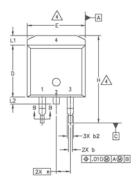
.028

A1 0.00 0.254 .000 .010

c1 0.38 0.58 .015 .023

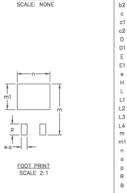
D 8.51

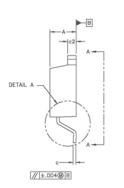
Ε 9.65







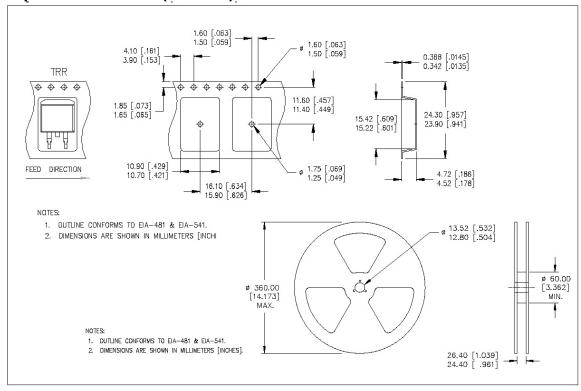




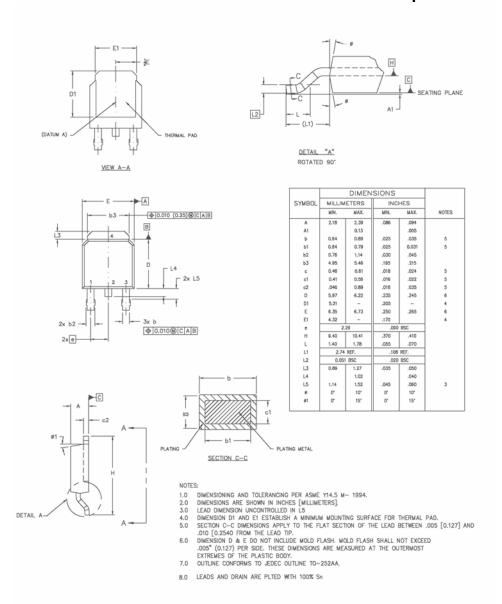
- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
- 2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
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- 4. DIMENSION 61 AND c1 APPLY TO BASE METAL ONLY.
 - 5. CONTROLLING DIMENSION: INCH.
 - 6. LEADS & DRAIN CONTACT ARE PLATED : 100% Sn



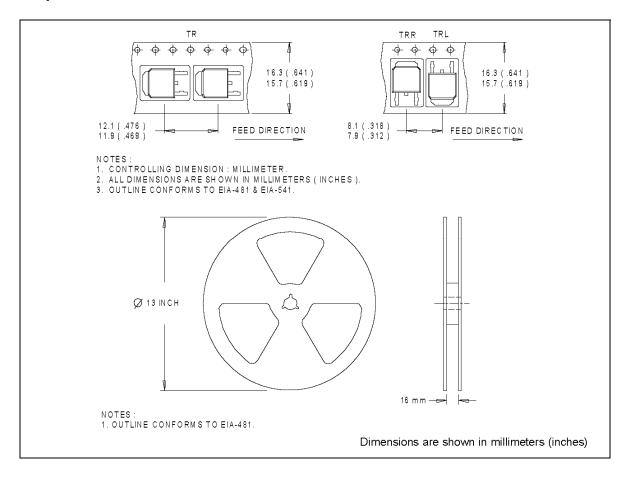
Tape & Reel - D²Pak (SMD220)



Case Outline - D-Pak - Automotive Q100 PbF MSL1 qualified

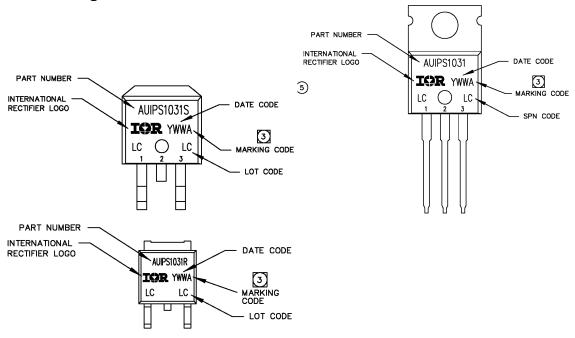


Tape & Reel - D-Pak





Part Marking Information



Ordering Information

Base Part Number		Standard Pack	Occupation Board Name Long	
base i ait ivuilibei	Package Type	Form	Quantity	Complete Part Number
	TO220 – 5Leads	Tube	50	AUIPS1031
	D2-Pak-5- Leads	Tube	50	AUIPS1031S
		Tape and reel left	800	AUIPS1031STRL
AUIPS1031		Tape and reel right	800	AUIPS1031STRR
7.6 6.66.	D. Dalla E. Lacad	Tube	75	AUIPS1031R
		Tape and reel	2000	AUIPS1031RTR
	D-Pak-5-Lead	Tape and reel left	3000	AUIPS1031RTRL
		Tape and reel right	3000	AUIPS1031RTRR



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Revision History

Revision	Date	Notes/Changes
D	November, 24 th , 2010	AU release
D1	December, 7 th , 2010	Remove ESD section page 3
D2	December, 9 th 2010	Update qual page 2
Е	February, 8th 2011	Update Vclamp page 1
F	February, 28 th 2011	Update Max rating

Mouser Electronics

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