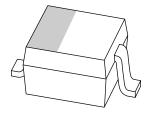
DISCRETE SEMICONDUCTORS

DATA SHEET



BAP63-03 Silicon PIN diode

Product specification Supersedes data of 2001 May 18 2004 Feb 11



Silicon PIN diode BAP63-03

FEATURES

- High speed switching for RF signals
- Low diode capacitance
- Low diode forward resistance
- · Very low series inductance
- For applications up to 3 GHz.

APPLICATIONS

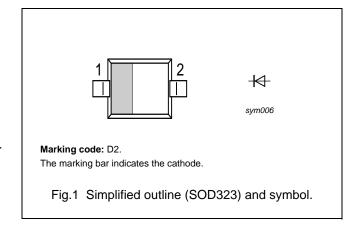
· RF attenuators and switches.

DESCRIPTION

Planar PIN diode in a SOD323 small SMD plastic package.

PINNING

PIN	DESCRIPTION
1	cathode
2	anode



ORDERING INFORMATION

TYPE		PACKAGE				
NUMBER	NAME	DESCRIPTION VERSIO				
BAP63-03	_	plastic surface mounted package; 2 leads	SOD323			

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _R	continuous reverse voltage		_	50	V
I _F	continuous forward current		_	100	mA
P _{tot}	total power dissipation	T _s ≤ 90 °C	_	500	mW
T _{stg}	storage temperature		-65	+150	°C
T _j	junction temperature		-65	+150	°C

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ELECTRICAL CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

SYMBOL	L PARAMETER CONDITIONS		TYP.	MAX.	UNIT	
V _F	forward voltage	I _F = 50 mA	0.95	1.1	V	
I _R	reverse leakage current	V _R = 35 V	_	10	nA	
C _d	diode capacitance	V _R = 0; f = 1 MHz	0.4	_	pF	
		V _R = 1 V; f = 1 MHz	0.35	_	pF	
		V _R = 20 V; f = 1 MHz	0.27	0.32	pF	
r _D	diode forward resistance	I _F = 0.5 mA; f = 100 MHz; note 1	2.5	3.5	Ω	
		I _F = 1 mA; f = 100 MHz; note 1	1.95	3	Ω	
		I _F = 10 mA; f = 100 MHz; note 1	1.17	1.8	Ω	
		I _F = 100 mA; f = 100 MHz; note 1	0.9	1.5	Ω	
$ s_{21} ^2$	isolation	V _R = 0; f = 900 MHz	15.4	_	dB	
		V _R = 0; f = 1800 MHz	10.1	_	dB	
		V _R = 0; f = 2450 MHz	7.8	-	dB	
$ s_{21} ^2$	insertion loss	I _F = 0.5 mA; f = 900 MHz	0.21	_	dB	
		I _F = 0.5 mA; f = 1800 MHz	0.28	-	dB	
		I _F = 0.5 mA; f = 2450 MHz	0.38	-	dB	
$ s_{21} ^2$	insertion loss	I _F = 1 mA; f = 900 MHz	0.18	-	dB	
		I _F = 1 mA; f = 1800 MHz	0.26	-	dB	
		I _F = 1 mA; f = 2450 MHz	0.35	-	dB	
$ s_{21} ^2$	insertion loss	I _F = 10 mA; f = 900 MHz	0.13	_	dB	
		I _F = 10 mA; f = 1800 MHz	0.20	_	dB	
		I _F = 10 mA; f = 2450 MHz	0.30	-	dB	
$ s_{21} ^2$	insertion loss	I _F = 100 mA; f = 900 MHz	0.10	_	dB	
		I _F = 100 mA; f = 1800 MHz	0.18	-	dB	
		I _F = 100 mA; f = 2450 MHz	0.28	-	dB	
τι	charge carrier life time	when switched from I _F = 10 mA to I _R = 6 mA; R _L = 100 Ω ; measured at I _R = 3 mA	310	-	ns	
L _S	series inductance		1.5	_	nH	

Note

1. Guaranteed on AQL basis: inspection level S4, AQL 1.0.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
$R_{th(j-s)}$	thermal resistance from junction to soldering point	120	K/W

Silicon PIN diode BAP63-03

GRAPHICAL DATA

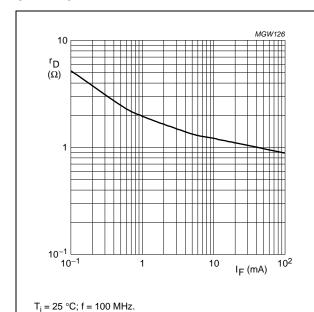


Fig.2 Forward resistance as a function of forward current; typical values.

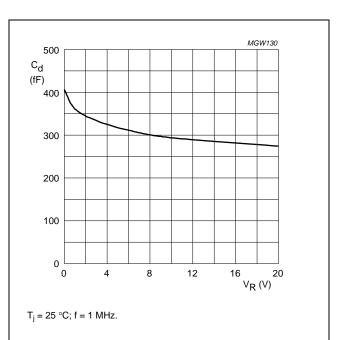
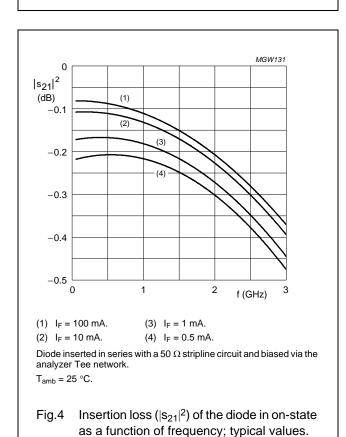


Fig.3 Diode capacitance as a function of reverse voltage; typical values.



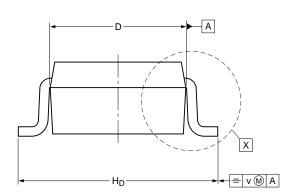
 $|s_{21}|^2 \\ (dB) \\ -10 \\ -20 \\ -40 \\ 0 \\ 1 \\ 2 \\ f (GHz) \\ 3$ Diode zero biased and inserted in series with a 50 Ω stripline circuit. $T_{amb} = 25 \, ^{\circ}C$.

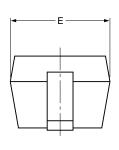
Fig.5 Isolation ($|s_{21}|^2$) of the diode in off-state as a function of frequency; typical values.

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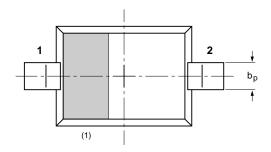
PACKAGE OUTLINE

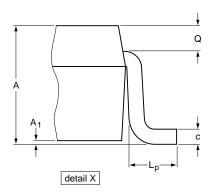
Plastic surface-mounted package; 2 leads





SOD323







DIMENSIONS (mm are the original dimensions)

UNIT	Α	A ₁ max	bp	С	D	E	H _D	Lp	Q	v
mm	1.1 0.8	0.05	0.40 0.25	0.25 0.10	1.8 1.6	1.35 1.15	2.7 2.3	0.45 0.15		0.2

Note

The marking bar indicates the cathode

OUTLINE		REFERENCES				ISSUE DATE
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOD323			SC-76			-03-12-17- 06-03-16

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DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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