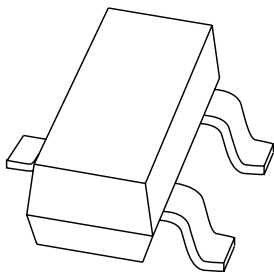


DATA SHEET



BCV26; BCV46 PNP Darlington transistors

Product data sheet
Supersedes data of 1999 Apr 08

2004 Jan 13

PNP Darlington transistors

BCV26; BCV46

FEATURES

- High current (max. 500 mA)
- Low voltage (max. 60 V)
- Very high DC current gain (min. 10000).

APPLICATIONS

- Where very high amplification is required.

DESCRIPTION

PNP Darlington transistor in a SOT23 plastic package.
NPN complements: BCV27 and BCV47.

MARKING

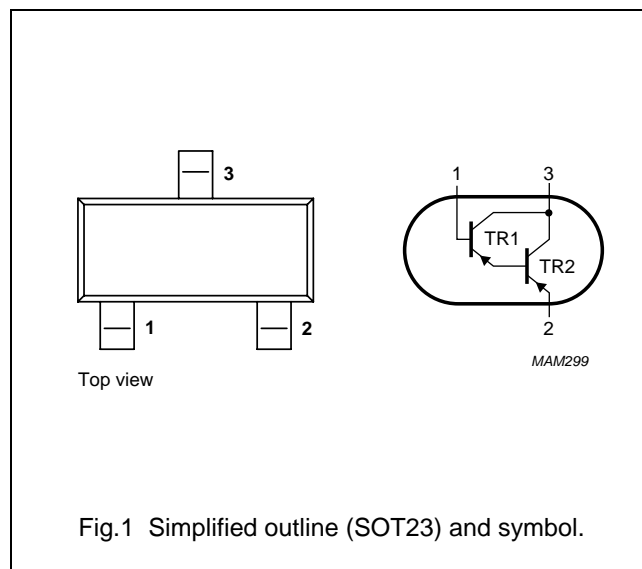
TYPE NUMBER	MARKING CODE ⁽¹⁾
BCV26	FD*
BCV46	FE*

Note

1. * = p : Made in Hong Kong.
 * = t : Made in Malaysia.
 * = W : Made in China.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



ORDERING INFORMATION

TYPE NUMBER	PACKAGE		
	NAME	DESCRIPTION	VERSION
BCV26	—	plastic surface mounted package; 3 leads	SOT23
BCV46			

PNP Darlington transistors

BCV26; BCV46

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	BCV26		–	–40	V
	BCV46		–	–80	V
V _{CES}	collector-emitter voltage	V _{BE} = 0			
	BCV26		–	–30	V
	BCV46		–	–60	V
V _{EBO}	emitter-base voltage	open collector	–	–10	V
I _C	collector current (DC)		–	–500	mA
I _{CM}	peak collector current		–	–800	mA
I _B	base current (DC)		–	–100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	–	250	mW
T _{stg}	storage temperature		–65	+150	°C
T _j	junction temperature		–	150	°C
T _{amb}	operating ambient temperature		–65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

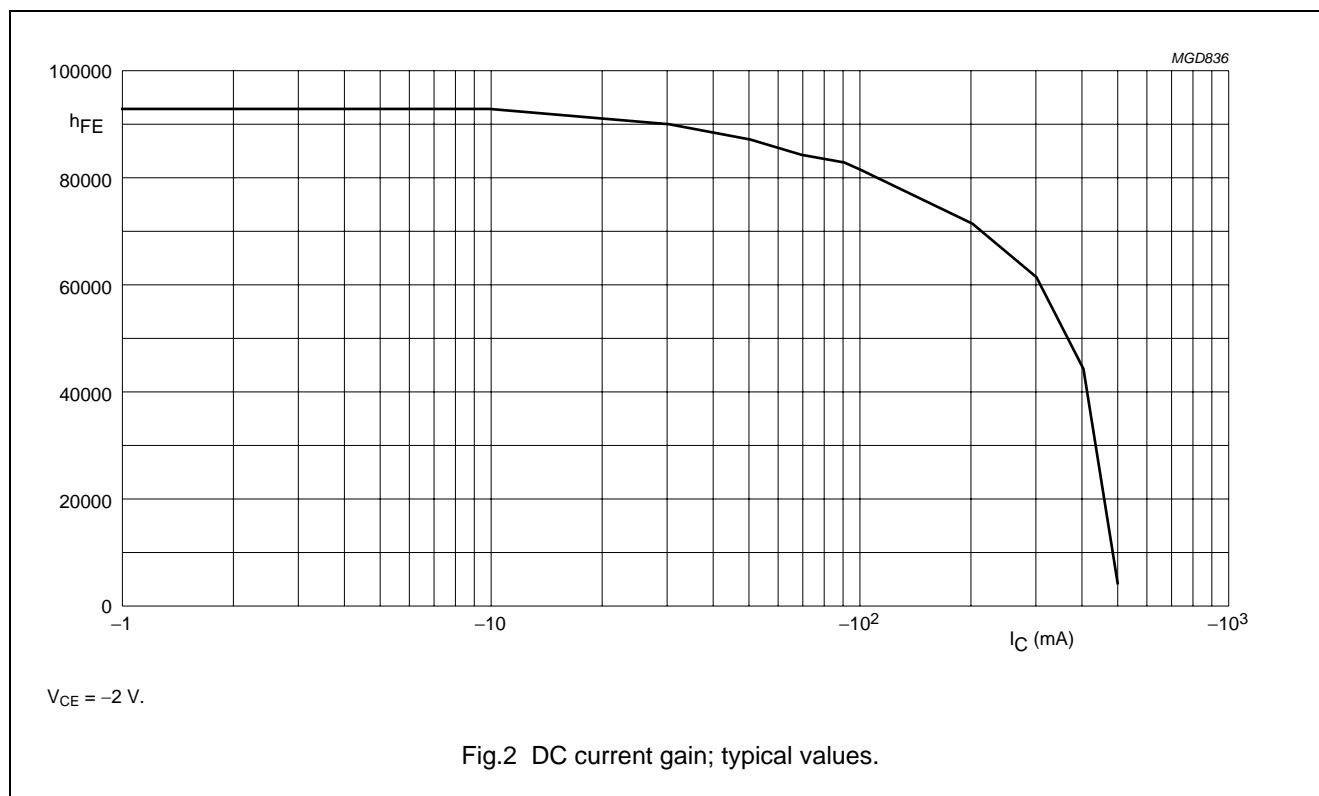
PNP Darlington transistors

BCV26; BCV46

CHARACTERISTICS

$T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I_{CBO}	collector cut-off current BCV26 BCV46	$I_E = 0$; $V_{CB} = -30\text{ V}$ $I_E = 0$; $V_{CB} = -60\text{ V}$	–	–	–100	nA
I_{EBO}	emitter cut-off current	$I_C = 0$; $V_{EB} = -10\text{ V}$	–	–	–100	nA
h_{FE}	DC current gain BCV26	$I_C = -1\text{ mA}$; $V_{CE} = -5\text{ V}$; (see Fig.2)	4000	–	–	
	BCV46		2000	–	–	
	DC current gain BCV26	$I_C = -10\text{ mA}$; $V_{CE} = -5\text{ V}$; (see Fig.2)	10000	–	–	
	BCV46		4000	–	–	
	DC current gain BCV26	$I_C = -100\text{ mA}$; $V_{CE} = -5\text{ V}$; (see Fig.2)	20000	–	–	
	BCV46		10000	–	–	
V_{CEsat}	collector-emitter saturation voltage	$I_C = -100\text{ mA}$; $I_B = -0.1\text{ mA}$	–	–	–1	V
V_{BEsat}	base-emitter saturation voltage	$I_C = -100\text{ mA}$; $I_B = -0.1\text{ mA}$	–	–	–1.5	V
V_{BEon}	base-emitter on-state voltage	$I_C = -10\text{ mA}$; $V_{CE} = -5\text{ V}$	–	–	–1.4	V
f_T	transition frequency	$I_C = -30\text{ mA}$; $V_{CE} = -5\text{ V}$; $f = 100\text{ MHz}$	–	220	–	MHz



PNP Darlington transistors
BCV26; BCV46

PACKAGE OUTLINE

Plastic surface-mounted package; 3 leads
SOT23

The diagrams illustrate the SOT23 package outline. The top view shows dimensions D (total width) and B (lead width). The side view shows dimensions E (lead length), HE (height), and A (total height). A detail view 'X' shows the lead profile with dimensions A1, Q, c, and Lp. The bottom view shows dimensions 1, 2, 3, e1, bp, e, and w. A scale bar indicates 0 to 2 mm.

DIMENSIONS (mm are the original dimensions)

UNIT	A	A ₁ max.	b _p	c	D	E	e	e ₁	H _E	L _p	Q	v	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT23		TO-236AB				04-11-04 06-03-16

PNP Darlington transistors

BCV26; BCV46

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

1. Please consult the most recently issued document before initiating or completing a design.
2. The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

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Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

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