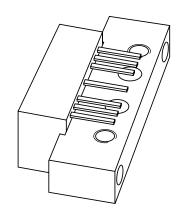
DISCRETE SEMICONDUCTORS

DATA SHEET



BGD906; BGD906MI 860 MHz, 21.5 dB gain power doubler amplifier

Product specification Supersedes data of 2000 Mar 28 2001 Nov 01



860 MHz, 21.5 dB gain power doubler amplifier

BGD906; BGD906MI

FEATURES

- · Excellent linearity
- · Extremely low noise
- · Excellent return loss properties
- Silicon nitride passivation
- Rugged construction
- · Gold metallization ensures excellent reliability.

APPLICATIONS

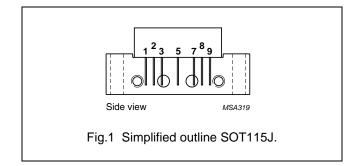
 CATV systems operating in the 40 to 900 MHz frequency range.

DESCRIPTION

Hybrid amplifier modules in a SOT115J package operating with a voltage supply of 24 V (DC). Both modules are electrically identical, only the pinning is different.

PINNING - SOT115J

PIN	DESCRIPTION		
	BGD906	BGD906MI	
1	input	output	
2, 3	common	common	
5	+V _B	+V _B	
7, 8	common	common	
9	output	input	



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Gp	power gain	f = 50 MHz	21.2	21.8	dB
		f = 900 MHz	22	23	dB
I _{tot}	total current consumption (DC)	V _B = 24 V; T _{mb} = 35 °C	405	435	mA

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER		MAX.	UNIT
V _B	supply voltage		30	٧
Vi	RF input voltage		70	dBmV
T _{stg}	storage temperature		+100	°C
T _{mb}	operating mounting base temperature		+100	°C

860 MHz, 21.5 dB gain power doubler amplifier

BGD906; BGD906MI

CHARACTERISTICS

Bandwidth 40 to 900 MHz; V_B = 24 V; T_{mb} = 35 °C; Z_S = Z_L = 75 Ω

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Gp	power gain	f = 50 MHz	21.2	21.5	21.8	dB
		f = 900 MHz	22	22.5	23	dB
SL	slope straight line	f = 40 to 900 MHz		1	1.5	dB
FL	flatness straight line	f = 40 to 900 MHz	_	_	±0.35	dB
S ₁₁	input return losses	f = 40 to 80 MHz	22	25	_	dB
		f = 80 to 160 MHz	21	24	_	dB
		f = 160 to 320 MHz	18	23	_	dB
		f = 320 to 550 MHz	17	23	_	dB
		f = 550 to 900 MHz	16	20	_	dB
S ₂₂	output return losses	f = 40 to 80 MHz	22	25	_	dB
		f = 80 to 160 MHz	21	25	_	dB
		f = 160 to 320 MHz	20	23	_	dB
		f = 320 to 550 MHz	19	22	_	dB
		f = 550 to 650 MHz	18	24	_	dB
		f = 650 to 750 MHz	17	23	_	dB
		f = 750 to 900 MHz	16	21	_	dB
S ₂₁	phase response	f = 50 MHz	-45	_	+45	deg
СТВ	composite triple beat	49 chs flat; $V_0 = 47 \text{ dBmV}$; $f_m = 859.25 \text{ MHz}$	_	-68.5	-66	dB
		77 chs flat; $V_0 = 44 \text{ dBmV}$; $f_m = 547.25 \text{ MHz}$	_	-70	-67	dB
		110 chs flat; $V_0 = 44 \text{ dBmV}$; $f_m = 745.25 \text{ MHz}$	_	-63	-61	dB
		129 chs flat; $V_0 = 44 \text{ dBmV}$; $f_m = 859.25 \text{ MHz}$	_	-59	-57	dB
		110 chs; f _m = 397.25 MHz; V _o = 49 dBmV at 550 MHz; note 1	_	-62.5	-60.5	dB
		129 chs; $f_m = 697.25 \text{ MHz}$; $V_0 = 49.5 \text{ dBmV}$ at 860 MHz; note 2	_	-57	-54.5	dB
X _{mod}	cross modulation	49 chs flat; $V_0 = 47 \text{ dBmV}$; $f_m = 55.25 \text{ MHz}$	_	-64	-62	dB
		77 chs flat; $V_0 = 44 \text{ dBmV}$; $f_m = 55.25 \text{ MHz}$	_	-67.5	-65	dB
		110 chs flat; $V_0 = 44 \text{ dBmV}$; $f_m = 55.25 \text{ MHz}$	_	-64	-61.5	dB
		129 chs flat; $V_0 = 44 \text{ dBmV}$; $f_m = 55.25 \text{ MHz}$	_	-61	-60	dB
		110 chs; f _m = 397.25 MHz; V _o = 49 dBmV at 550 MHz; note 1	_	-60	-58	dB
		129 chs; f _m = 859.25 MHz; V _o = 49.5 dBmV at 860 MHz; note 2	_	-56.5	-55	dB

860 MHz, 21.5 dB gain power doubler amplifier

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
CSO	composite second order distortion	49 chs flat; V _o = 47 dBmV; f _m = 860.5 MHz	_	-63	-59	dB
		77 chs flat; V _o = 44 dBmV; f _m = 548.5 MHz	_	-74	-65	dB
		110 chs flat; V _o = 44 dBmV; f _m = 746.5 MHz	_	-66	-58	dB
		129 chs flat; V _o = 44 dBmV; f _m = 860.5 MHz	_	-59	-54	dB
		110 chs; f _m = 150 MHz; V _o = 49 dBmV at 550 MHz; note 1	_	-64	-60	dB
		129 chs; f _m = 150 MHz; V _o = 49.5 dBmV at 860 MHz; note 2	_	-60	-54	dB
d_2	second order distortion	note 3	-	-83	-70	dB
		note 4	_	-81.5	-73	dB
		note 5	_	-79	-76	dB
Vo	output voltage	$d_{im} = -60 \text{ dB}$; note 6	63.5	64.5	_	dBmV
		d _{im} = -60 dB; note 7	64.5	66.5	_	dBmV
		$d_{im} = -60 \text{ dB}$; note 8	66.5	69	_	dBmV
		CTB compression = 1 dB; 129 chs flat; f = 859.25 MHz	48.5	49	_	dBmV
		CSO compression = 1 dB; 129 chs flat; f = 860.5 MHz	51	54	_	dBmV
NF	noise figure	f = 50 MHz	_	5	5.5	dB
		f = 550 MHz	_	4.5	5	dB
		f = 750 MHz		5	6	dB
		f = 900 MHz		6	7.5	dB
I _{tot}	total current note 9 consumption (DC)		405	420	435	mA

Notes

- Tilt = 9 dB (50 to 550 MHz)
 tilt = 3.5 dB at -6 dB offset (550 to 750 MHz).
- 2. Tilt = 12.5 dB (50 to 860 MHz).
- 3. $f_p = 55.25 \text{ MHz}; V_p = 44 \text{ dBmV};$ $f_q = 805.25 \text{ MHz}; V_q = 44 \text{ dBmV};$ measured at $f_p + f_q = 860.5 \text{ MHz}.$
- $\begin{array}{ll} \text{4.} & f_p = 55.25 \text{ MHz; V}_p = 44 \text{ dBmV;} \\ & f_q = 691.25 \text{ MHz; V}_q = 44 \text{ dBmV;} \\ & \text{measured at } f_p + f_q = 746.5 \text{ MHz.} \\ \end{array}$
- 5. $f_p = 55.25 \text{ MHz}; V_p = 44 \text{ dBmV};$ $f_q = 493.25 \text{ MHz}; V_q = 44 \text{ dBmV};$ measured at $f_p + f_q = 548.5 \text{ MHz}.$
- 6. Measured according to DIN45004B: $f_p = 851.25 \text{ MHz}; V_p = V_o;$ $f_q = 858.25 \text{ MHz}; V_q = V_o -6 \text{ dB};$ $f_r = 860.25 \text{ MHz}; V_r = V_o -6 \text{ dB};$ measured at $f_p + f_q f_r = 849.25 \text{ MHz}.$

- 7. Measured according to DIN45004B:
 - $$\begin{split} f_p &= 740.25 \text{ MHz; } V_p = V_o; \\ f_q &= 747.25 \text{ MHz; } V_q = V_o 6 \text{ dB;} \\ f_r &= 749.25 \text{ MHz; } V_r = V_o 6 \text{ dB;} \\ \text{measured at } f_p + f_q f_r = 738.25 \text{ MHz.} \end{split}$$

BGD906; BGD906MI

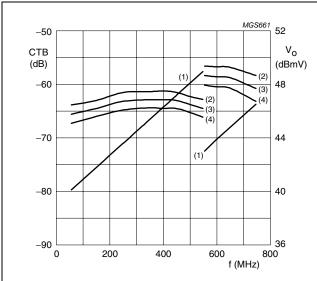
8. Measured according to DIN45004B:

```
\begin{split} f_p &= 540.25 \text{ MHz; } V_p = V_o; \\ f_q &= 547.25 \text{ MHz; } V_q = V_o - 6 \text{ dB;} \\ f_r &= 549.25 \text{ MHz; } V_r = V_o - 6 \text{ dB;} \\ \text{measured at } f_p + f_q - f_r = 538.25 \text{ MHz.} \end{split}
```

9. The module normally operates at $V_B = 24 \text{ V}$, but is able to withstand supply transients up to 35 V.

860 MHz, 21.5 dB gain power doubler amplifier

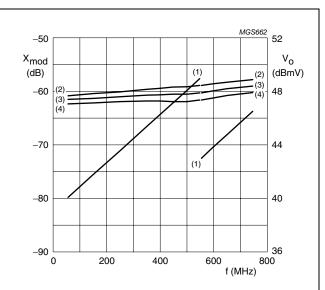
BGD906; BGD906MI



 $Z_S=Z_L=75~\Omega;~V_B=24~V;~110~chs;~tilt=9~dB~(50~to~550~MHz);~tilt=3.5~dB~at~-6~dB~offset~(550~to~750~MHz).$

- (1) V_o.
- (3) Typ.
- (2) Typ. +3 σ .
- (4) Typ. −3 σ.

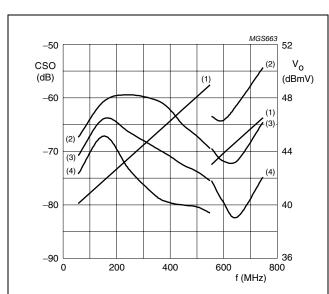
Fig.2 Composite triple beat as a function of frequency under tilted conditions.



 Z_S = Z_L = 75 $\Omega;$ V_B = 24 V; 110 chs; tilt = 9 dB (50 to 550 MHz); tilt = 3.5 dB at –6 dB offset (550 to 750 MHz).

- $(1) \quad V_o.$
- (3) Typ.
- (2) Typ. +3 σ.
- (4) Typ. −3 σ.

Fig.3 Cross modulation as a function of frequency under tilted conditions.



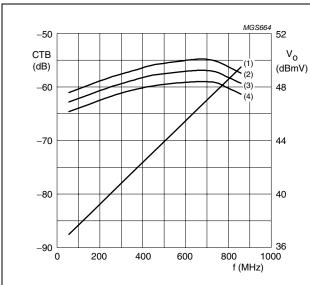
 $Z_S=Z_L=75~\Omega;~V_B=24~V;~110~chs;~tilt=9~dB~(50~to~550~MHz);~tilt=3.5~dB~at~-6~dB~offset~(550~to~750~MHz).$

- (1) V_o.
- (3) Typ.
- (2) Typ. +3 σ.
- (4) Typ. –3 σ.

Fig.4 Composite second order distortion as a function of frequency under tilted conditions.

860 MHz, 21.5 dB gain power doubler amplifier

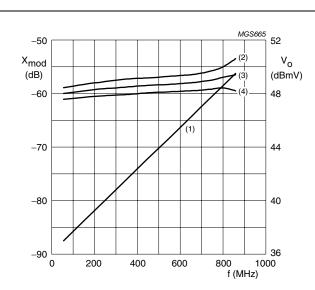
BGD906; BGD906MI



 Z_S = Z_L = 75 $\Omega;$ V_B = 24 V; 129 chs; tilt = 12.5 dB (50 to 860 MHz).

- (1) V_o.
- (3) Typ.
- (2) Typ. +3 σ.
- (4) Typ. −3 σ.

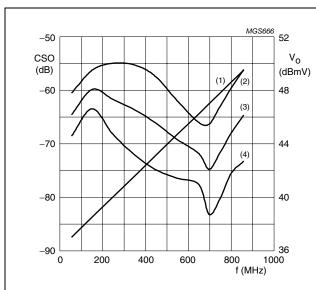
Fig.5 Composite triple beat as a function of frequency under tilted conditions.



 Z_S = Z_L = 75 $\Omega;$ V_B = 24 V; 129 chs; tilt = 12.5 dB (50 to 860 MHz).

- $(1) \quad V_o.$
- (3) Typ.
- (2) Typ. +3 σ.
- (4) Typ. –3 σ.

Fig.6 Cross modulation as a function of frequency under tilted conditions.



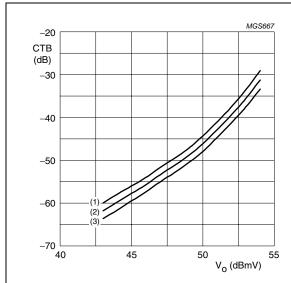
 $Z_S = Z_L = 75~\Omega; V_B = 24~V; 129~chs;$ tilt = 12.5 dB (50 to 860 MHz).

- (1) V_o.
- (3) Typ.
- (2) Typ. +3 σ.
- (4) Typ. –3 σ.

Fig.7 Composite second order distortion as a function of frequency under tilted conditions.

860 MHz, 21.5 dB gain power doubler amplifier

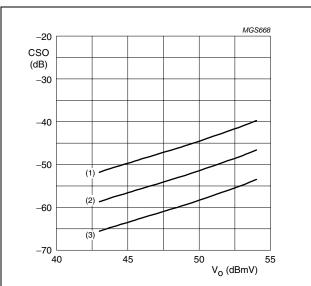
BGD906; BGD906MI



 Z_S = Z_L = 75 $\Omega;$ V_B = 24 V; 129 chs; f_m = 859.25 MHz.

- (1) Typ. $+3 \sigma$.
- (2) Typ.
- (3) Typ. -3σ .

Fig.8 Composite triple beat as a function of output voltage.



 Z_S = Z_L = 75 $\Omega;~V_B$ = 24 V; 129 chs; f_m = 860.5 MHz.

- (1) Typ. +3 σ.
- (2) Typ.

7

(3) Typ. -3σ .

Fig.9 Composite second order distortion as a function of output voltage.

2001 Nov 01

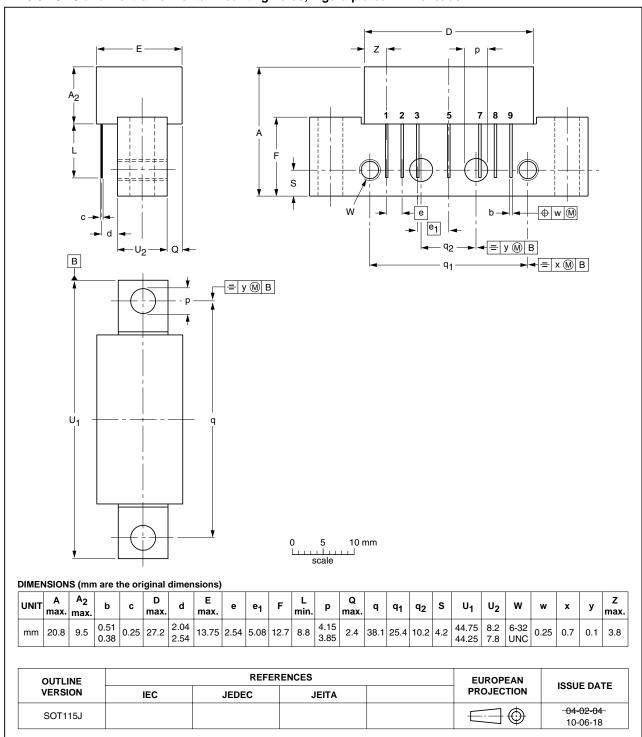
860 MHz, 21.5 dB gain power doubler amplifier

BGD906; BGD906MI

PACKAGE OUTLINE

Rectangular single-ended package; aluminium flange; 2 vertical mounting holes; 2 x 6-32 UNC and 2 extra horizontal mounting holes; 7 gold-plated in-line leads

SOT115J



860 MHz, 21.5 dB gain power doubler amplifier

BGD906; BGD906MI

DATA SHEET STATUS

DOCUMENT STATUS(1)	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
- The product status of device(s) described in this document may have changed since this document was published
 and may differ in case of multiple devices. The latest product status information is available on the Internet at
 URL http://www.nxp.com.

DISCLAIMERS

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe

property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

860 MHz, 21.5 dB gain power doubler amplifier

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

ee data — The Quick reference data is an

BGD906; BGD906MI

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Non-automotive qualified products — Unless this data sheet expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

NXP Semiconductors

provides High Performance Mixed Signal and Standard Product solutions that leverage its leading RF, Analog, Power Management, Interface, Security and Digital Processing expertise

Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

For additional information please visit: http://www.nxp.com
For sales offices addresses send e-mail to: salesaddresses@nxp.com

© NXP B.V. 2010

All rights are reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.

The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent- or other industrial or intellectual property rights.

Printed in The Netherlands 613518/04/pp11 Date of release: 2001 Nov 01 Document order number: 9397 750 08863

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

NXP: BGD906