

# BGY585A

# 550 MHz, 18.2 dB gain push-pull amplifier Rev. 6 — 29 September 2010

Product data sheet

#### 1. **Product profile**

# 1.1 General description

Hybrid amplifier module for CATV systems operating over a frequency range of 40 MHz to 550 MHz at a voltage supply of 24 V (DC). Intended for use as a final amplifier.

### 1.2 Features and benefits

- Excellent linearity
- Extremely low noise
- Silicon nitride passivation
- Rugged construction
- Optimal reliability ensured by TiPtAu metallized crystals

#### 1.3 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$G_p$	power gain	f = 50 MHz	17.7	-	18.7	dB
		f = 550 MHz	18.8	-	20	dB
I <sub>tot</sub>	total current consumption (DC)	V <sub>B</sub> = 24 V	-	220	240	mA

#### **Pinning information** 2.

Table 2. **Pinning** 

Pin	Description	Simplified outline	Symbol
1	input		
2	common	1 3 5 7 9	5
3	common		1 9
5	+V <sub>B</sub>		2 3 7 8
7	common		2 3 7 8 sym095
8	common		
9	output		



## 550 MHz, 18.2 dB gain push-pull amplifier

# 3. Ordering information

Table 3. Ordering information

Туре	Package		Version		
number	Name	Description	Version		
BGY585A	-	rectangular single-ended package; aluminium flange; 2 vertical mounting holes; 2 × 6-32 UNC and 2 extra horizontal mounting holes; 7 gold-plated in-line leads	SOT115J		

# 4. Limiting values

### Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{i}$	RF input voltage		-	65	dBmV
T <sub>stg</sub>	storage temperature		-40	+100	°C
T <sub>case</sub>	case operating temperature		-20	+100	°C

# 5. Characteristics

**Table 5. Characteristics** 

 $T_{case} = 30$  °C;  $Z_S = Z_L = 75 \Omega$ .

Symbol	Parameter	Conditions	Mir	тур Тур	Max	Unit
Bandwidth	40 MHz to 550 MHz					
$G_p$	power gain	f = 50 MHz	17.	7 -	18.7	dB
		f = 550 MHz	18.	8 -	20	dB
SL	slope cable equivalent	f = 40 MHz to 550 MHz	0.5	-	2	dB
FL	flatness of frequency response	f = 40 MHz to 550 MHz	-	-	±0.2	dB
S <sub>11</sub>	input return losses	f = 40 MHz to 80 MHz	20	-	-	dB
		f = 80 MHz to 160 MHz	19	-	-	dB
		f = 160 MHz to 550 MHz	18	-	-	dB
\$22	output return losses	f = 40 MHz to 80 MHz	20	-	-	dB
		f = 80 MHz to 160 MHz	19	-	-	dB
		f = 160 MHz to 550 MHz	18	-	-	dB
СТВ	composite triple beat	77 channels flat; $V_0 = 44 \text{ dBmV}$ ; measured at 547.25 MHz	-	-	-59	dB
X <sub>mod</sub>	cross modulation	77 channels flat; $V_0 = 44 \text{ dBmV}$ ; measured at 55.25 MHz	-	-	-62	dB
CSO	composite second order distortion	77 channels flat; $V_0 = 44 \text{ dBmV}$ ; measured at 548.5 MHz	-	-	-59	dB
$d_2$	second order distortion		<u>[1]</u> -	-	-72	dB
Vo	output voltage	$d_{im} = -60 \text{ dB}$	<u>[2]</u> 61.	5 -	-	dBmV
F	noise figure	f = 550 MHz	-	-	8	dB
I <sub>tot</sub>	total current consumption (DC)	V <sub>B</sub> = 24 V	[3] -	220	240	mA

### 550 MHz, 18.2 dB gain push-pull amplifier

 Table 5.
 Characteristics ...continued

 $T_{case} = 30$  °C;  $Z_S = Z_L = 75 \Omega$ .

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Bandwidth	40 MHz to 450 MHz					
$G_p$	power gain	f = 50 MHz	17.7	-	18.7	dB
		f = 450 MHz	18.6	-	19.8	dB
SL	slope cable equivalent	f = 40 MHz to 450 MHz	0.5	-	1.8	dB
FL	flatness of frequency response	f = 40 MHz to 450 MHz	-	-	±0.2	dB
S <sub>11</sub>	input return losses	f = 40 MHz to 80 MHz	20	-	-	dB
		f = 80 MHz to 160 MHz	19	-	-	dB
		f = 160 MHz to 450 MHz	18	-	-	dB
S <sub>22</sub>	output return losses	f = 40 MHz to 80 MHz	20	-	-	dB
		f = 80 MHz to 160 MHz	19	-	-	dB
		f = 160 MHz to 450 MHz	18	-	-	dB
СТВ	composite triple beat	60 channels flat; $V_0 = 46 \text{ dBmV}$ ; measured at 445.25 MHz	-	-	<del>-</del> 61	dB
$X_{mod}$	cross modulation	60 channels flat; $V_0 = 46 \text{ dBmV}$ ; measured at 55.25 MHz	-	-	<del>-</del> 61	dB
CSO	composite second order distortion	60 channels flat; $V_0 = 46 \text{ dBmV}$ ; measured at 446.5 MHz	-	-	<del>-</del> 61	dB
d <sub>2</sub>	second order distortion		<u>[4]</u> _	-	<b>-75</b>	dB
Vo	output voltage	$d_{im} = -60 \text{ dB}$	<u>[5]</u> 64	-	-	dBmV
F	noise figure	f = 450 MHz	-	-	7	dB
I <sub>tot</sub>	total current consumption (DC)	V <sub>B</sub> = 24 V	<u>[3]</u> _	220	240	mA

<sup>[1]</sup>  $f_p = 55.25$  MHz;  $V_p = 44$  dBmV;  $f_q = 493.25$  MHz;  $V_q = 44$  dBmV; measured at  $f_p + f_q = 548.5$  MHz.

<sup>[2]</sup> Measured according to DIN45004B;  $f_p$  = 540.25 MHz;  $V_p$  =  $V_o$ ;  $f_q$  = 547.25 MHz;  $V_q$  =  $V_o$  -6 dB;  $f_r$  = 549.25 MHz;  $V_r$  =  $V_o$  -6 dB; measured at  $f_p$  +  $f_q$  -  $f_r$  = 538.25 MHz.

<sup>[3]</sup> The module normally operates at  $V_B = 24$  V, but is able to withstand supply transients up to 30 V.

<sup>[4]</sup>  $f_p = 55.25$  MHz;  $V_p = 46$  dBmV;  $f_q = 391.25$  MHz;  $V_q = 46$  dBmV; measured at  $f_p + f_q = 446.5$  MHz.

<sup>[5]</sup> Measured according to DIN45004B;  $f_p$  = 440.25 MHz;  $V_p$  =  $V_o$ ;  $f_q$  = 447.25 MHz;  $V_q$  =  $V_o$  -6 dB;  $f_r$  = 449.25 MHz;  $V_r$  =  $V_o$  -6 dB; measured at  $f_p$  +  $f_q$  -  $f_r$  = 438.25 MHz.

550 MHz, 18.2 dB gain push-pull amplifier

# 6. Package outline

Rectangular single-ended package; aluminium flange; 2 vertical mounting holes; 2 x 6-32 UNC and 2 extra horizontal mounting holes; 7 gold-plated in-line leads

SOT115J

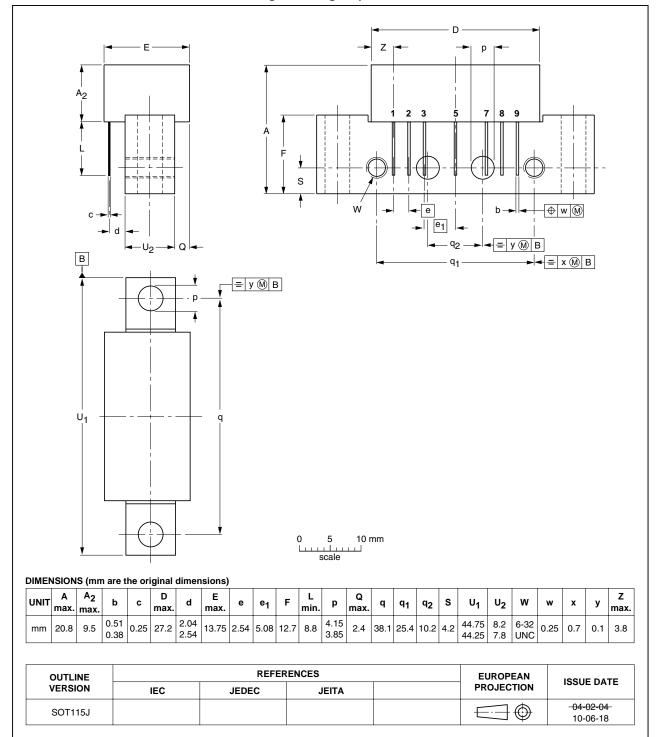


Fig 1. Package outline SOT115J

BGY585A

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# 550 MHz, 18.2 dB gain push-pull amplifier

# 7. Revision history

### Table 6. Revision history

Release date	Data sheet status	Change notice	Supersedes
20100929	Product data sheet	-	BGY585A v.5
of NXP Semi	conductors.		, ,
ū	•	• •	• • •
• Package outil	me drawings have been upda	ted to the latest version	11.
20050124	Product data sheet	-	BGY585A v.4
20011018	Product specification	-	BGY585A v.3
19990326	Product specification	-	BGY585A v.2
	20100929  The format of of NXP Semice Legal texts has Package outline 20050124  20011018	<ul> <li>20100929 Product data sheet</li> <li>The format of this data sheet has been rede of NXP Semiconductors.</li> <li>Legal texts have been adapted to the new endowned Package outline drawings have been updated to the semiconduct data sheet</li> <li>20050124 Product data sheet</li> <li>Product specification</li> </ul>	<ul> <li>20100929 Product data sheet -</li> <li>The format of this data sheet has been redesigned to comply with of NXP Semiconductors.</li> <li>Legal texts have been adapted to the new company name where</li> <li>Package outline drawings have been updated to the latest version</li> <li>20050124 Product data sheet -</li> <li>20011018 Product specification -</li> </ul>

#### 550 MHz, 18.2 dB gain push-pull amplifier

# 8. Legal information

#### 8.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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BGY585A

### 550 MHz, 18.2 dB gain push-pull amplifier

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# 550 MHz, 18.2 dB gain push-pull amplifier

# 10. Contents

1	Product profile	1
1.1	General description	1
1.2	Features and benefits	1
1.3	Quick reference data	1
2	Pinning information	1
3	Ordering information	2
4	Limiting values	2
5	Characteristics	2
6	Package outline	4
7	Revision history	5
8	Legal information	6
8.1	Data sheet status	ĉ
8.2	Definitions	ĉ
8.3	Disclaimers	ĉ
8.4	Trademarks	7
9	Contact information	7
10	Contents	Q

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