# **BTA204-600E**



3Q Hi-Com Triac Rev. 5 — 9 May 2011

Product data sheet

#### 1. **Product profile**

### 1.1 General description

Planar passivated high commutation three quadrant triac in a SOT78 (TO-220AB) plastic package. This "series E" triac balances the requirements of commutation performance and gate sensitivity and is intended for interfacing with low power drivers and logic ICs including microcontrollers.

#### 1.2 Features and benefits

- 3Q technology for improved noise immunity
- Direct triggering from low power drivers and logic ICs
- High blocking voltage capability
- High commutation capability
- Planar passivated for voltage ruggedness and reliability
- Sensitive gate for easy logic level triggering
- Triggering in three quadrants only

### 1.3 Applications

- AC solenoids
- General purpose motor control circuits

Home appliances

#### 1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{DRM}$	repetitive peak off-state voltage		-	-	600	V
I <sub>TSM</sub>	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25 ^{\circ}C$ ; $t_p = 20  \text{ms}$ ; see Figure 4; see Figure 5	-	-	25	Α
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 107 °C; see <u>Figure 1</u> ; see <u>Figure 2</u> ; see <u>Figure 3</u>	-	-	4	Α



Table 1. Quick reference data ...continued

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	aracteristics					
I <sub>GT</sub> ga	gate trigger current	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 °C; see <u>Figure 7</u>	-	-	10	mA
		$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2 + G -;$ $T_j = 25 \text{ °C; see } \frac{\text{Figure 7}}{ Company of the compan$	-	-	10	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2- \text{G-};$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 7}}{\text{Figure 7}}$	-	-	10	mA

## 2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1		<b>N</b> I
2	T2	main terminal 2	mb	T2 — T1
3	G	gate		`G sym051
mb	T2	mounting base; main terminal 2	1 2 3	
			SOT78 (TO-220AB)	

## 3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BTA204-600E	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

## 4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

		- ,			
Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DRM}$	repetitive peak off-state voltage		-	600	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 107 °C; see <u>Figure 1</u> ; see <u>Figure 2</u> ; see <u>Figure 3</u>	-	4	Α
I <sub>TSM</sub>	non-repetitive peak on-state current	full sine wave; T <sub>j(init)</sub> = 25 °C; t <sub>p</sub> = 20 ms; see <u>Figure 4</u> ; see <u>Figure 5</u>	-	25	Α
		full sine wave; $T_{j(init)} = 25 \text{ °C}$ ; $t_p = 16.7 \text{ ms}$	-	27	Α
I <sup>2</sup> t	I <sup>2</sup> t for fusing	t <sub>p</sub> = 10 ms; sine-wave pulse	-	3.1	$A^2s$
dI <sub>T</sub> /dt	rate of rise of on-state current	$I_T = 6 \text{ A}; I_G = 0.2 \text{ A}; dI_G/dt = 0.2 \text{ A/}\mu\text{s}$	-	100	A/µs
I <sub>GM</sub>	peak gate current		-	2	А
P <sub>GM</sub>	peak gate power		-	5	W
P <sub>G(AV)</sub>	average gate power	over any 20 ms period	-	0.5	W
T <sub>stg</sub>	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C

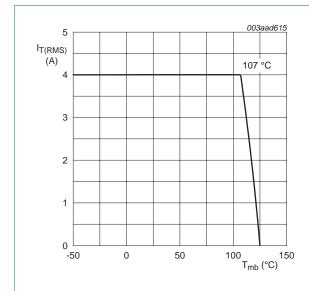
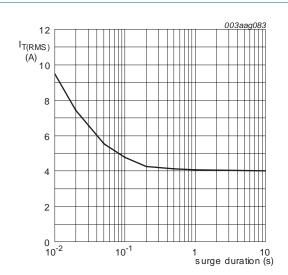


Fig 1. RMS on-state current as a function of mounting base temperature; maximum values



f = 50 Hz;  $T_{mb}$  = 107 °C

Fig 2. RMS on-state current as a function of surge duration; maximum values

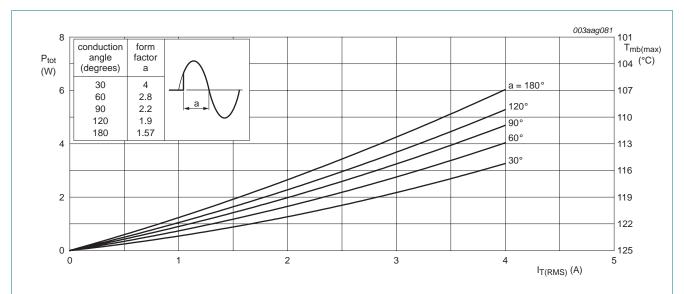


Fig 3. Total power dissipation as a function of RMS on-state current; maximum values

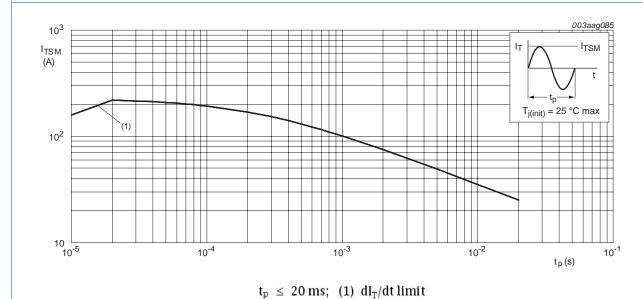
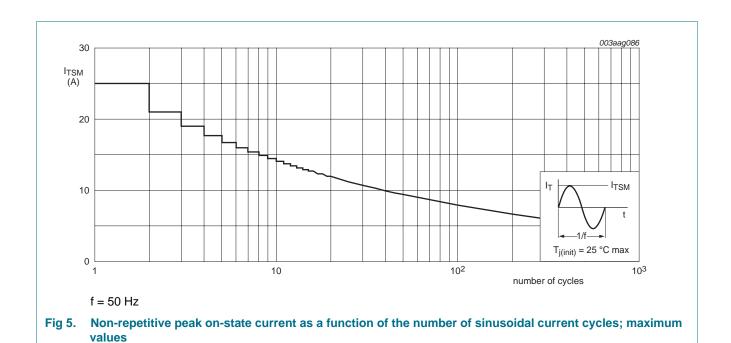


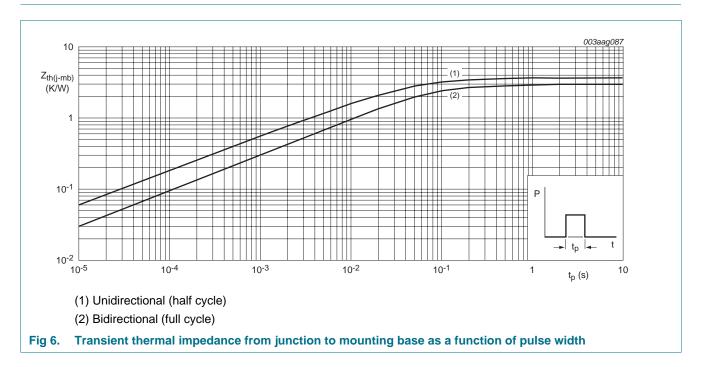
Fig 4. Non-repetitive peak on-state current as a function of pulse width; maximum values



### 5. Thermal characteristics

Table 5. Thermal characteristics

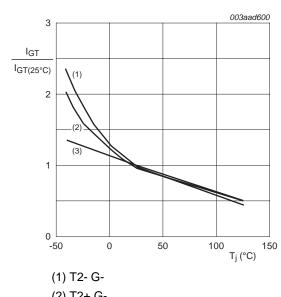
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
ui(j iiib)	thermal resistance from	full cycle; see Figure 6	-	-	3	K/W
	junction to mounting base	half cycle; see Figure 6	-	-	3.7	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	-	60	-	K/W



## 6. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	racteristics					
I <sub>GT</sub>	gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2+ G+;$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 7}}{}$	-	-	10	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2+ \text{ G-};$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 7}}{}$	-	-	10	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2- \text{G-};$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 7}}{}$	-	-	10	mA
IL	latching current	$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; T2+ G+;$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 8}}{}$	-	-	12	mA
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; T2+ G-;$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 8}}{}$	-	-	18	mA
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; \text{ T2- G-};$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 8}}{}$	-	-	12	mA
I <sub>H</sub>	holding current	$V_D = 12 \text{ V; } T_j = 25 \text{ °C; see } \frac{\text{Figure 9}}{\text{ or } 100 \text{ J}}$	-	-	12	mΑ
V <sub>T</sub>	on-state voltage	$I_T = 5 \text{ A}$ ; $T_j = 25 \text{ °C}$ ; see Figure 10	-	1.4	1.7	V
V <sub>GT</sub> gate	gate trigger voltage	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 \text{ °C};$ see Figure 11	-	0.7	1.5	V
		$V_D = 400 \text{ V}; I_T = 0.1 \text{ A}; T_j = 125 \text{ °C};$ see Figure 11	0.25	0.4	-	V
I <sub>D</sub>	off-state current	V <sub>D</sub> = 600 V; T <sub>j</sub> = 125 °C	-	0.1	0.5	mΑ
Dynamic	characteristics					
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 402 V; $T_j$ = 125 °C; exponential waveform; gate open circuit	30	-	-	V/µs
dI <sub>com</sub> /dt	rate of change of commutating current	$V_D = 400 \text{ V}; T_j = 125 ^{\circ}\text{C}; I_{T(RMS)} = 4 \text{ A};$ $dV_{com}/dt = 0.1 \text{ V/}\mu\text{s}; gate open circuit}$	8	-	-	A/ms
		$V_D = 400 \text{ V}; T_j = 125 ^{\circ}\text{C}; I_{T(RMS)} = 4 \text{ A};$ $dV_{com}/dt = 10 \text{ V}/\mu\text{s}; gate open circuit}$	2.1	-	-	A/m:
t <sub>gt</sub>	gate-controlled turn-on time	$I_{TM} = 12 \text{ A}; V_D = 600 \text{ V}; I_G = 0.1 \text{ A};$ $dI_G/dt = 5 \text{ A/}\mu\text{s}$	-	2	-	μs



- (2) T2+ G-
- (3) T2+ G+

Fig 7. Normalized gate trigger current as a function of junction temperature

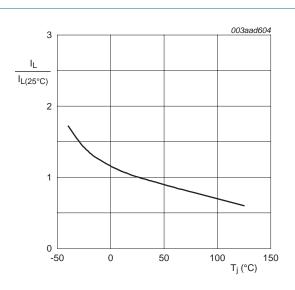
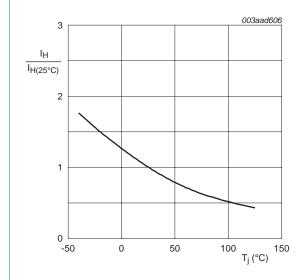
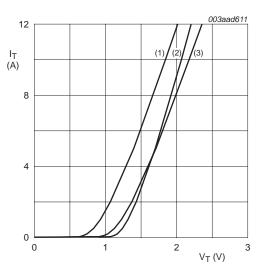


Fig 8. Normalized latching current as a function of junction temperature



Normalized holding current as a function of Fig 9. junction temperature



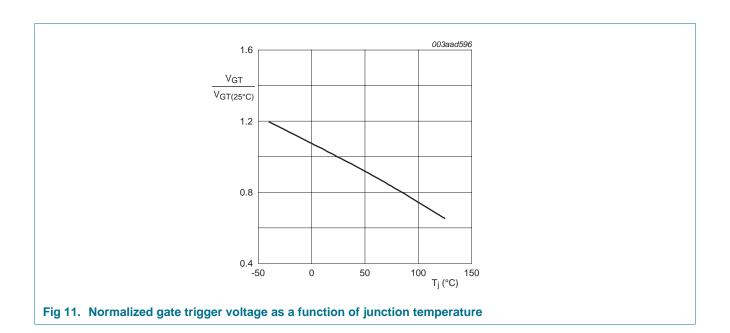
Vo = 1.27 V; Rs = 0.091  $\Omega$ 

(1) Tj = 125 °C; typical values

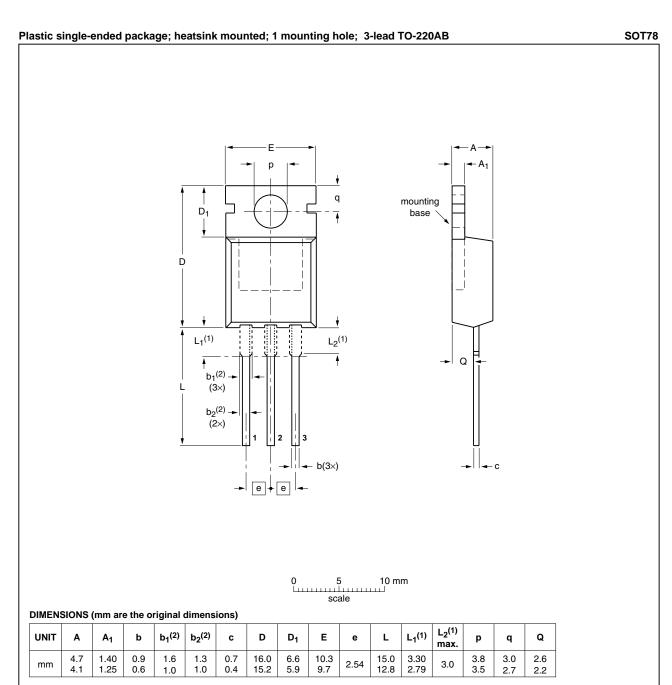
(2) Tj = 125 °C; maximum values

(3) Tj = 25 °C; maximum values

Fig 10. On-state current as a function of on-state voltage



### 7. Package outline



#### Notes

- 1. Lead shoulder designs may vary.
- 2. Dimension includes excess dambar.

OUTLINE		REFER	ENCES	EUROPEAN ISSUE DA	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE	
SOT78		3-lead TO-220AB	SC-46		<del>08-04-23</del> 08-06-13	

Fig 12. Package outline SOT78 (TO-220AB)

BTA204-600E

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## 8. Revision history

### Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BTA204-600E v.5	20110509	Product data sheet	-	BTA204_SERIES_D_E_F_4
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the new ident guidelines of NXP Semiconductors.</li> </ul>			
	<ul> <li>Legal text</li> </ul>	s have been adapted to t	he new company r	name where appropriate.
	<ul><li>Type num</li></ul>	ber BTA204-600E separa	ated from data she	et BTA204_SERIES_D_E_F_4.
BTA204_SERIES_D_E_F_4	20030501	Product specification	-	BTA204_SERIES_D_E_F_3

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Document status [1] [2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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