

16 A Three-quadrant triacs high commutation Rev. 01 — 18 April 2007

Product data sheet

Product profile

1.1 General description

Passivated, new generation, high commutation triacs in a SOT78 plastic package

1.2 Features

- Sensitive gate
- Very high commutation performance maximized at each gate sensitivity
- High immunity to dV/dt

1.3 Applications

- High power motor control e.g. washing
 Refrigeration and air conditioning machines and vacuum cleaners
- Electronic thermostats

compressors

1.4 Quick reference data

- $V_{DRM} \le 600 \text{ V (BTA316-600D/E)}$
- $V_{DRM} \le 800 \text{ V (BTA316-800E)}$
- $I_{TSM} \le 140 \text{ A (t = 20 ms)}$
- I_{GT} \leq 10 mA (BTA316 series E)
- $I_{GT} \le 5 \text{ mA (BTA316-600D)}$
- $I_{T(RMS)} \le 16 A$

Pinning information

Table 1. **Pinning**

Pin	Description	Simplified outline	Symbol
1	main terminal 1 (T1)		
2	main terminal 2 (T2)	mb	T2—T1
3	gate (G)		sym051
mb	mounting base; main terminal 2 (T2)	1 2 3	
		SOT78 (TO-220AB))



3. Ordering information

Table 2. Ordering information

Type number	Package	Package					
	Name	Description	Version				
BTA316-600D	SC-46	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead	SOT78				
BTA316-600E		TO-220AB					
BTA316-800E							

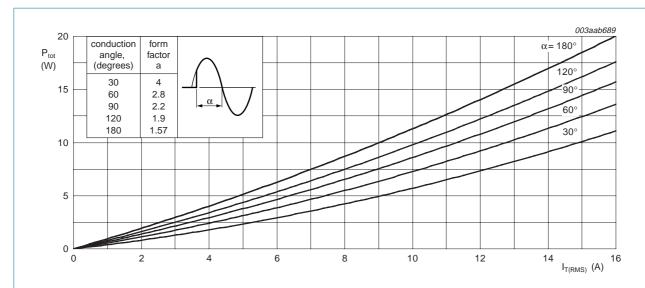
4. Limiting values

Table 3. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

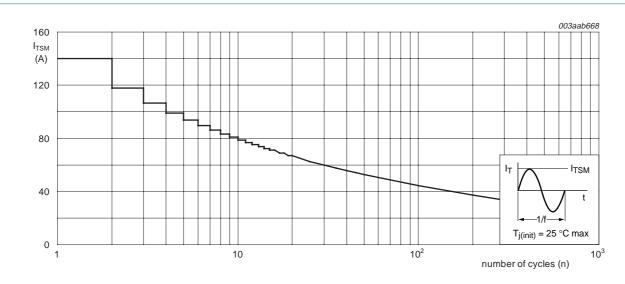
Symbol	Parameter	Conditions	Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage	BTA316-600D; BTA316-600E	<u>[1]</u> _	600	V
		BTA316-800E	-	800	V
I _{T(RMS)}	RMS on-state current	full sine wave; $T_{mb} \le 101$ °C; see Figure 4 and 5	-	16	Α
I _{TSM} non-repetitive peak on-state current		full sine wave; $T_j = 25$ °C prior to surge; see Figure 2 and 3			
		t = 20 ms	-	140	Α
		t = 16.7 ms	-	150	Α
l ² t	I ² t for fusing	t = 10 ms	-	98	A ² s
dI _T /dt	rate of rise of on-state current	$I_{TM} = 20 \text{ A}; I_G = 0.2 \text{ A};$ $dI_G/dt = 0.2 \text{ A}/\mu\text{s}$	-	100	A/μs
I _{GM}	peak gate current		-	2	Α
P_{GM}	peak gate power		-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	-	0.5	W
T _{stg}	storage temperature		-40	+150	°C
T _j	junction temperature		-	125	°C

^[1] Although not recommended, off-state voltages up to 800 V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15 A/μs.



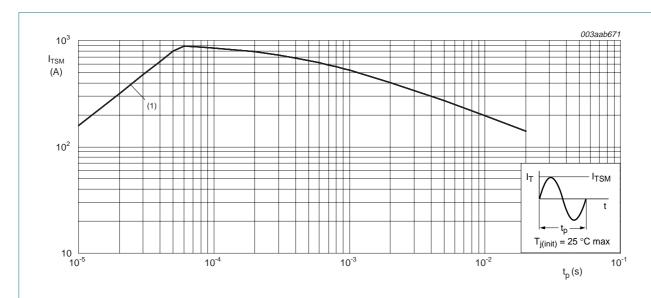
 α = conduction angle

Fig 1. Total power dissipation as a function of RMS on-state current; maximum values



f = 50 Hz

Fig 2. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values



 $t_p \le 20 \text{ ms}$ (1) $dI_T/dt \text{ limit}$

Fig 3. Non-repetitive peak on-state current as a function of pulse duration; maximum values

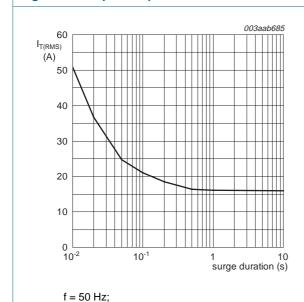


Fig 4. RMS on-state current as a function of surge duration; maximum values

 $T_{mb} = 101$ °C

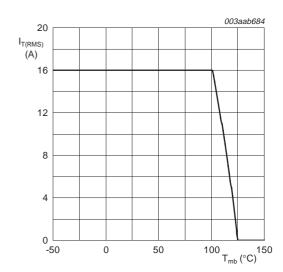
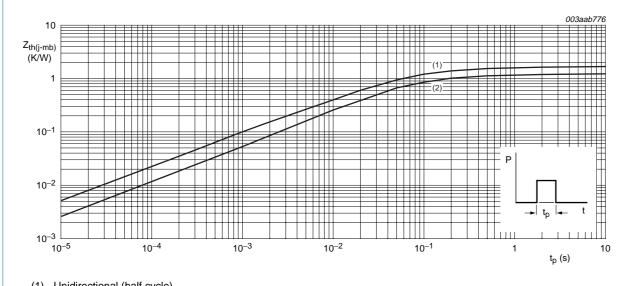


Fig 5. RMS on-state current as a function of mounting base temperature; maximum values

Thermal characteristics

Table 4. **Thermal characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-mb)}$		half cycle; see Figure 6	-	-	1.7	K/W
	mounting base	full cycle; see Figure 6	-	-	1.2	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	-	60	-	K/W



- (1) Unidirectional (half cycle)
- (2) Bidirectional (full cycle)

Fig 6. Transient thermal impedance from junction to mounting base as a function of pulse duration

6. Static characteristics

Table 5. Static characteristics

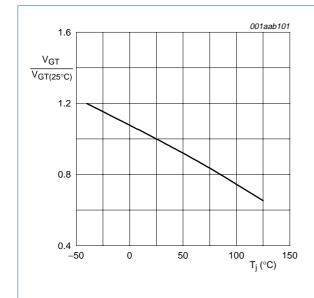
 $T_i = 25 \,^{\circ}C$ unless otherwise specified.

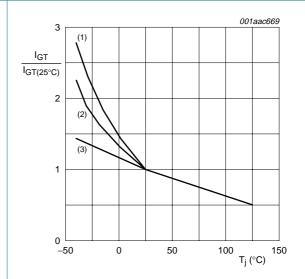
Symbol Parameter		Conditions		BTA316-600D			BTA316-600E BTA316-800E		
			Min	Тур	Max	Min	Тур	Max	
I_{GT}	gate trigger	$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; see } \frac{\text{Figure 8}}{}$							
	current	T2+ G+	-	-	5	-	-	10	mΑ
	T2+ G-	-	-	5	-	-	10	mΑ	
		T2- G-	-	-	5	-	-	10	mΑ
I _L latching current	V _D = 12 V; I _{GT} = 0.1 A; see <u>Figure 10</u>								
		T2+ G+	-	-	15	-	-	25	mΑ
		T2+ G-	-	-	25	-	-	30	mΑ
		T2- G-	-	-	25	-	-	30	mΑ
I _H	holding current	V _D = 12 V; I _{GT} = 0.1 A; see <u>Figure 11</u>	-	-	15	-	-	15	mΑ
V_{T}	on-state voltage	I _T = 18 A; see <u>Figure 9</u>	-	1.3	1.5	-	1.3	1.5	V
V_{GT}	/ _{GT} gate trigger	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; \text{ see } \frac{\text{Figure 7}}{}$	-	0.7	1.5	-	8.0	1.5	V
voltage		$V_D = 400 \text{ V}; I_T = 0.1 \text{ A}; T_j = 125 ^{\circ}\text{C}$	0.25	0.4	-	0.25	0.4	-	V
I _D	off-state current	$V_D = V_{DRM(max)}$; $T_j = 125 ^{\circ}C$	-	0.1	0.5	-	0.1	0.5	mΑ

7. Dynamic characteristics

Table 6. Dynamic characteristics

Symbol	Parameter	Conditions	BTA316-600D			BTA316-600E BTA316-800E			Unit
			Min	Тур	Max	Min	Тур	Max	
dV _D /dt	rate of rise of off-state voltage	$V_{DM} = 0.67 \times V_{DRM(max)}; T_j = 125 ^{\circ}C;$ exponential waveform; gate open circuit	30	-	-	60	-	-	V/μs
dl _{com} /dt rate of change of commutating current	$V_{DM} = 400 \text{ V}; T_j = 125 ^{\circ}\text{C}; I_{T(RMS)} = 16 \text{ A};$ without snubber; gate open circuit	1.5	-	-	5	-	-	A/ms	
	$V_{DM} = 400 \text{ V}; T_j = 125 ^{\circ}\text{C}; I_{T(RMS)} = 16 \text{ A};$ dV/dt = 10 V/ μ s; gate open circuit	3	-	-	8	-	-	A/ms	
		$V_{DM} = 400 \text{ V}; T_j = 125 ^{\circ}\text{C}; I_{T(RMS)} = 16 \text{ A};$ dV/dt = 1 V/ μ s; gate open circuit	8	-	-	12	-	-	A/ms
t _{gt}	gate-controlled turn-on time	I_{TM} = 20 A; V_D = $V_{DRM(max)}$; I_G = 0.1 A; dI_G/dt = 5 A/ μs	-	2	-	-	2	-	μs



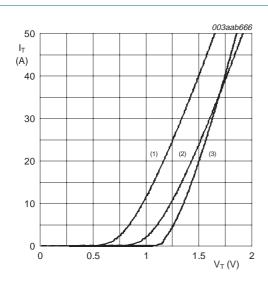


- (1) T2-G-
- (2) T2+ G-
- (3) T2+ G+

Fig 7. Normalized gate trigger voltage as a function of junction temperature

Fig 8. Normalized gate trigger current as a function of junction temperature

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 $V_0 = 1.024 \text{ V}$

 $R_s = 0.021 \Omega$

- (1) $T_i = 125$ °C; typical values
- (2) $T_j = 125 \,^{\circ}C$; maximum values
- (3) $T_j = 25$ °C; maximum values

Fig 9. On-state current as a function of on-state voltage

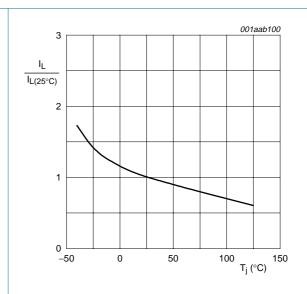


Fig 10. Normalized latching current as a function of junction temperature

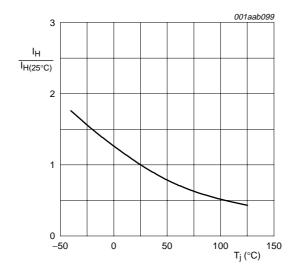
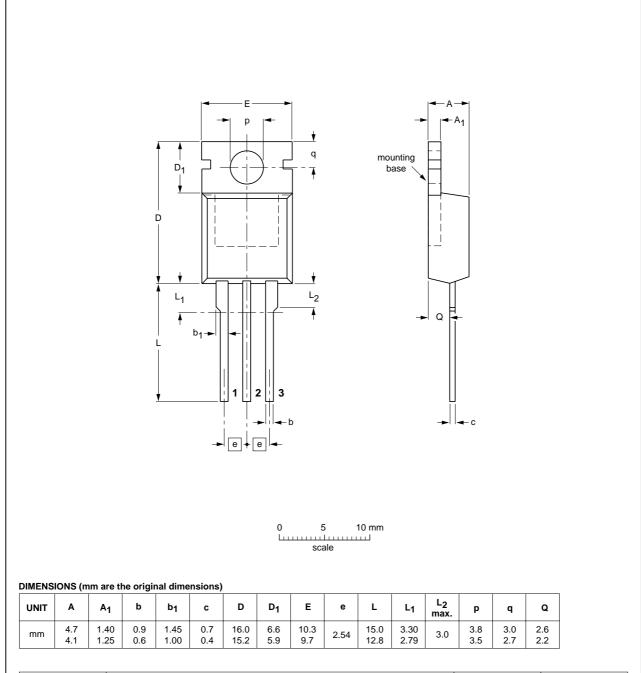


Fig 11. Normalized holding current as a function of junction temperature

8. Package outline

Plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB

SOT78



OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE
SOT78		3-lead TO-220AB	SC-46		05-03-22 05-10-25

Fig 12. Package outline SOT78 (3-lead TO-220AB)

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9. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BTA316_SER_D_E_1	20070418	Product data sheet	-	-

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10.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions"
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