



BUK9214-30A

N-channel TrenchMOS logic level FET

Rev. 3 — 14 June 2012

Product data sheet

1. Product profile

1.1 General description

Logic level N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using TrenchMOS technology. This product has been designed and qualified to the appropriate AEC standard for use in automotive critical applications.

1.2 Features and benefits

- Low conduction losses due to low on-state resistance
- Q101 compliant
- Suitable for logic level gate drive sources
- Suitable for thermally demanding environments due to 175 °C rating

1.3 Applications

- 12 V loads
- Automotive and general purpose power switching
- Motors, lamps and solenoids

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{DS}	drain-source voltage	$T_j \geq 25 \text{ }^\circ\text{C}; T_j \leq 175 \text{ }^\circ\text{C}$	-	-	30	V
I_D	drain current	$V_{GS} = 5 \text{ V}; T_{mb} = 25 \text{ }^\circ\text{C}$; see Figure 1 ; see Figure 3	-	-	63	A
P_{tot}	total power dissipation	$T_{mb} = 25 \text{ }^\circ\text{C}$; see Figure 2	-	-	107	W
Static characteristics						
R_{DSon}	drain-source on-state resistance	$V_{GS} = 4.5 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ }^\circ\text{C}$	-	-	15.5	$\text{m}\Omega$
		$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ }^\circ\text{C}$	-	9	12	$\text{m}\Omega$
		$V_{GS} = 5 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ }^\circ\text{C}$; see Figure 11 ; see Figure 12	-	11	14	$\text{m}\Omega$
Dynamic characteristics						
Q_{GD}	gate-drain charge	$V_{GS} = 5 \text{ V}; I_D = 25 \text{ A}; V_{DS} = 24 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$; see Figure 13	-	12.2	-	nC
Avalanche ruggedness						
$E_{DS(AL)S}$	non-repetitive drain-source avalanche energy	$I_D = 63 \text{ A}; V_{sup} \leq 30 \text{ V}; R_{GS} = 50 \Omega; V_{GS} = 5 \text{ V}; T_{j(init)} = 25 \text{ }^\circ\text{C}$; unclamped	-	-	230	mJ



2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		
2	D	drain		
3	S	source		
mb	D	mounting base; connected to drain		

SOT428 (DPAK)

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BUK9214-30A	DPAK	plastic single-ended surface-mounted package (DPAK); 3 leads (one lead cropped)	SOT428

4. Marking

Table 4. Marking codes

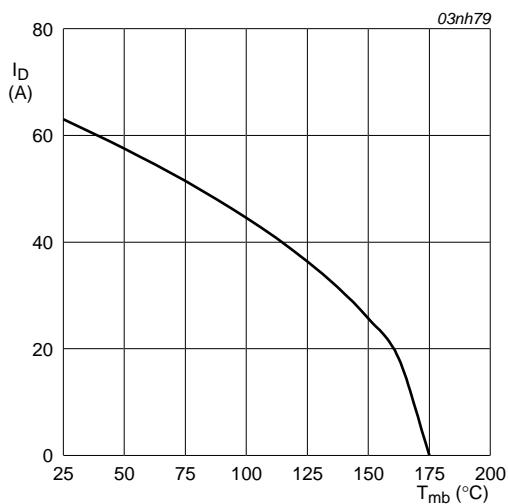
Type number	Marking code
BUK9214-30A	BUK9214-30A

5. Limiting values

Table 5. Limiting values

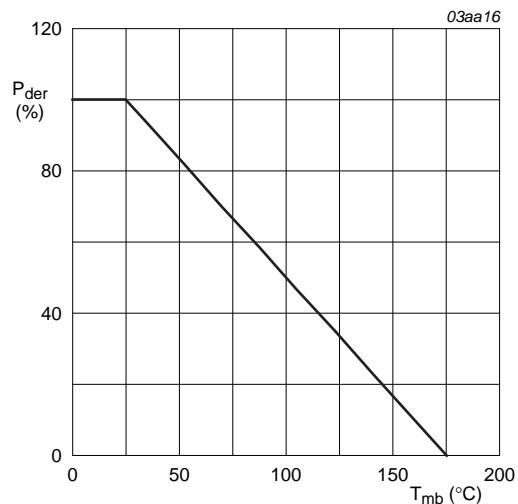
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage	$T_j \geq 25^\circ\text{C}; T_j \leq 175^\circ\text{C}$	-	30	V
V_{DGR}	drain-gate voltage	$R_{GS} = 20 \text{ k}\Omega$	-	30	V
V_{GS}	gate-source voltage		-15	15	V
I_D	drain current	$T_{mb} = 25^\circ\text{C}; V_{GS} = 5 \text{ V}$; see Figure 1 ; see Figure 3	-	63	A
		$T_{mb} = 100^\circ\text{C}; V_{GS} = 5 \text{ V}$; see Figure 1	-	45	A
I_{DM}	peak drain current	$T_{mb} = 25^\circ\text{C}$; pulsed; $t_p \leq 10 \mu\text{s}$; see Figure 3	-	253	A
P_{tot}	total power dissipation	$T_{mb} = 25^\circ\text{C}$; see Figure 2	-	107	W
T_{stg}	storage temperature		-55	175	°C
T_j	junction temperature		-55	175	°C
Source-drain diode					
I_S	source current	$T_{mb} = 25^\circ\text{C}$	-	63	A
I_{SM}	peak source current	pulsed; $t_p \leq 10 \mu\text{s}$; $T_{mb} = 25^\circ\text{C}$	-	253	A
Avalanche ruggedness					
$E_{DS(AL)S}$	non-repetitive drain-source avalanche energy	$I_D = 63 \text{ A}$; $V_{sup} \leq 30 \text{ V}$; $R_{GS} = 50 \Omega$; $V_{GS} = 5 \text{ V}$; $T_{j(init)} = 25^\circ\text{C}$; unclamped	-	230	mJ



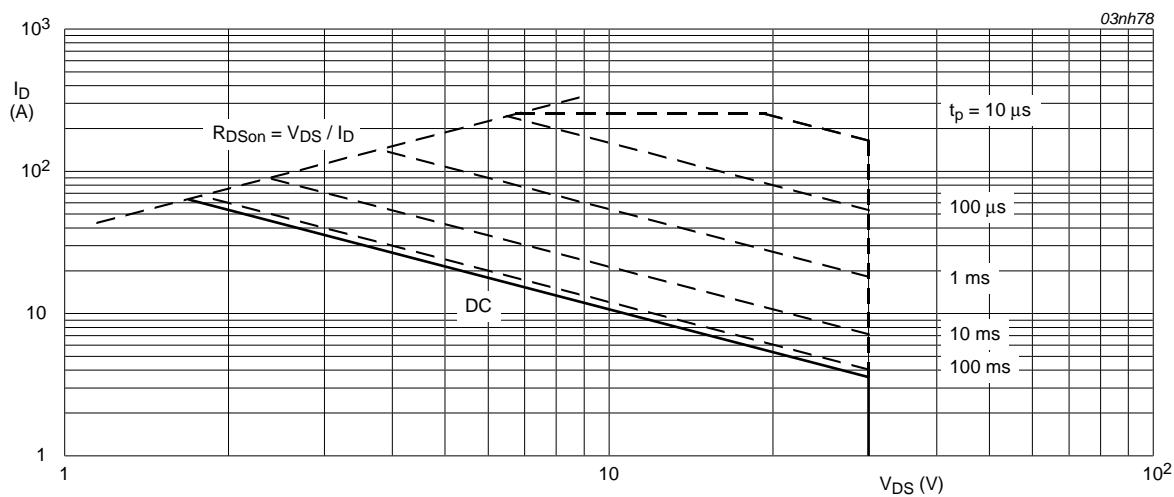
$$V_{GS} \geq 4.5V$$

Fig 1. Continuous drain current as a function of mounting base temperature



$$P_{der} = \frac{P_{tot}}{P_{tot}(25^\circ\text{C})} \times 100 \%$$

Fig 2. Normalized total power dissipation as a function of mounting base temperature



$T_{mb} = 25^\circ C$; I_{DM} is single pulse

Fig 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	see Figure 4	-	-	1.4	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient		-	71.4	-	K/W

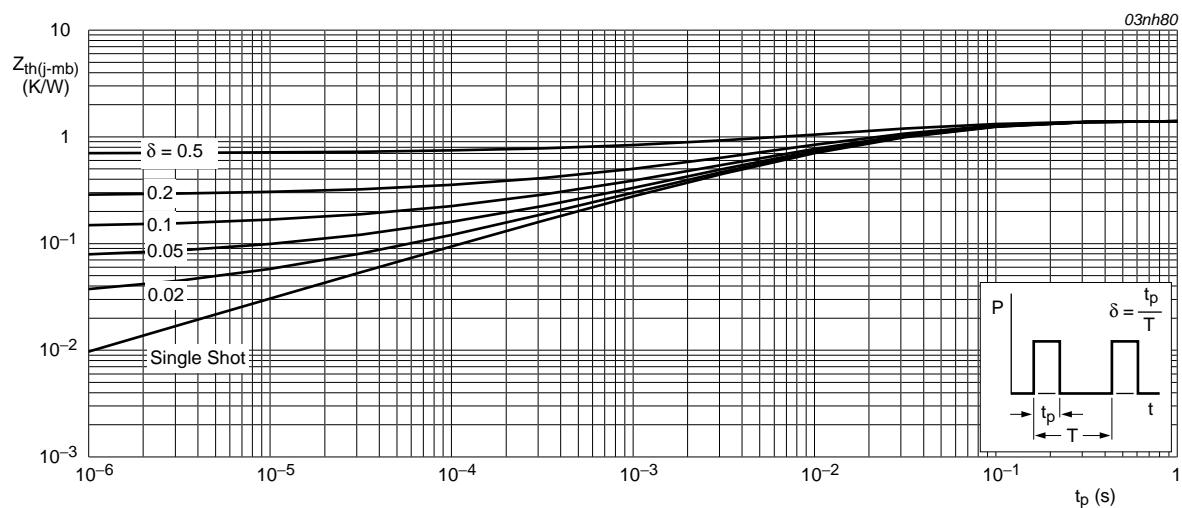
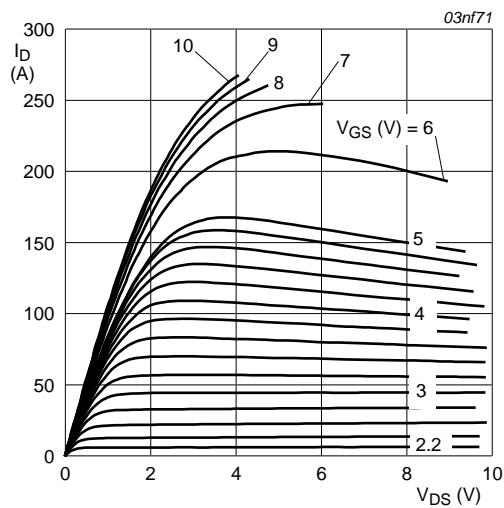


Fig 4. Transient thermal impedance from junction to mounting base as a function of pulse duration

7. Characteristics

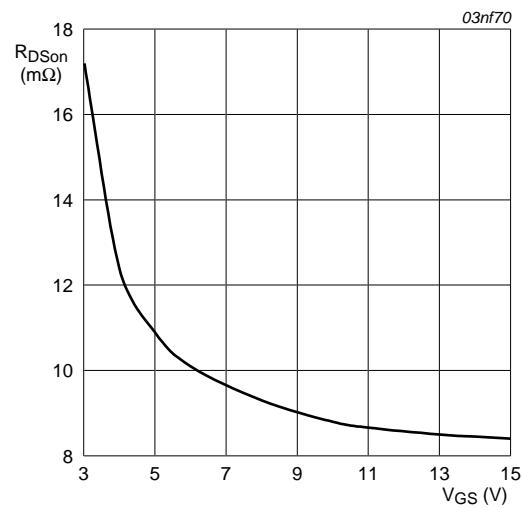
Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$ $I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = -55 \text{ }^\circ\text{C}$	30	-	-	V
$V_{GS(\text{th})}$	gate-source threshold voltage	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ }^\circ\text{C};$ see Figure 10 $I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ }^\circ\text{C};$ see Figure 10 $I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ }^\circ\text{C};$ see Figure 10	1	1.5	2	V
I_{DSS}	drain leakage current	$V_{DS} = 30 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 175 \text{ }^\circ\text{C}$ $V_{DS} = 30 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$	-	-	500	μA
I_{GSS}	gate leakage current	$V_{GS} = 10 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$ $V_{GS} = -10 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$	-	2	100	nA
R_{DSon}	drain-source on-state resistance	$V_{GS} = 4.5 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ }^\circ\text{C}$ $V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ }^\circ\text{C}$ $V_{GS} = 5 \text{ V}; I_D = 25 \text{ A}; T_j = 175 \text{ }^\circ\text{C};$ see Figure 11 ; see Figure 12 $V_{GS} = 5 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ }^\circ\text{C};$ see Figure 11 ; see Figure 12	-	9	12	$\text{m}\Omega$
Dynamic characteristics						
$Q_{G(\text{tot})}$	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 24 \text{ V}; V_{GS} = 5 \text{ V};$	-	31	-	nC
Q_{GS}	gate-source charge	$T_j = 25 \text{ }^\circ\text{C};$ see Figure 13	-	5.3	-	nC
Q_{GD}	gate-drain charge		-	12.2	-	nC
C_{iss}	input capacitance	$V_{GS} = 0 \text{ V}; V_{DS} = 25 \text{ V}; f = 1 \text{ MHz};$	-	1730	2317	pF
C_{oss}	output capacitance	$T_j = 25 \text{ }^\circ\text{C};$ see Figure 14	-	400	481	pF
C_{rss}	reverse transfer capacitance		-	260	365	pF
$t_{d(\text{on})}$	turn-on delay time	$V_{DS} = 30 \text{ V}; R_L = 1.2 \text{ }\Omega; V_{GS} = 5 \text{ V};$	-	10	-	ns
t_r	rise time	$R_{G(\text{ext})} = 10 \text{ }\Omega; T_j = 25 \text{ }^\circ\text{C}$	-	85	-	ns
$t_{d(\text{off})}$	turn-off delay time		-	94	-	ns
t_f	fall time		-	108	-	ns
L_D	internal drain inductance	from drain to centre of die ; $T_j = 25 \text{ }^\circ\text{C}$	-	2.5	-	nH
L_S	internal source inductance	from source lead to source bond pad ; $T_j = 25 \text{ }^\circ\text{C}$	-	7.5	-	nH
Source-drain diode						
V_{SD}	source-drain voltage	$I_S = 20 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C};$ see Figure 15	-	0.85	1.2	V
t_{rr}	reverse recovery time	$I_S = 20 \text{ A}; dI_S/dt = -100 \text{ A}/\mu\text{s};$ $V_{GS} = -10 \text{ V}; V_{DS} = 30 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$	-	83	-	ns
Q_r	recovered charge		-	119	-	nC



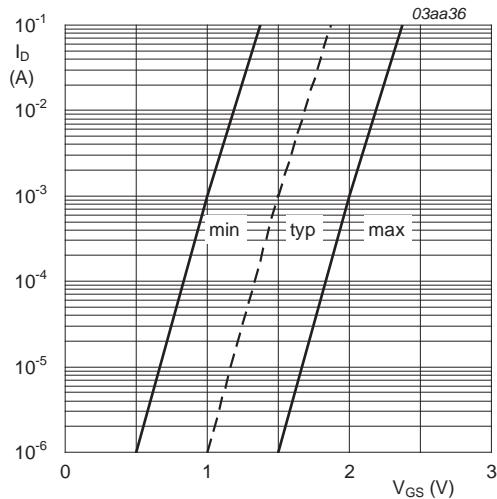
$T_j = 25^\circ C; t_p = 300\mu s$

Fig 5. Output characteristics: drain current as a function of drain-source voltage; typical values



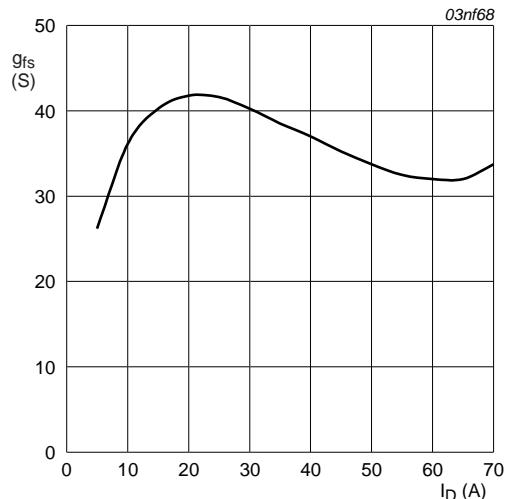
$T_j = 25^\circ C; I_D = 25A$

Fig 6. Drain-source on-state resistance as a function of gate-source voltage; typical values



$T_j = 25^\circ C; V_{DS} = 5 V$

Fig 7. Sub-threshold drain current as a function of gate-source voltage



$T_j = 25^\circ C; V_{DS} = 25V$

Fig 8. Forward transconductance as a function of drain current; typical values

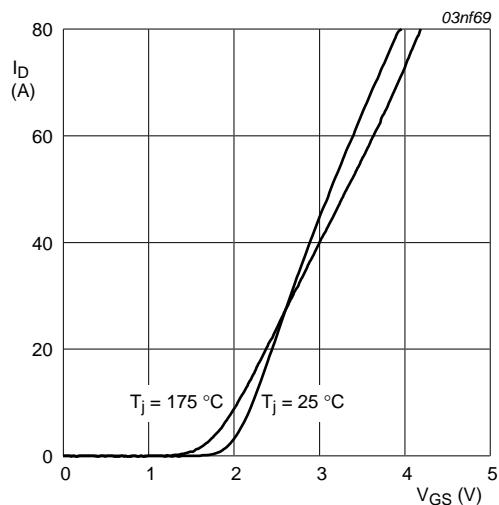


Fig 9. Transfer characteristics: drain current as a function of gate-source voltage; typical values

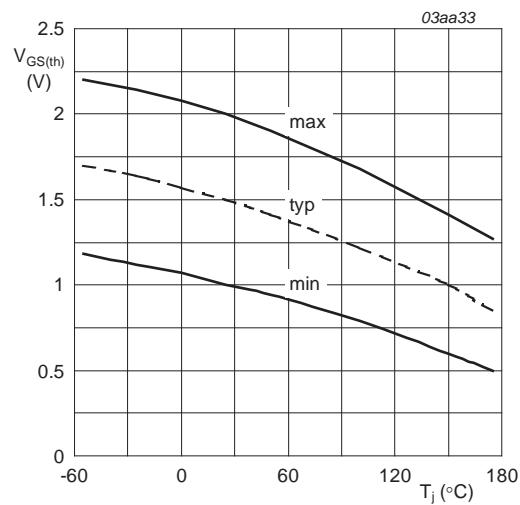


Fig 10. Gate-source threshold voltage as a function of junction temperature

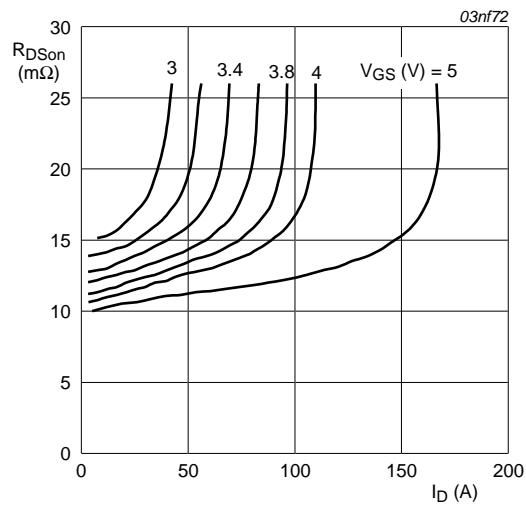


Fig 11. Drain-source on-state resistance as a function of drain current; typical values

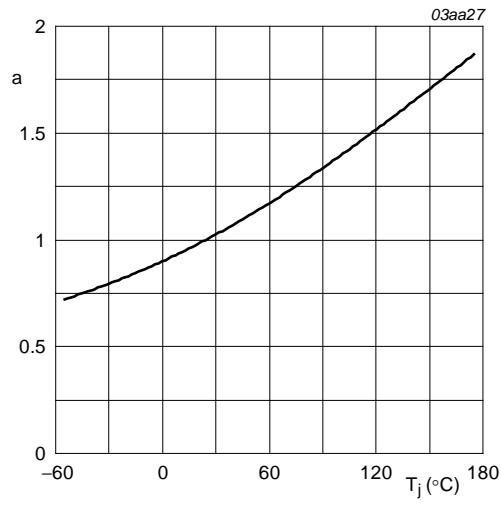
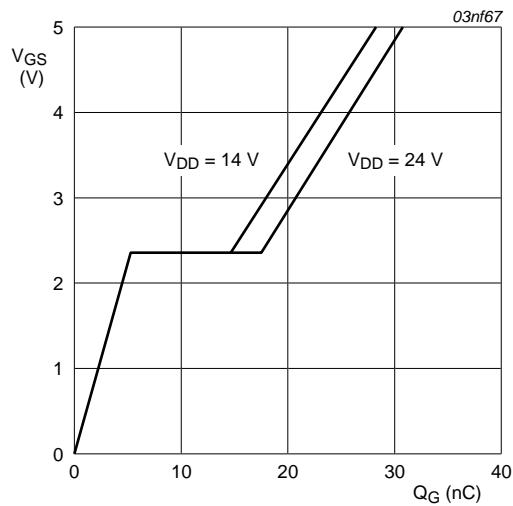
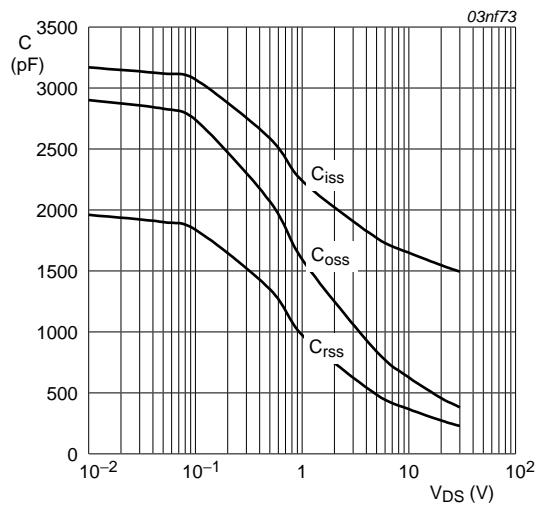


Fig 12. Normalized drain-source on-state resistance factor as a function of junction temperature



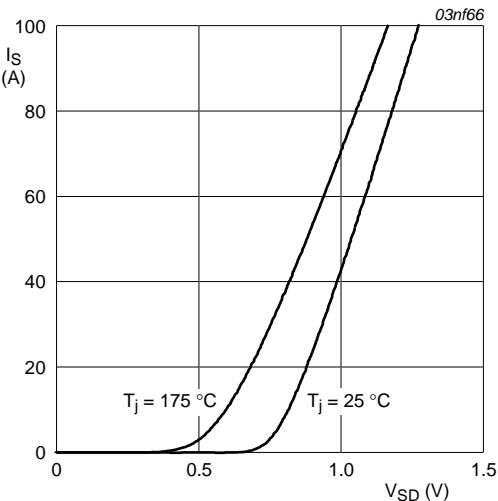
$T_j = 25^\circ\text{C}$; $I_D = 25\text{A}$

Fig 13. Gate-source voltage as a function of turn-on gate charge; typical values



$V_{GS} = 0\text{V}$; $f = 1\text{MHz}$

Fig 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



$V_{GS} = 0\text{V}$

Fig 15. Reverse diode current as a function of reverse diode voltage; typical values

8. Package outline

Plastic single-ended surface-mounted package (DPAK); 3 leads (one lead cropped)

SOT428

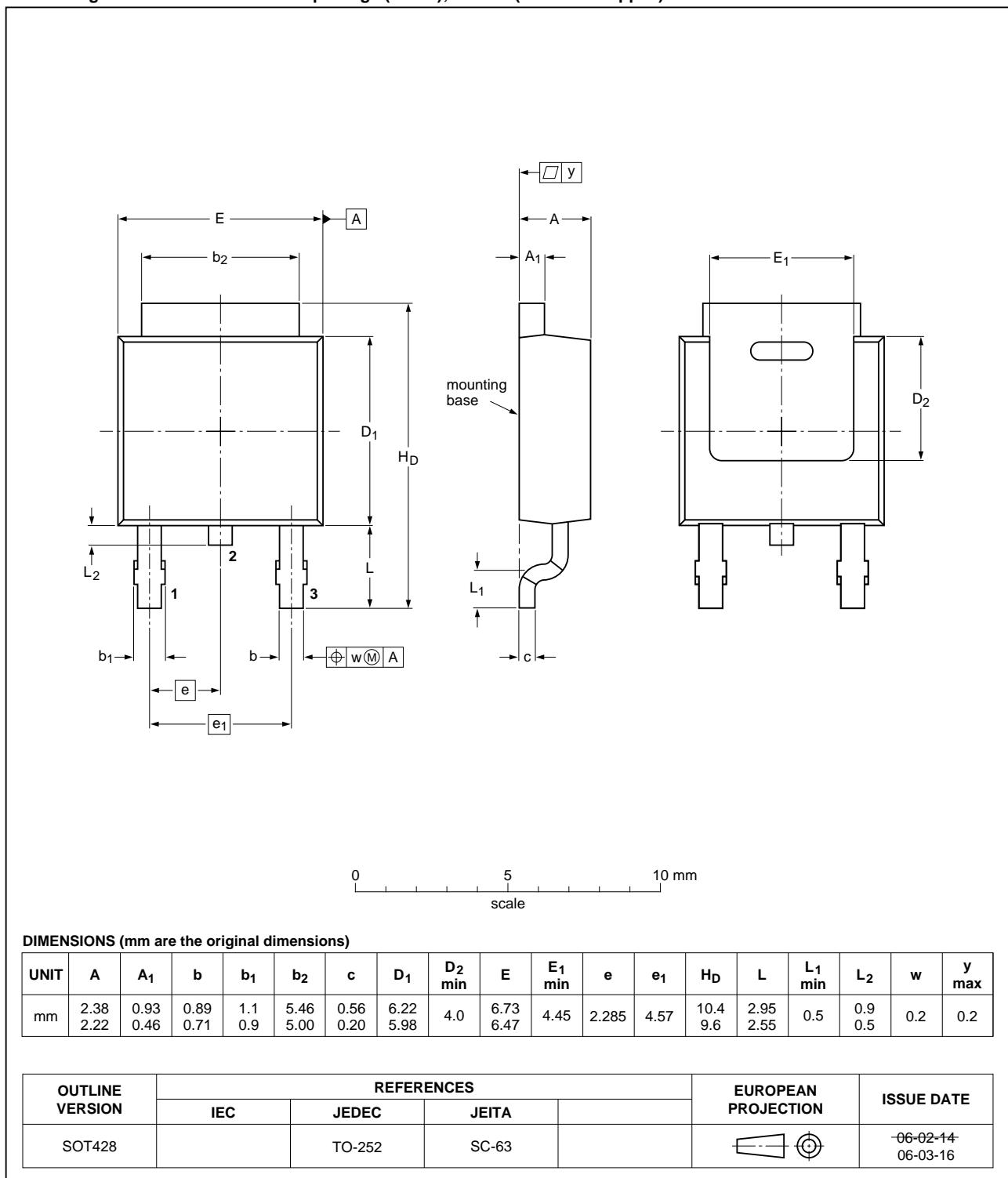


Fig 16. Package outline SOT428 (DPAK)

9. Revision history

Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BUK9214-30A v.3	20120614	Product data sheet	-	BUK9214-30A v.2
Modifications:		• Various changes to content.		
BUK9214-30A v.2	20100615	Product data sheet	-	BUK9214-30A v.1

10. Legal information

10.1 Data sheet status

Document status ^[1] [2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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For sales office addresses, please send an email to: salesaddresses@nxp.com

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