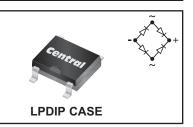
CBRLDSH2-40

SURFACE MOUNT HIGH DENSITY 2 AMP SILICON SCHOTTKY BRIDGE RECTIFIER





www.centralsemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CBRLDSH2-40 is a full wave Schottky bridge rectifier mounted in a low profile epoxy surface mount molded case, utilizing glass passivated chips.

MARKING CODE: CL40

APPLICATIONS:

- Input rectification
- Polarity protection
- · Steering diode array

FEATURES:

- Low leakage current (5.5μA TYP @ V_{RRM})
- High 2.0A current rating
- Low V_F Schottky diodes (480mV TYP @ I_F=2.0A)
- Low profile package (1.6mm MAX)

MAXIMUM RATINGS: (T _A =25°C unless otherwise noted) Peak Repetitive Reverse Voltage	SYMBOL V _{RRM}	40	UNITS V
DC Blocking Voltage	V_{R}	40	V
RMS Reverse Voltage	V _{R(RMS)}	28	V
Average Forward Current (T _L =50°C)	Ìο	2.0	Α
Peak Forward Surge Current	I _{FSM}	50	Α
Operating Junction Temperature	T_J	-55 to +125	°C
Storage Temperature	T _{stg}	-55 to +150	°C
Thermal Resistance	$\Theta_{\sf JA}$	95	°C/W

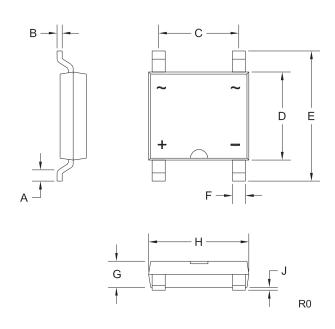
ELECTRICAL CHARACTERISTICS PER DIODE: (TA=25°C unless otherwise noted) SYMBOL **TEST CONDITIONS UNITS TYP** MAX V_R=40V 5.5 μΑ 100 I_R V_R =40V, T_A =100°C 2.0 20 I_R mΑ V_{F} I_F=2.0A 480 500 mV рF C_{J} V_R =4.0V, f=1.0MHz 155 250

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LPDIP CASE - MECHANICAL OUTLINE



DIMENSIONS						
	INCHES		MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
Α	0.019	0.028	0.50	0.70		
В	0.007	0.012	0.20	0.30		
С	0.153	0.162	3.90	4.10		
D	0.167	0.179	4.25	4.55		
E	0.248	0.264	6.30	6.70		
F	0.023	0.028	0.60	0.70		
G	0.045	0.058	1.15	1.45		
Н	0.190	0.203	4.85	5.15		
J	0.002	0.006	0.05	0.15		
LPDIP (REV: R0)						

MARKING CODE: CL40

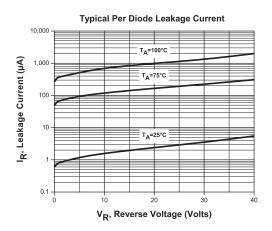
R1 (26-November 2012)

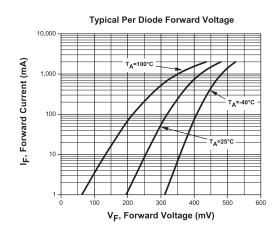
CBRLDSH2-40

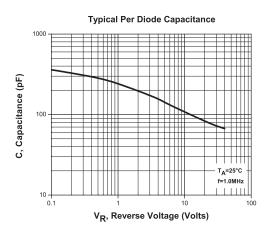
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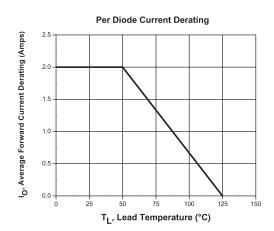


TYPICAL ELECTRICAL CHARACTERISTICS









OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- · Special wafer diffusions
- · PbSn plating options
- Package details
- · Application notes
- · Application and design sample kits
- · Custom product and package development

CONTACT US

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