

TOSHIBA Schottky Barrier Diode

CMS16

Switching Mode Power Supply Applications

Portable Equipment Battery Applications

DC-DC Converter Applications

- Forward voltage: $V_{FM} = 0.55$ V (max) (@ $I_{FM} = 3.0$ A)
- Average forward current: $I_F (AV) = 3.0$ A
- Repetitive peak reverse voltage: $V_{RRM} = 40$ V
- Suitable for compact assembly due to a small surface-mount package: "M-FLATTM" (Toshiba package name)"

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Repetitive peak reverse voltage	V_{RRM}	40	V
Average forward current	$I_F (AV)$	3.0 (Note 1)	A
Nonrepetitive peak surge current	I_{FSM}	30 (50 Hz)	A
Junction temperature	T_j	-40 to 150	°C
Storage temperature range	T_{stg}	-40 to 150	°C

Note : Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

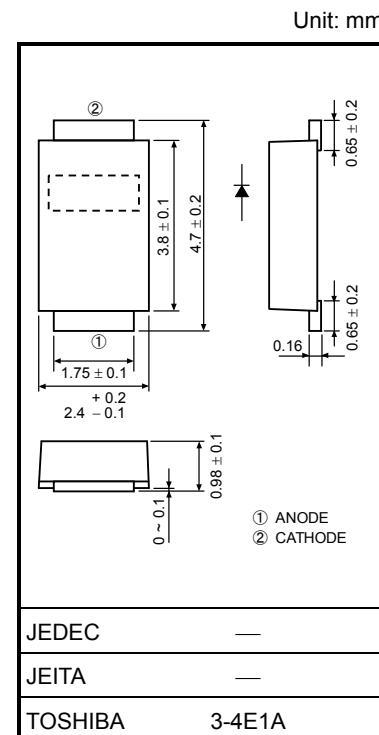
Note 1: $T_{fl} = 106$ °C

Device mounted on a ceramic board

Board size: 50 mm × 50 mm

Soldering size: 2 mm × 2 mm

Board thickness: 0.64 mm

Rectangular waveform ($\alpha = 180$ °), $V_R = 20$ V

Weight: 0.023 g (typ.)

Electrical Characteristics (Ta = 25°C)

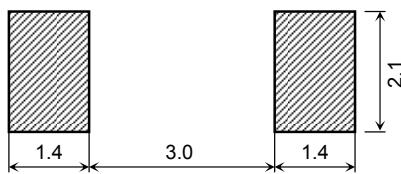
Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Peak forward voltage	V _{FM} (1)	I _{FM} = 1.0 A (pulse test)	—	0.40	—	V
	V _{FM} (2)	I _{FM} = 3.0 A (pulse test)	—	0.50	0.55	
Peak repetitive reverse current	I _{RRM} (1)	V _{RRM} = 5 V (pulse test)	—	2.0	—	μA
	I _{RRM} (2)	V _{RRM} = 40 V (pulse test)	—	26	200	
Junction capacitance	C _j	V _R = 10 V, f = 1.0 MHz	—	95	—	pF
Thermal resistance (junction to ambient)	R _{th} (j-a)	Device mounted on a ceramic board (board size: 50 mm × 50 mm) (soldering land: 2 mm × 2 mm) (board thickness: 0.64 mm)	—	—	60	°C/W
		Device mounted on a glass-epoxy board (board size: 50 mm × 50 mm) (soldering land: 6 mm × 6 mm) (board thickness: 1.6 mm)	—	—	135	
		Device mounted on a glass-epoxy board (board size: 50 mm × 50 mm) (soldering land: 2.1 mm × 1.4 mm) (board thickness: 1.6 mm)	—	—	210	
Thermal resistance (junction to lead)	R _{th} (j-t)	—	—	—	16	°C/W

Marking

Abbreviation Code	Part No.
SF	CMS16

Land pattern dimensions for reference only

Unit: mm



Handling Precaution

1) Schottky barrier diodes have reverse current characteristics compared to other diodes. SBDs can cause thermal runaway when used under high temperature or high voltage conditions. Be sure to take forward and reverse loss into consideration during design.

2) The absolute maximum ratings denote the absolute maximum ratings, which are rated values that must not be exceeded during operation, even for an instant. The following are the general derating methods that we recommend for designing a circuit incorporating this device.

 V_{RRM}: Use this rating with reference to (1) above. V_{RRM} has a temperature coefficient of 0.1%/°C. Take this temperature coefficient into account when designing a device for operation at low temperature.

 I_{F (AV)}: We recommend that the worst case current be no greater than 80% of the absolute maximum rating of

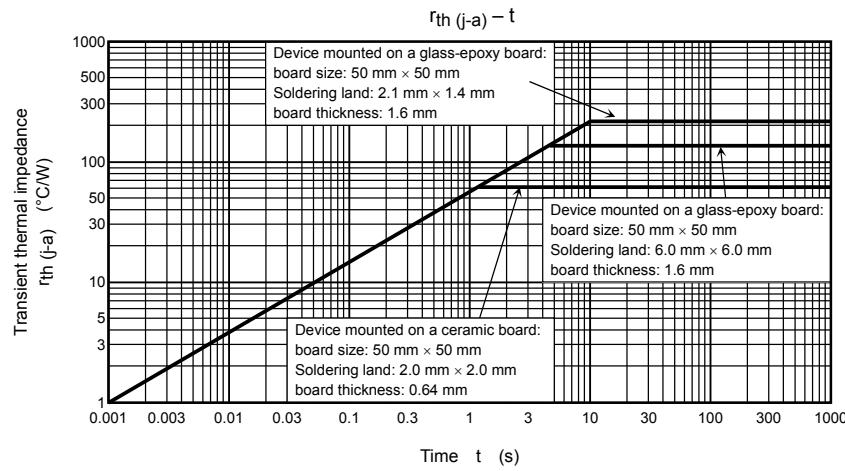
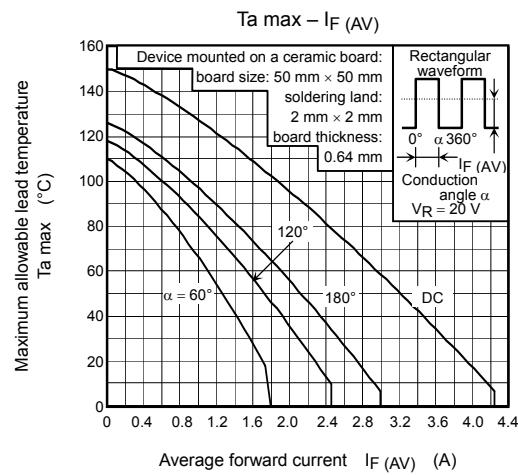
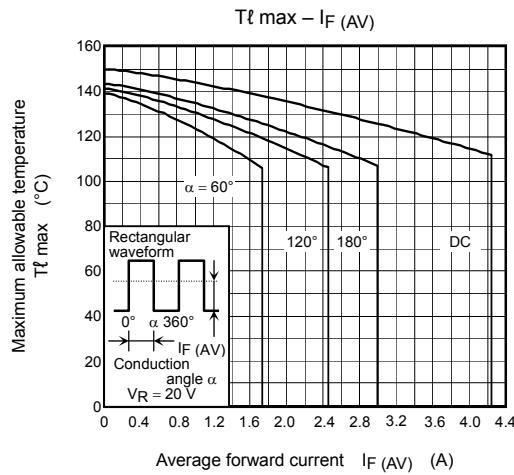
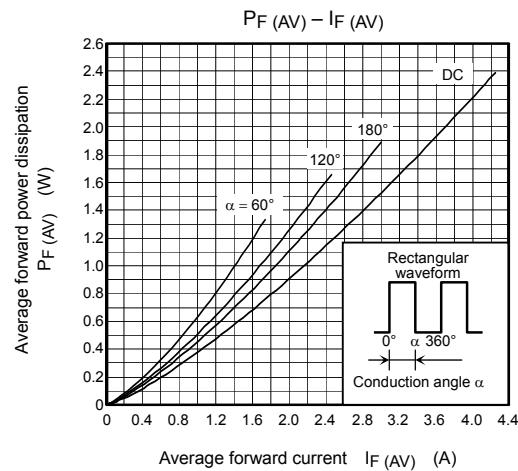
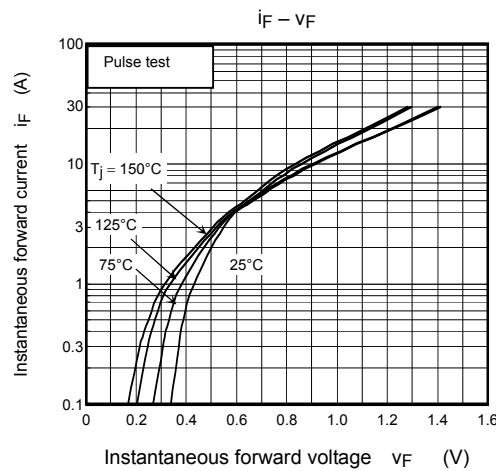
 I_{F (AV)} and that T_j be below 120°C. When using this device, take the margin into consideration by using an allowable Tamax-I_{F (AV)} curve.

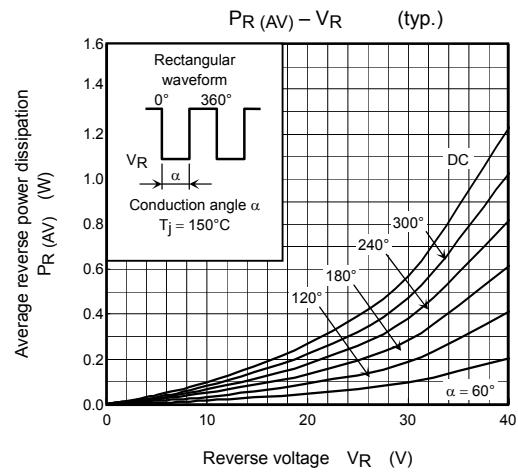
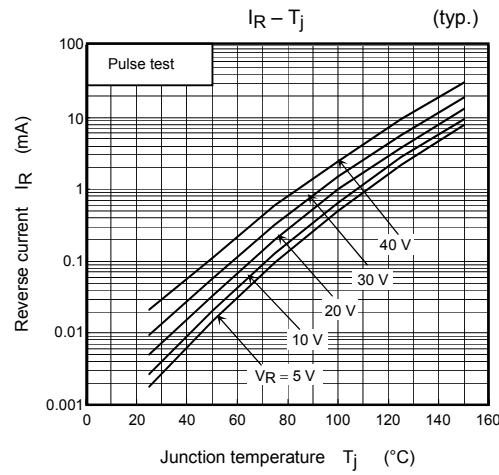
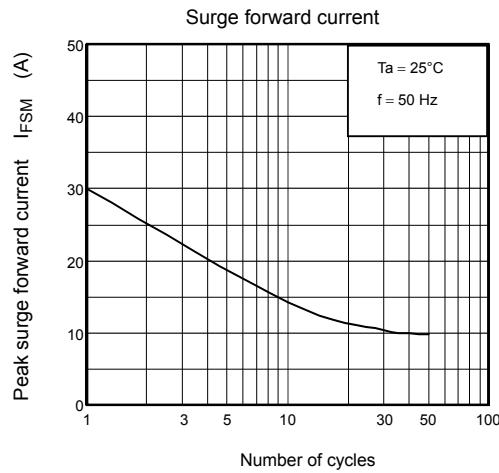
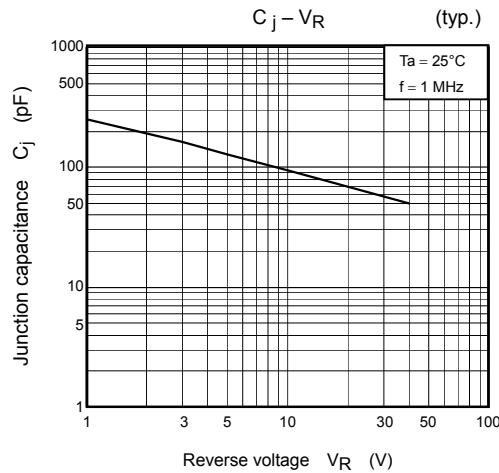
 I_{FSM}: This rating specifies the nonrepetitive peak current. This is applied only for abnormal operation, which seldom occurs during the lifespan of the device.

 T_j: Derate this rating when using the device to ensure high reliability.
 We recommend that the device be used at a T_j of below 120°C.

3) Thermal resistance between junction and ambient fluctuates depending on the mounting condition of the device. When using the device, design the circuit board and soldering land size to match the appropriate thermal resistance value.

4) Refer to the Rectifiers databook for further information.





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