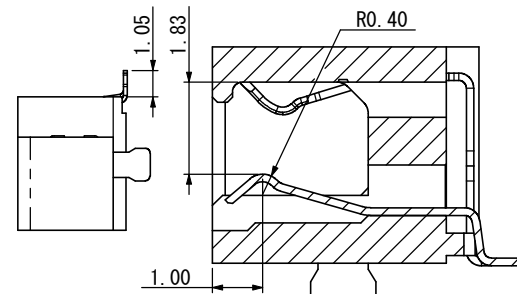
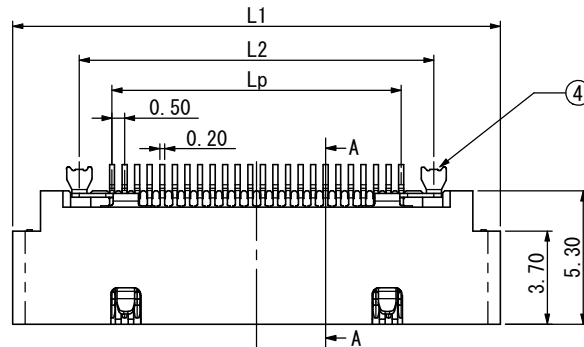
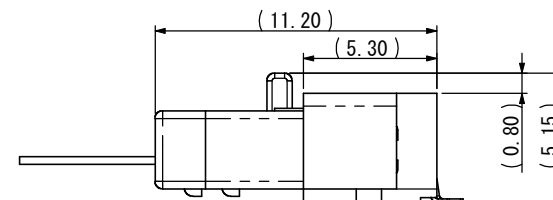
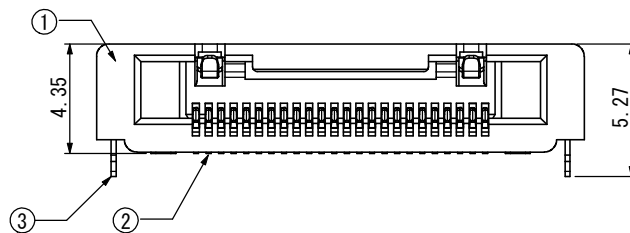
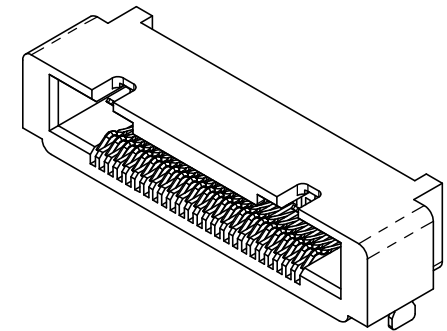


	REVISION DESCRIPTION	APP.	DRW.	DATE	CHANGE NO.
△ x					
△ x					
△ x					

50	24.5	32.4	27.1	31.05	CNU018S-050-0012
32	15.5	23.4	18.1	22.05	CNU018S-032-0012
24	11.5	19.4	14.1	18.05	CNU018S-024-0012
Pin count	Lp	L1	L2	L3	P/N



SEC. A-A (S=10/1)

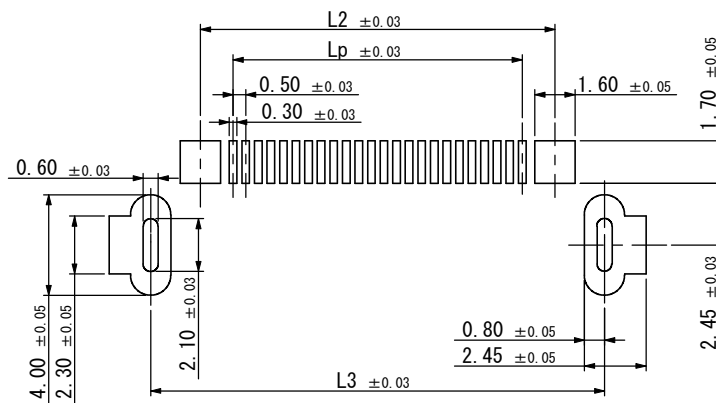


PCB edge
No component area recommended

CNU018P-xxx-0001 mating condition

Note.

1. Coplanarity between terminal and reinforcement bracket terminal should be 0.10mm max.
2. Performance shall be referred to YEUF2013.
3. Applicable plug
CNU018P-xxx-0001



Recommended PCB layout (REF.)

4	Reinforcement bracket-2	2	SUS	Ni/Au
3	Reinforcement bracket-1	2	SUS	Ni/Au
2	Contact	n	Copper alloy	Ni/Au
1	Insulator	1	LCP	Black
Item No.	Description	QNT.	Material	Content

Tolerance ±0.25 Unless otherwise specified.



YAMAICHI ELECTRONICS
USA INC.

CLASS

SCALE	5/1	APP.	APP.	CHK.	DRW.	DSGN.	TITLE
DIMENTION	mm						CNU018S-xxx-0012
ANGLE PROJ	3rd		K. Abe	K. Abe	O. Shimizu	O. Shimizu	DRW No. YEUF2019

Sheet No. / REV. A

A3-001

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