

IGBT SIP Module (Fast IGBT)



IMS-2

PRODUCT SUMMARY						
OUTPUT CURRENT IN A TYPICAL 20 kHz MOTOR DRIVE						
V _{CES}	600 V					
I _{RMS} per phase (3.1 kW total) with T _C = 90 °C	4.6 A _{RMS}					
TJ	125 °C					
Supply voltage	360 V _{DC}					
Power factor	0.8					
Modulation depth (see fig. 1)	115 %					
V _{CE(on)} (typical) at I _C = 3.9 A, 25 °C	1.7 V					
Speed	8 kHz to 30 kHz					
Package	SIP					
Circuit	Three phase inverter					

FEATURES





- Switching-loss rating includes all "tail" losses
- HEXFRED® soft ultrafast diodes
- RoHS
- Optimized for high speed, see fig. 1 for current vs. frequency curve
- UL approved file E78996
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- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The IGBT technology is the key to Vishay's Semiconductors advanced line of IMS (Insulated Metal Substrate) power modules. These modules are more efficient than comparable bipolar transistor modules, while at the same time having the simpler gate-drive requirements of the familiar power MOSFET. This superior technology has now been coupled to a state of the art materials system that maximizes power throughput with low thermal resistance. This package is highly suited to motor drive applications and where space is at a premium.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		600	V	
Continuous collector current, each IGBT	1	T _C = 25 °C	7.2		
	I _C	T _C = 100 °C	3.9		
Pulsed collector current	I _{CM} ⁽¹⁾		22		
Clamped inductive load current	I _{LM} (2)		22	Α	
Diode continuous forward current	I _F	T _C = 100 °C	3.4		
Diode maximum forward current	I _{FM}		22		
Gate to emitter voltage	V_{GE}		± 20	V	
Isolation voltage	V _{ISOL}	1 minute, any terminal to case	2500	V_{RMS}	
Maximum power dissipation, each IGBT	P _D	T _C = 25 °C 23		14/	
		T _C = 100 °C	9.1	W	
Operating junction and storage temperature range	T _J , T _{Stg}	T _J , T _{Stg}		90	
Soldering temperature		10 s, (0.063" (1.6 mm) from case)	300	°C	
Mounting torque		6-32 or M3 screw	5 to 7 (0.55 to 0.8)	lbf · in (N · m)	

Notes

⁽¹⁾ Repetitive rating; V_{GE} = 20 V, pulse width limited by maximum junction temperature (see fig. 20)

 $^{^{(2)}}$ V_{CC} = 80 % (V_{GES}), V_{GE} = 20 V, L = 10 μ H, R_G = 50 Ω (see fig.19)





THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TYP.	MAX.	UNITS		
Junction to case, each IGBT, one IGBT in conduction	R _{thJC} (IGBT)	-	5.5			
Junction to case, each DIODE, one DIODE on conduction	R _{thJC} (DIODE)	-	9.0	°C/W		
Case to sink, flat, greased surface	R _{thCS} (MODULE)	0.1	-			
Weight of module		20		g		
weight of module		0.7		OZ.		

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(VB)CES} (1)	$V_{GE} = 0 \text{ V}, I_{C} = 250 \mu\text{A}$		600	-	-	V
Temperature coefficient of breakdown voltage	$\Delta V_{(BR)CES}/\Delta T_{J}$	V _{GE} = 0 V, I _C = 1 mA	V _{GE} = 0 V, I _C = 1 mA		0.63	-	V/°C
		I _C = 3.9 A	V _{GE} = 15 V See fig. 2, 5	-	1.70	2.2	V
Collector to emitter saturation voltage	V _{CE(on)}	I _C = 7.2 A		-	1.95	-	
		I _C = 3.9 A, T _J = 150 °C		-	1.70	-	
Gate threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}, I_{C} = 250 \ \mu A$		3.0	-	6.0	
Temperature coefficient of threshold voltage	$\Delta V_{GE(th)}/\Delta T_{J}$			-	-11	-	mV/°C
Forward transconductance	g _{fe} ⁽²⁾	V _{CE} = 100 V, I _C = 6.5 A		1.4	4.3	-	S
Zero gate voltage collector current	I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 600 \text{ V}$		-	-	250	
		V _{GE} = 0 V, V _{CE} = 600 V, T _J = 150 °C		-	-	2500	μA
Diode forward voltage drop	V_{FM}	I _C = 8.0 A	See fig. 13	-	1.4	1.7	V
		I _C = 8.0 A, T _J = 150 °C		-	1.3	1.6	v
Gate to emittler leakage current	I _{GES}	V _{GE} = ± 20 V	V _{GE} = ± 20 V		-	± 100	nA

Notes

 $^{^{(1)}~}$ Pulse width $\leq 80~\mu s;~duty~factor \leq 0.1~\%$

 $^{^{(2)}}$ Pulse width 5.0 μ s, single shot





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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS		
Total gate charge (turn-on)	Og	I _C = 3.9 A V _{CC} = 400 V V _{GE} = 15 V			-	31	47		
Gate to emitter charge (turn-on)	O _{GE}				-	5.0	7.5	nC	
Gate to collector charge (turn-on)	O _{gc}				-	13	20		
Turn-on delay time	t _{d(on)}				-	45	-		
Rise time	t _r				-	22	-		
Turn-off delay time	t _{d(off)}	$T_J = 25 ^{\circ}\text{C}$ $I_C = 3.9 \text{A, V}_C$	_C = 480 V		-	100	160	ns	
Fall time	t _f	$V_{\rm GE}$ = 15 V, $R_{\rm G}$ = 50 Ω Energy losses include "tail" and diode reverse recovery See fig. 9, 10, 11, 18			-	120	180		
Turn-on switching loss	E _{on}				-	0.13	-	mJ	
Turn-off switching loss	E _{off}				-	0.07	-		
Total switching loss	E _{ts}		-	0.20	0.3				
Turn-on delay time	t _{d(on)}	$T_J=150~^{\circ}\text{C}$ $I_C=3.9~\text{A, V}_{CC}=480~\text{V}$ $V_{GE}=15~\text{V, R}_{G}=50~\Omega$ Energy losses include "tail" and diode reverse recovery See fig. 9, 10, 11, 18			-	42	-	ns	
Rise time	t _r				-	22	-		
Turn-off delay time	t _{d(off)}				-	120	-		
Fall time	t _f				-	250	-		
Total switching loss	E _{ts}				-	0.35	-	mJ	
Input capacitance	C _{ies}	V - 0 V	V 0V		-	530	-		
Output capacitance	C _{oes}	$V_{GE} = 0 \text{ V}$ $V_{CC} = 30 \text{ V}$ f = 1.0 MHz		See fig. 7	-	39	-	pF	
Reverse transfer capacitance	C _{res}				-	7.4	-		
Die de verreure vereure Hiere		T _J = 25 °C	See fig. 14		-	37	55		
Diode reverse recovery time	t _{rr}	T _J = 125 °C			-	55	90	ns	
Diada asal managara		T _J = 25 °C	0		-	3.5	5.0	^	
Diode peak reverse recovery current	peak reverse recovery current I_{rr} $T_{J} = 125 ^{\circ}\text{C}$	T _J = 125 °C	See fig. 15 $I_F = 8.0 \text{ A}$ $V_R = 200 \text{ V}$	-	4.5	8.0	A		
Diada rayawa raassa sharas	$T_{\rm J} = 25 ^{\circ}{\rm C}$ dl/dt = 200		-	65	138				
Diode reverse recovery charge	Q _{rr}	T _J = 125 °C	See fig. 16	See fig. 16	J. 10 7.440	-	124	360	nC
	e peak rate of fall of	1	i	C . 47		1	1	1	
Diode peak rate of fall of	dl _{(rec)M} /dt	T _J = 25 °C	See fig. 17		-	240	-	A/µs	

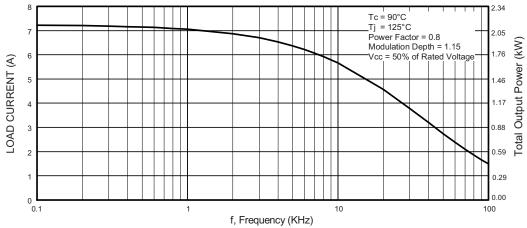


Fig. 1 - Typical Load Current vs. Frequency (Load Current = I_{RMS} of Fundamental)

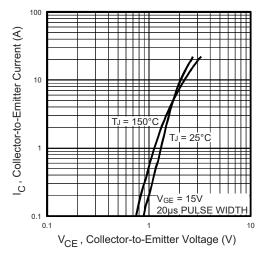


Fig. 2 - Typical Output Characteristics

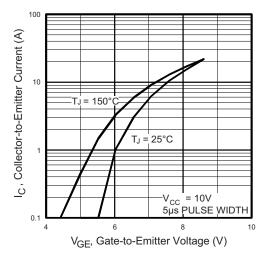


Fig. 3 - Typical Transfer Characteristics

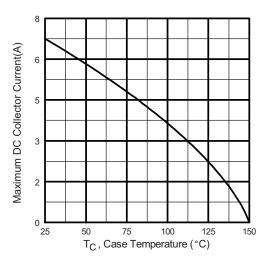


Fig. 4 - Maximum Collector Current vs.
Case Temperature

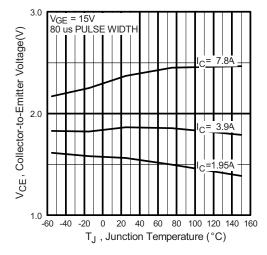


Fig. 5 - Typical Collector to Emitter Voltage vs. Junction Temperature

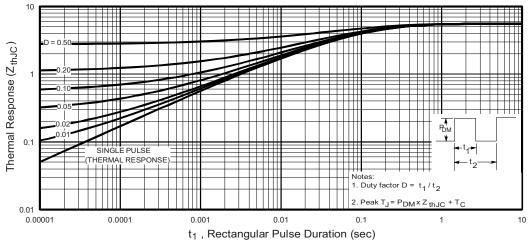


Fig. 6 - Maximum Effective Transient Thermal Impedance, Junction to Case

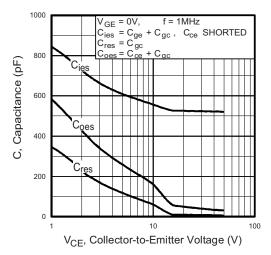


Fig. 7 - Typical Capacitance vs. Collector to Emitter Voltage

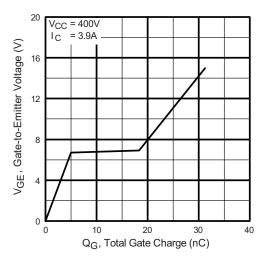


Fig. 8 - Typical Gate Charge vs. Gate to Emitter Voltage

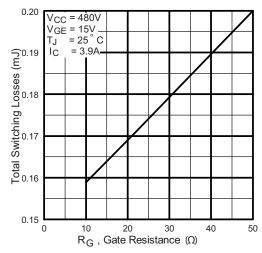


Fig. 9 - Typical Switching Losses vs. Gate Resistance

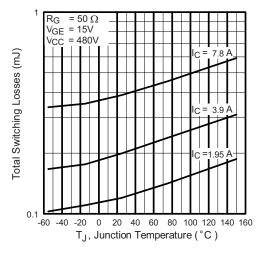


Fig. 10 - Typical Switching Losses vs. Junction Temperature



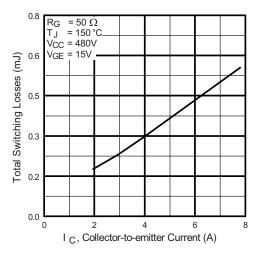


Fig. 11 - Typical Switching Losses vs. Collector to Emitter Current

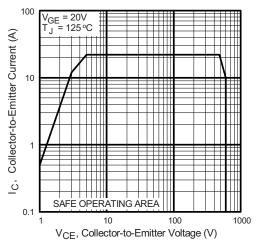


Fig. 12 - Turn-Off SOA

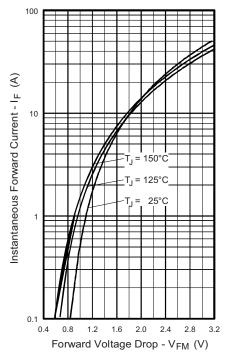


Fig. 13 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current



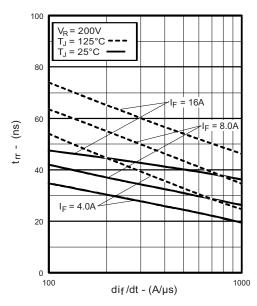


Fig. 14 - Typical Reverse Recovery Time vs. dl_F/dt

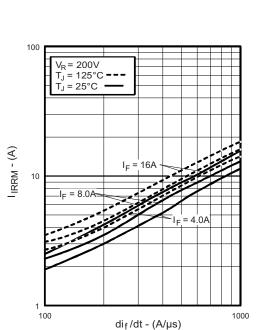


Fig. 15 - Typical Recovery Current vs. dl_F/dt

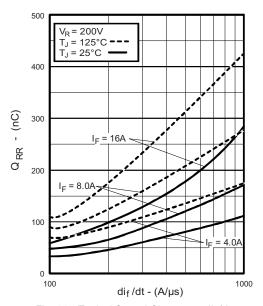


Fig. 16 - Typical Stored Charge vs. dl_F/dt

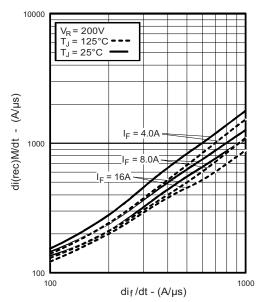


Fig. 17 - Typical $dI_{(rec)M}/dt$ vs. dI_F/dt

GATE VOLTAGE D.U.T.

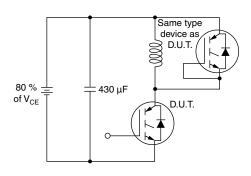
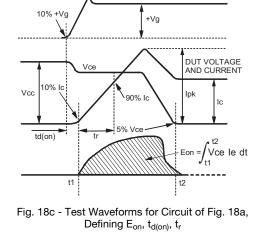


Fig. 18a - Test Circuit for Measurement of I_{LM} , E_{on} , $E_{off(diode)}$, t_{rr} , Q_{rr} , I_{rr} , $t_{d(on)}$, t_r , $t_{d(off)}$, t_f



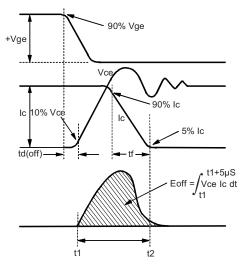


Fig. 18b - Test Waveforms for Circuit of Fig. 18a, Defining E_{off} , $t_{\text{d(off)}}$, t_{f}

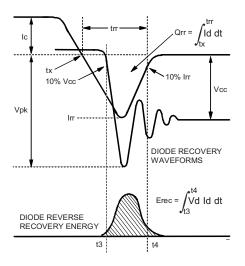


Fig. 18d - Test Waveforms for Circuit of Fig. 18a, Defining E_{rec} , t_{rr} , Q_{rr} , I_{rr}

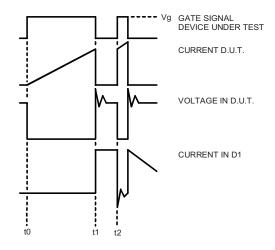


Fig. 18e - Macro Waveforms for Fig. 18a's Test Circuit



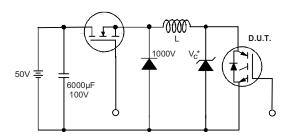


Fig. 19 - Clamped Inductive Load Test Circuit

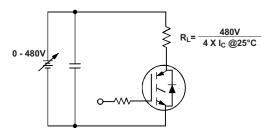
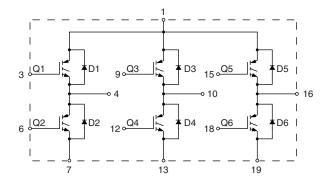


Fig. 20 - Pulsed Collector Current Test Circuit

CIRCUIT CONFIGURATION

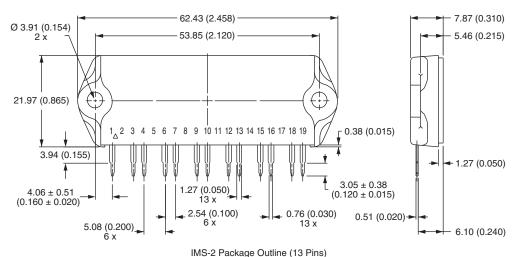


LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95066				



IMS-2 (SIP)

DIMENSIONS in millimeters (inches)



INIS-2 Fackage Outline (13 Fil

Notes

- $^{(1)}$ Tolerance uless otherwise specified \pm 0.254 mm (0.010")
- (2) Controlling dimension: inch
- (3) Terminal numbers are shown for reference only

Document Number: 95066 Revision: 30-Jul-07



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