

# **CR02AM-8**

# **Thyristor**

Low Power Use

REJ03G0542-0100 Rev.1.00 Mar.28.2005

### **Features**

 $\begin{array}{ll} \bullet & I_{T\,(AV)}: 0.3\;A \\ \bullet & V_{DRM}: 400\;V \\ \bullet & I_{GT}: 100\;\mu A \end{array}$ 

Planar Passivation Type

Completed Pb free product

## **Outline**

RENESAS Package code: PRSS0003DE-A (Package name: TO-92(3))

1. Cathode
2. Anode
3. Gate

# **Applications**

Solid state relay, leakage protector, fire alarm, timer, ring counter, electric blanket, protective circuit for acoustic equipment, strobe flasher, and other general purpose control applications

# **Maximum Ratings**

Parameter	Symbol	Voltage class	Unit	
Parameter	Symbol	8	Offic	
Repetitive peak reverse voltage	$V_{RRM}$	400	V	
Non-repetitive peak reverse voltage	$V_{RSM}$	500	V	
DC reverse voltage	V <sub>R (DC)</sub>	320	V	
Repetitive peak off-state voltage <sup>Note1</sup>	$V_{DRM}$	400	V	
DC off-state voltage <sup>Note1</sup>	V <sub>D (DC)</sub>	320	V	

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I <sub>T (RMS)</sub>	0.47	А	
Average on-state current	I <sub>T (AV)</sub>	0.3	A	Commercial frequency, sine half wave 180° conduction, Ta = 30°C
Surge on-state current	I <sub>TSM</sub>	10	А	60Hz sine half wave 1 full cycle, peak value, non-repetitive
I <sup>2</sup> t for fusing	l <sup>2</sup> t	0.4	A <sup>2</sup> s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	$P_GM$	0.1	W	
Average gate power dissipation	P <sub>G (AV)</sub>	0.01	W	
Peak gate forward voltage	$V_{FGM}$	6	V	
Peak gate reverse voltage	$V_{RGM}$	6	V	
Peak gate forward current	I <sub>FGM</sub>	0.1	А	
Junction temperature	Tj	- 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	_	0.23	g	Typical value

Notes: 1. With gate to cathode resistance  $R_{GK} = 1 \text{ k}\Omega$ .

## **Electrical Characteristics**

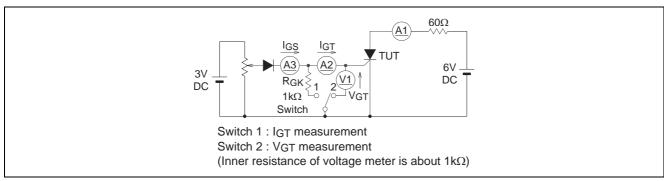
Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak reverse current	I <sub>RRM</sub>	_	_	0.1	mA	Tj = 125°C, V <sub>RRM</sub> applied
Repetitive peak off-state current	I <sub>DRM</sub>	_	_	0.1	mA	$Tj = 125$ °C, $V_{DRM}$ applied, $R_{GK} = 1 \text{ k}\Omega$
On-state voltage	V <sub>TM</sub>	_	_	1.6	V	Ta = 25°C, I <sub>TM</sub> = 0.6 A, instantaneous value
Gate trigger voltage	V <sub>GT</sub>	_	_	0.8	V	$Tj = 25$ °C, $V_D = 6$ V, $I_T = 0.1$ A <sup>Note3</sup>
Gate non-trigger voltage	$V_{GD}$	0.2	_	_	V	$Tj = 125^{\circ}C, V_D = 1/2 V_{DRM},$ $R_{GK} = 1 k\Omega$
Gate trigger current	I <sub>GT</sub>	20	_	100 <sup>Note2</sup>	μΑ	$Tj = 25$ °C, $V_D = 6$ V, $I_T = 0.1$ A <sup>Note3</sup>
Holding current	I <sub>H</sub>	_	_	3	mA	$Tj = 25$ °C, $V_D = 12$ V, $R_{GK} = 1$ k $\Omega$
Thermal resistance	R <sub>th (j-a)</sub>	_	_	180	°C/W	Junction to ambient

Notes: 2. If special values of I<sub>GT</sub> are required, choose item E from those listed in the table below if possible.

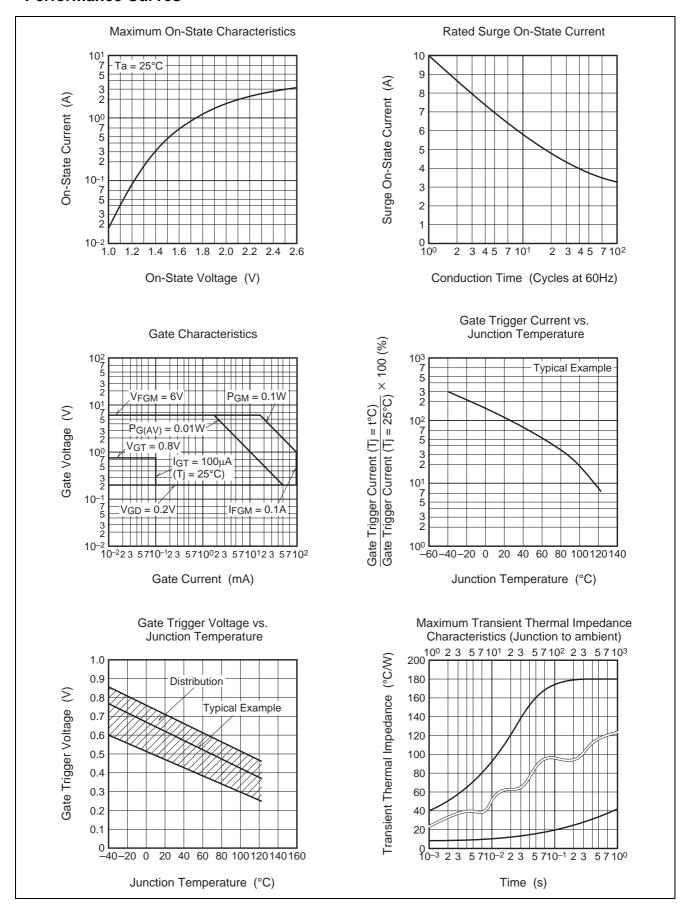
Item	В	E
I <sub>GT</sub> (μA)	20 to 50	20 to 100

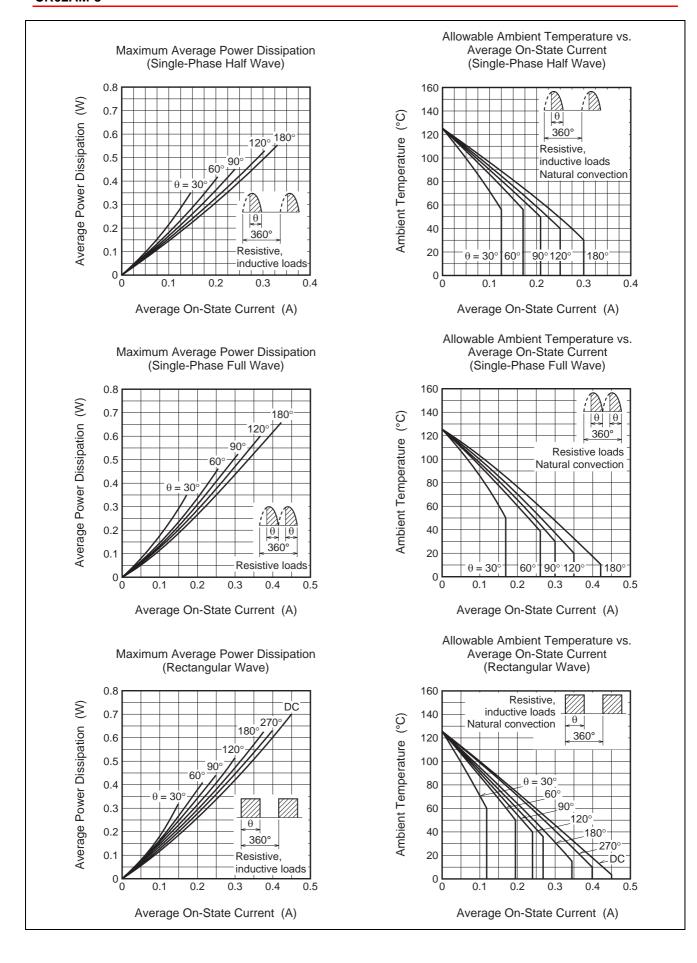
The above values do not include the current flowing through the 1  $k\Omega$  resistance between the gate and cathode.

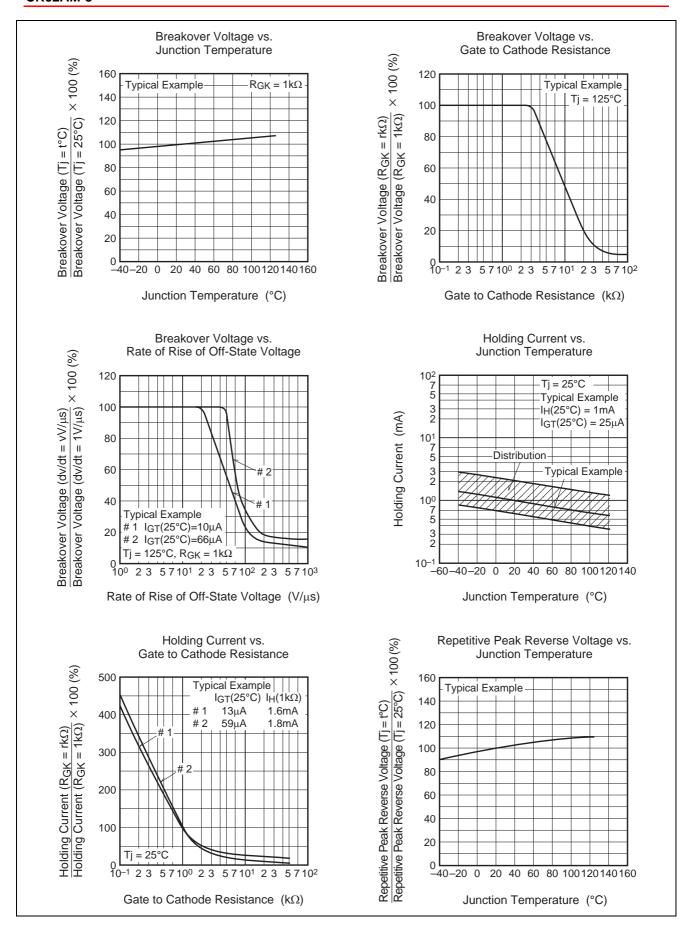
3.  $I_{\text{GT}}$ ,  $V_{\text{GT}}$  measurement circuit.

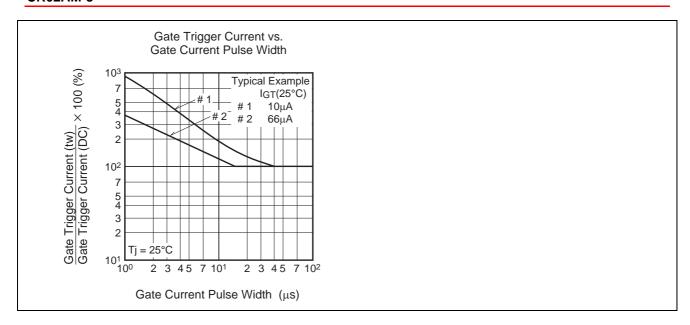


### **Performance Curves**

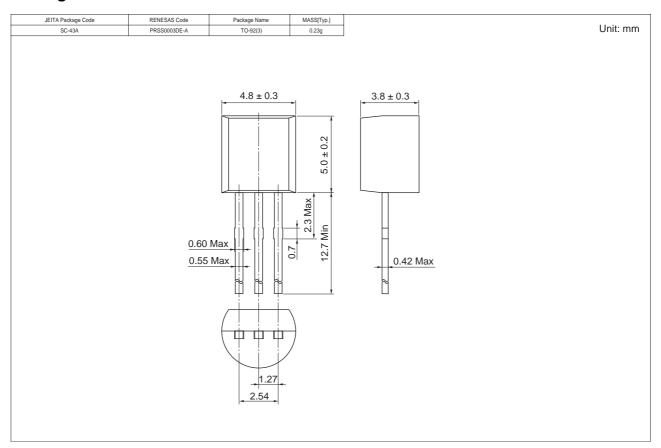








# **Package Dimensions**



# **Order Code**

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Straight type	Vinyl sack	500	Type name	CR02AM-8-E
Form A8	Taping	2500	Type name – ETZ	CR02AM-8-ETZ

Note: Please confirm the specification about the shipping in detail.

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Renesas Technology Europe Limited
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Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001