



FEATURES

- UL 60950 Recognised
- Wide temperature performance at full 2 Watt load, -40°C to 85°C
- UL 94V-0 package material
- Single isolated output
- Industry standard pinout
- 1kVDC isolation 'Hi Pot Test'
- 5V & 12V Input
- 5V Output
- Fully encapsulated with toroidal magnetics
- No external components required
- Pin compatible with CME, CRE1, LME, MEE1, MEE3, NKE, NME & NML series
- No electrolytic or tantalum capacitors

PRODUCT OVERVIEW

The CRL2 series of DC/DC Converters is particularly suited to isolating and/or converting DC power rails. The galvanic isolation allows the device to be configured to provide an isolated negative rail in systems where only positive rails exist. The wide temperature range guarantees startup from -40°C and full 2 watt output at 85°C . Pin compatibility with the NME and LME ensures ease of upgradeability.

CRL2 Series

Isolated 2W Single Output DC/DC Converters

SELECTION GUIDE

| Order Code | Nominal Input Voltage | Output Voltage | Output Current | Input Current at Rated Load | Load Regulation (Typ) | Ripple & Noise (Typ) ¹ | Ripple & Noise (Max) ¹ | Efficiency (Min) | Efficiency (Typ) | Isolation Capacitance | MTTF ² | |
|--------------------|-----------------------|----------------|----------------|-----------------------------|-----------------------|-----------------------------------|-----------------------------------|------------------|------------------|-----------------------|-------------------|------|
| | V | | | | | | | | | | | |
| CRL2S0505SC | 5 | 5 | 400 | 513 | 6 | 8 | 96 | 200 | 75 | 78 | 19 | 2327 |
| CRL2S1205SC | 12 | 5 | 400 | 207 | 4 | 5 | 76 | 200 | 77 | 81 | 28 | 716 |

INPUT CHARACTERISTICS

| Parameter | Conditions | Min. | Typ. | Max. | Units |
|--------------------------|---------------------------------------|------|------|------|--------|
| Voltage range | Continuous operation, 5V input types | 4.5 | 5 | 5.5 | V |
| | Continuous operation, 12V input types | 10.8 | 12 | 13.2 | |
| Reflected ripple current | 5V input types | | 33 | | mA p-p |
| | 12V input types | | 38 | | |

OUTPUT CHARACTERISTICS

| Parameter | Conditions | Min. | Typ. | Max. | Units |
|----------------------------|-----------------------------------------------------|------|------|------|-------|
| Rated Power | $T_{A}=-40^{\circ}\text{C}$ to 85°C | | | 1.0 | W |
| Voltage Set Point Accuracy | See tolerance envelope | | | | |
| Line regulation | High V_{IN} to low V_{IN} | | 1.1 | 1.2 | %/% |

ISOLATION CHARACTERISTICS

| Parameter | Conditions | Min. | Typ. | Max. | Units |
|------------------------|---------------------------|------|------|------|------------|
| Isolation test voltage | Flash tested for 1 second | 1000 | | | VDC |
| Resistance | $V_{iso}=1000\text{VDC}$ | 10 | | | G Ω |

GENERAL CHARACTERISTICS

| Parameter | Conditions | Min. | Typ. | Max. | Units |
|---------------------|-----------------|------|------|------|-------|
| Switching frequency | All input types | | 90 | | kHz |

ABSOLUTE MAXIMUM RATINGS

| | |
|-------------------------------------------------|-------|
| Lead temperature 1.5mm from case for 10 seconds | 260°C |
| Input voltage V_{IN} , 5V input | 7V |
| Input voltage V_{IN} , 12V input | 15V |

TEMPERATURE CHARACTERISTICS

| Parameter | Conditions | Min. | Typ. | Max. | Units |
|--------------------------------|---------------------|------|------|------|-------|
| Specification | | -40 | | 85 | |
| Storage | | -50 | | 130 | |
| Case Temperature above ambient | | | | 45 | °C |
| Cooling | Free air convection | | | | |

1. See Ripple & Noise characterisation method.

2. Calculated using MIL-HDBK-217 FN2 and Telcordia SR-332 calculation model with nominal input voltage at full load. All specifications typical at $T_{A}=25^{\circ}\text{C}$, nominal input voltage and rated output current unless otherwise specified.

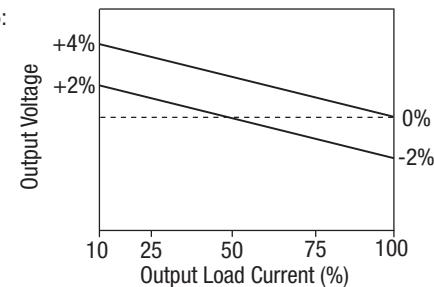
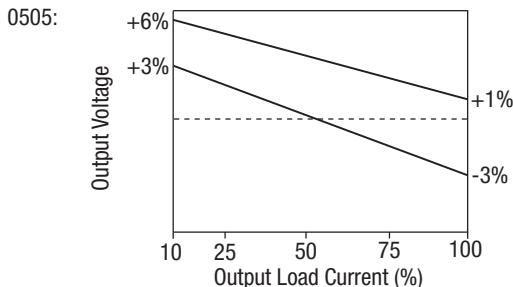


For full details go to
www.murata-ps.com/rohs



TOLERANCE ENVELOPE

The voltage tolerance envelope shows typical load regulation characteristics for this product series. The tolerance envelope is the maximum output voltage variation due to changes in output loading.



TECHNICAL NOTES

ISOLATION VOLTAGE

'Hi Pot Test', 'Flash Tested', 'Withstand Voltage', 'Proof Voltage', 'Dielectric Withstand Voltage' & 'Isolation Test Voltage' are all terms that relate to the same thing, a test voltage, applied for a specified time, across a component designed to provide electrical isolation, to verify the integrity of that isolation.

Murata Power Solutions CRL2 series of DC/DC converters are all 100% production tested at their stated isolation voltage. This is 1kVDC for 1 second.

A question commonly asked is, "What is the continuous voltage that can be applied across the part in normal operation?"

The CRL2 has been recognised by Underwriters Laboratory for functional insulation, both input and output should normally be maintained within SELV limits i.e. less than 42.4V peak, or 60VDC. The isolation test voltage represents a measure of immunity to transient voltages and the part should never be used as an element of a safety isolation system. The part could be expected to function correctly with several hundred volts offset applied continuously across the isolation barrier; but then the circuitry on both sides of the barrier must be regarded as operating at an unsafe voltage and further isolation/insulation systems must form a barrier between these circuits and any user-accessible circuitry according to safety standard requirements.

REPEATED HIGH-VOLTAGE ISOLATION TESTING

It is well known that repeated high-voltage isolation testing of a barrier component can actually degrade isolation capability, to a lesser or greater degree depending on materials, construction and environment. The CRL2 series has toroidal isolation transformers, with no additional insulation between primary and secondary windings of enameled wire. While parts can be expected to withstand several times the stated test voltage, the isolation capability does depend on the wire insulation. Any material, including this enamel (typically polyurethane) is susceptible to eventual chemical degradation when subject to very high applied voltages thus implying that the number of tests should be strictly limited. We therefore strongly advise against repeated high voltage isolation testing, but if it is absolutely required, that the voltage be reduced by 20% from specified test voltage.

This consideration equally applies to agency recognized parts rated for better than functional isolation where the wire enamel insulation is always supplemented by a further insulation system of physical spacing or barriers.

SAFETY APPROVAL

The CRL2 series has been recognised by Underwriters Laboratory (UL) to UL 60950 for functional insulation in a maximum still air ambient temperature of 100°C as measured at any point on the case of the unit (hotspot).

The CRL2 Series of converters are not internally fused so to meet the requirements of UL 60950 an anti-surge input line fuse should always be used with ratings as defined below.

CRL2x05xxxSC: 0.8A

CRL2x12xxxSC: 0.315A

All fuses should be UL recognised, 125V rated.

File number E151252 applies.

RoHS COMPLIANCE INFORMATION



This series is compatible with RoHS soldering systems with a peak wave solder temperature of 260°C for 10 seconds. The pin termination finish on this product series is Tin Plate, Hot Dipped over Matte Tin with Nickel Preplate. The series is backward compatible with Sn/Pb soldering systems.

For further information, please visit www.murata-ps.com/rohs

APPLICATION NOTES

Minimum load

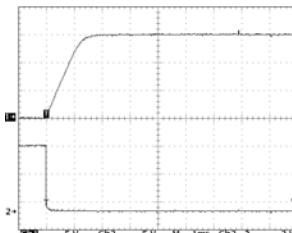
The minimum load to meet datasheet specification is 10% of the full rated load across the specified input voltage range. Lower than 10% minimum loading will result in an increase in output voltage, which may rise to typically double the specified output voltage if the output load falls to less than 5%.

Capacitive loading and start up

Typical start up times for this series, with a typical input voltage rise time of 2.2 μ s and output capacitance of 10 μ F, are shown in the table below. The product series will start into a capacitance of 47 μ F with an increased start time, however, the maximum recommended output capacitance is 10 μ F.

Typical Start-Up Wave Form

| Start-up time | |
|---------------|---------|
| | μ s |
| CRL2S0505SC | 790 |
| CRL2S1205SC | 396 |



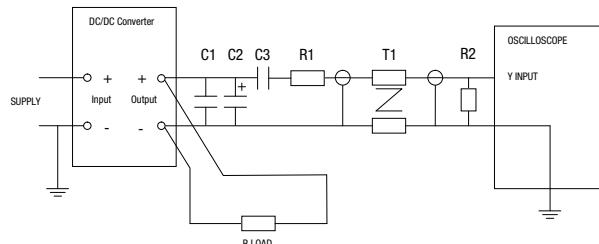
Ripple & Noise Characterisation Method

Ripple and noise measurements are performed with the following test configuration.

| | |
|-------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| C1 | 1 μ F X7R multilayer ceramic capacitor, voltage rating to be a minimum of 3 times the output voltage of the DC/DC converter |
| C2 | 10 μ F tantalum capacitor, voltage rating to be a minimum of 1.5 times the output voltage of the DC/DC converter with an ESR of less than 100m Ω at 100 kHz |
| C3 | 100nF multilayer ceramic capacitor, general purpose |
| R1 | 450 Ω resistor, carbon film, $\pm 1\%$ tolerance |
| R2 | 50 Ω BNC termination |
| T1 | 3T of the coax cable through a ferrite toroid |
| RLOAD | Resistive load to the maximum power rating of the DC/DC converter. Connections should be made via twisted wires |

Measured values are multiplied by 10 to obtain the specified values.

Differential Mode Noise Test Schematic



APPLICATION NOTES (continued)

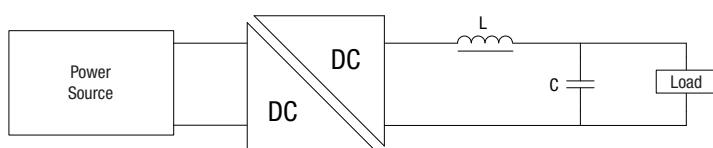
Output Ripple Reduction

By using the values of inductance and capacitance stated, the output ripple at the rated load is lowered to 5mV p-p max.

Component selection

Capacitor: It is required that the ESR (Equivalent Series Resistance) should be as low as possible, ceramic types are recommended. The voltage rating should be at least twice (except for 15V output), the rated output voltage of the DC/DC converter.

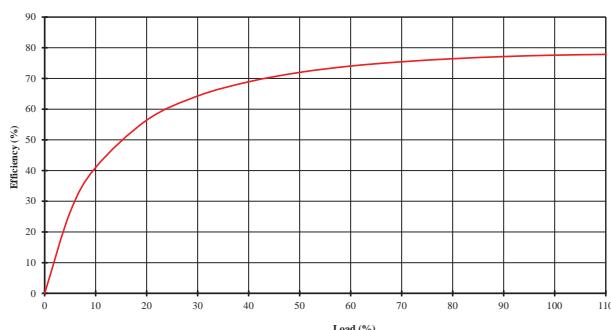
Inductor: The rated current of the inductor should not be less than that of the output of the DC/DC converter. At the rated current, the DC resistance of the inductor should be such that the voltage drop across the inductor is <2% of the rated voltage of the DC/DC converter. The SRF (Self Resonant Frequency) should be >20MHz.



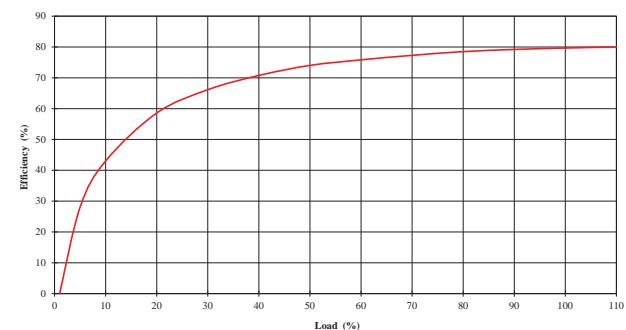
| | Inductor | | | Capacitor |
|-------------|------------|--------|--------------|-----------|
| | L, μ H | SMD | Through Hole | |
| CRL2S0505SC | 22 | 82223C | 11R223C | 2.2 |
| CRL2S1205SC | 22 | 82223C | 11R223C | 2.2 |

EFFICIENCY VS LOAD

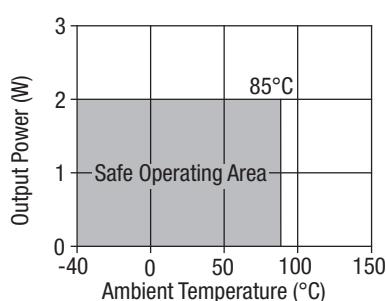
CRL2S0505SC



CRL2S1205SC



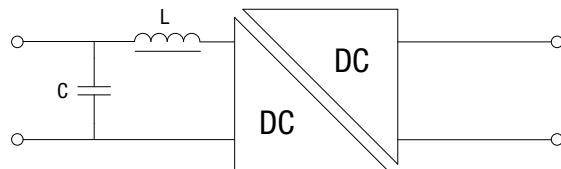
TEMPERATURE DERATING GRAPH



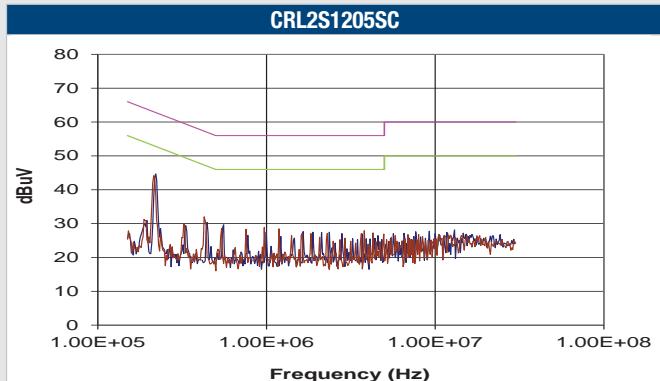
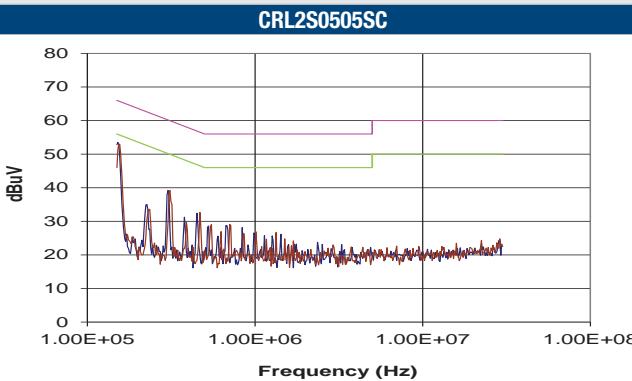
EMC FILTERING AND SPECTRA

FILTERING

The following table shows the additional input capacitor and input inductor typically required to meet EN 55022 Curve B Quasi-Peak EMC limit, as shown in the following plots. The following plots show positive and negative quasi peak and CISPR22 Average Limit B (pink line) and Quasi Peak Limit B (green line) adherence limits. The recommended input capacitor to use for this circuit is 50V 16V X7R ceramic capacitor.

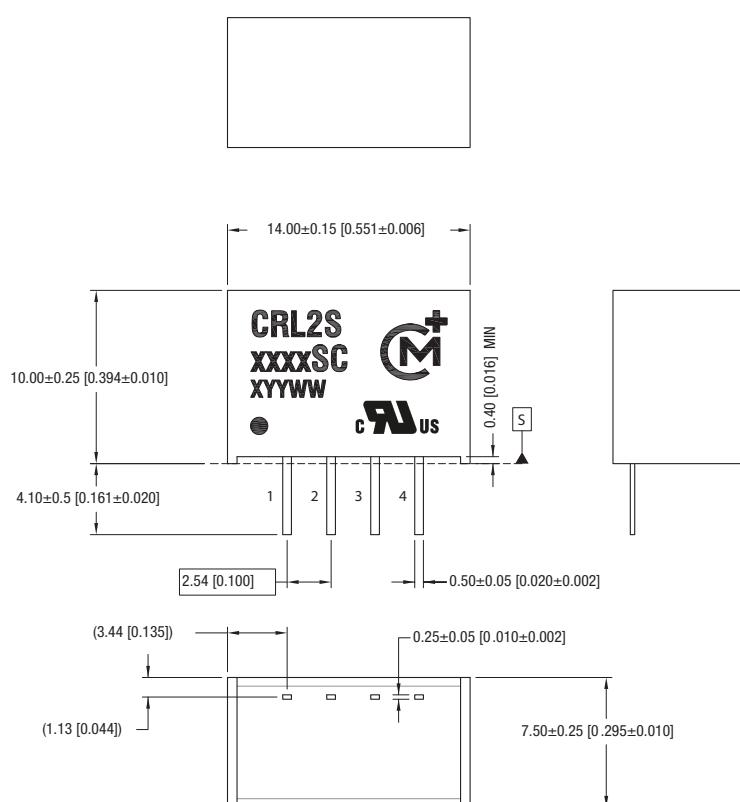


| Part Number | Inductor | | | Capacitor |
|-------------|------------|--------|--------------|-----------|
| | L, μ H | SMD | Through Hole | |
| CRL2S0505SC | 10 | 82223C | 13R103C | 2.2 |
| CRL2S1205SC | 10 | 82223C | 13R103C | 4.7 |



PACKAGE SPECIFICATIONS

MECHANICAL DIMENSIONS



All dimensions in mm (inches) controlling dimension is mm.

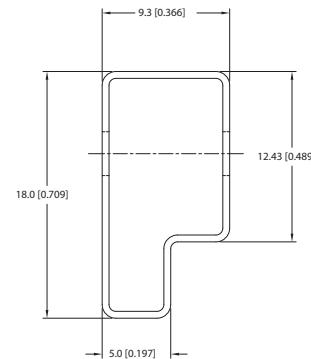
All pins on a 2.54 (0.100) pitch and within ±0.1 (0.004) of true position from pin 1 at seating plane 'S'

Weight: 2.0g

PIN CONNECTIONS

| Pin | Function |
|-----|----------|
| 1 | -Vin |
| 2 | +Vin |
| 3 | -Vout |
| 4 | +Vout |

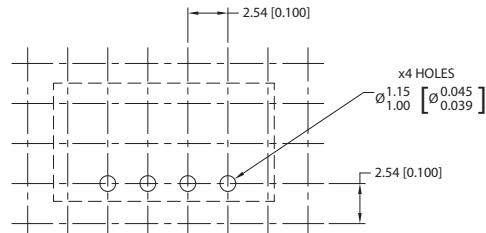
TUBE OUTLINE DIMENSIONS



Unless otherwise specified all dimensions in mm [inches] ±0.55mm
Tube Length : 520mm [20.472] ±2.0 [0.079].

Tube quantity: 35

RECOMMENDED FOOTPRINT DETAILS



Unless otherwise stated all dimensions in mm (inches) ±0.5mm.

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ISO 9001 and 14001 REGISTERED



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Refer to: <http://www.murata-ps.com/requirements/>

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