CSHD5-25L

SURFACE MOUNT SILICON LOW V_F SCHOTTKY RECTIFIER 5.0 AMP. 25 VOLT

DPAK CASE





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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CSHD5-25L is a silicon Schottky rectifier designed for power applications requiring a low forward voltage drop.

MARKING: FULL PART NUMBER

MAXIMUM RATINGS: (T _C =25°C unless otherwise no
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· · · ·	SYMBOL		UNITS
Peak Repetitive Reverse Voltage	V_{RRM}	25	V
Average Rectified Forward Current (T _C =120°C)	IO	5.0	Α
Peak Forward Surge Current, tp=10ms	I _{FSM}	80	Α
Peak Repetitive Reverse Surge Current, tp=2.0µs	IRRM	1.0	Α
Critical Rate of Rise of Reverse Voltage	dv/dt	10,000	V/µs
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	ΘJC	2.5	°C/W

ELECTRICAL	CHARACTERISTICS:	(T _C =25°C unless	otherwise noted)

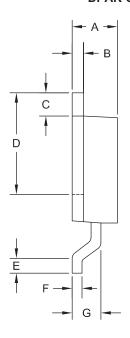
SYMBOL	TEST CONDITIONS	TYP	MAX	UNITS
I_{R}	V _R =25V	103	500	μΑ
I_R	V _R =25V, T _C =125°C	35.5	200	mA
VF	I _F =5.0A	0.42	0.47	V
V_{F}	I _F =5.0A, T _C =125°C	0.31	0.35	V
$C_{.1}$	V _R =0, f=1.0MHz	2,290		pF

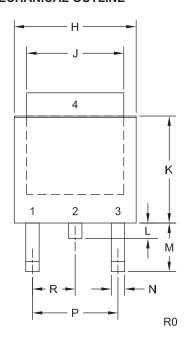
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DPAK CASE - MECHANICAL OUTLINE



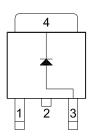


LEAD CODE:

- 1) No Connection
- 2) Cathode
- 3) Anode
- 4) Cathode

Pin 2 is common to the tab (4)

MARKING: FULL PART NUMBER



	DIMENSIONS				
	INCHES		MILLIMETERS		
SYMBOL	MIN	MAX	MIN	MAX	
Α	0.083	0.108	2.10	2.75	
В	0.016	0.032	0.40	0.81	
С	0.035	0.063	0.89	1.60	
D	0.203	0.228	5.15	5.79	
E	0.020	-	0.51	-	
F	0.018	0.024	0.45	0.60	
G	0.051	0.071	1.30	1.80	
Н	0.248	0.268	6.30	6.81	
J	0.197	0.217	5.00	5.50	
K	0.209	0.245	5.30	6.22	
L	0.025	0.040	0.64	1.02	
M	0.090	0.115	2.30	2.91	
N	0.012	0.045	0.30	1.14	
Р	0.180		4.60		
R	0.090		2.30		
DPAK (REV: R0				REV: R0)	

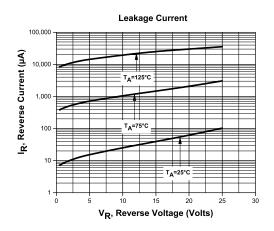
R6 (26-February 2016)

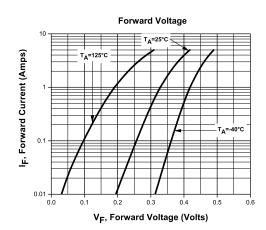
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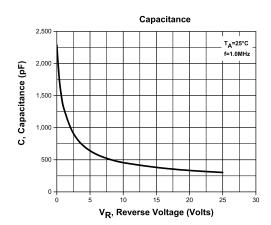
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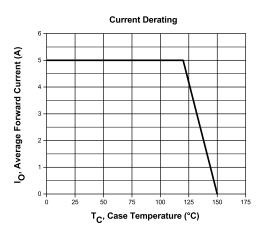


TYPICAL ELECTRICAL CHARACTERISTICS









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