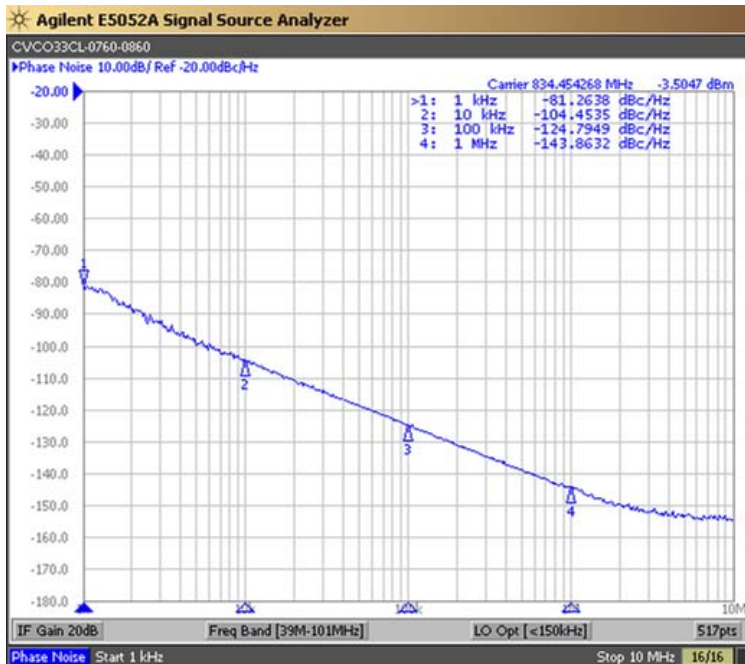
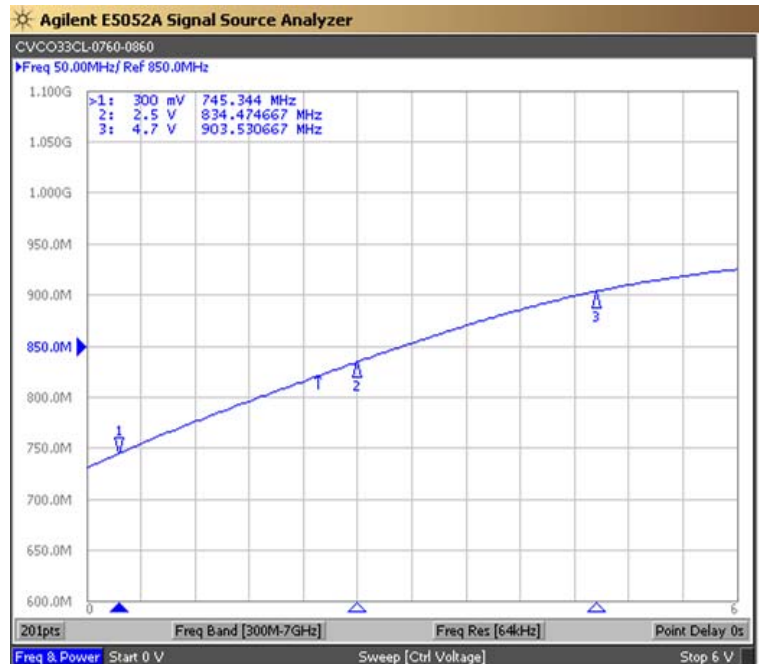


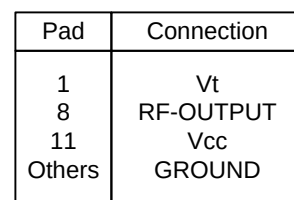
PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			760	MHz
Upper Frequency:	860			MHz
Tuning Voltage:	0.3		4.7	VDC
Supply Voltage:	2.66	2.8	2.94	VDC
Output Power:	-5.0	-4.0	-3.0	dBm
Supply Current:		15	22	mA
Harmonic Suppression (2 nd Harmonic):		-15	-10	dBc
Pushing:		0.7	1.5	MHz/V
Pulling, all Phases:		0.5	1.0	MHz pk-pk
Tuning Sensitivity:		40		MHz/V
Phase Noise @ 10kHz offset:		-105	-103	dBc/Hz
Phase Noise @ 100kHz offset:		-124	-122	dBc/Hz
Load Impedance:		50		Ω
Input Capacitance:			47	pF
Operating Temperature Range:	-40		+85	°C
Storage Temperature Range:	-45		+90	°C

Phase Noise (1 Hz BW, Typical)



Tuning Curve (Typical)





- Unless otherwise specified,
Dimensions are in: $\frac{\text{IN}}{(\text{mm})}$
- Pad Location
Dimensions are in: Inches

Technical drawing of a rectangular plate with five square holes and a row of ten circular holes. The drawing includes the following dimensions and features:

- Top Circular Holes:** A row of ten circles. The diameter is specified as $0.059 \text{ } \varnothing \text{ TYP}$ (1.50 $\varnothing$ TYP). The center-to-center spacing between these holes is 0.157 ± 0.004 (4.00 ± 0.10).
- Bottom Square Holes:** A row of five squares. The center-to-center spacing between these squares is 0.472 (12.00).
- Plate Width:** The overall width of the plate is 0.630 ± 0.012 (16.00 ± 0.30).
- Direction of Feed:** An arrow on the right points to the right, labeled "DIRECTION OF FEED".

Drawing not to scale

Crystek Part Number:	CVCO33CL-0760-0860	Release Date:	13-Dec-2012
Revision Level:	E	Responsible:	C. Vales

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12730 COMMONWEALTH DRIVE • FORT MYERS, FLORIDA 33913
PHONE: 239-561-3311 • 800-237-3061
FAX: 239-561-1025 • WWW.CRYSTEK.COM

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