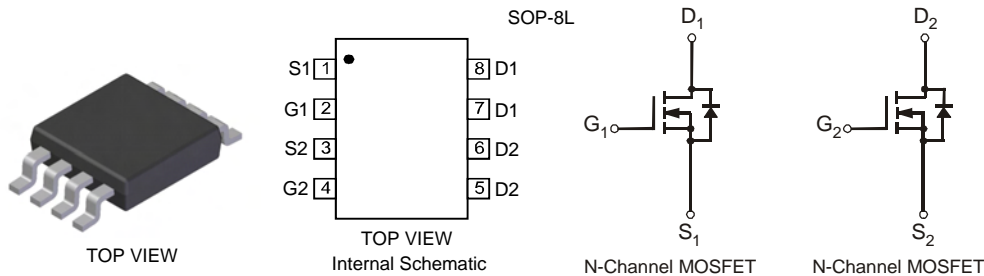


## Features

- Dual N-Channel MOSFET
- Low On-Resistance
  - 24mΩ @  $V_{GS} = 4.5V$
  - 29mΩ @  $V_{GS} = 2.5V$
  - 37mΩ @  $V_{GS} = 1.8V$
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Lead Free By Design/RoHS Compliant (Note 2)**
- **"Green" Device (Note 4)**
- **Qualified to AEC-Q101 Standards for High Reliability**

## Mechanical Data

- Case: SOP-8L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals Connections: See Diagram
- Terminals: Finish - Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.072g (approximate)



## Maximum Ratings @ $T_A = 25^\circ C$ unless otherwise specified

Characteristic			Symbol	Value	Units
Drain-Source Voltage			$V_{DSS}$	20	V
Gate-Source Voltage			$V_{GSS}$	$\pm 8$	V
Drain Current (Note 1)	Steady State	$T_A = 25^{\circ}C$	$I_D$	8	A
		$T_A = 70^{\circ}C$		6.7	
Pulsed Drain Current (Note 3)			$I_{DM}$	30	A

## Thermal Characteristics

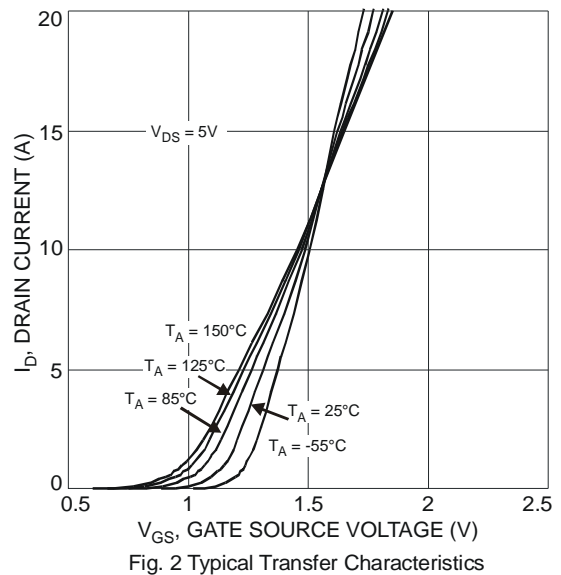
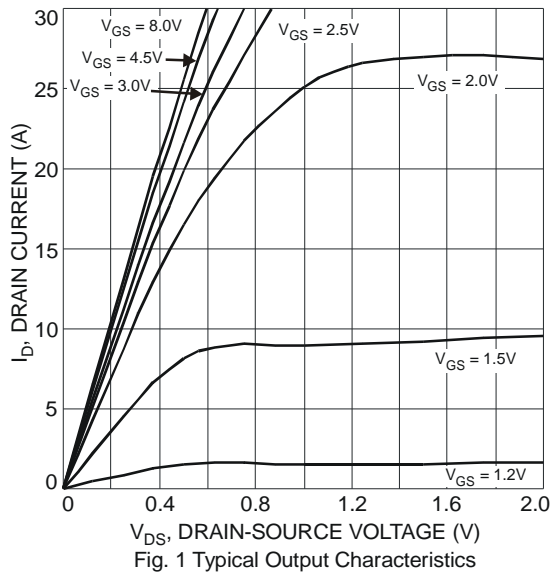
Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 1)	$P_D$	1.3	W
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	96	$^\circ C/W$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ C$

- Notes:
1. Device mounted on FR-4 PCB with minimum recommended pad layout.
  2. No purposefully added lead.
  3. Repetitive rating, pulse width limited by function temperature.
  4. Diodes Inc.'s "Green" policy can be found on our website at [http://www.diodes.com/products/lead\\_free/index.php](http://www.diodes.com/products/lead_free/index.php).

**Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 5)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	μA	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±8V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 5)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.5	—	0.9	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	—	19	24	mΩ	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 8.2A
			23	29		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 3.3A
			29	37		V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 2.0A
Forward Transfer Admittance	Y <sub>fs</sub>	—	7	—	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 4A
Diode Forward Voltage (Note 5)	V <sub>SD</sub>	0.5	—	0.9	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1A
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	C <sub>iss</sub>	—	867	—	pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	85	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	81	—	pF	
Gate Resistance	R <sub>G</sub>	—	1.29	—	Ω	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V, f = 1.0MHz
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge	Q <sub>g</sub>	—	8.8	—	nC	V <sub>GS</sub> = 4.5V, V <sub>DS</sub> = 10V, I <sub>D</sub> = 8.2A
Gate-Source Charge	Q <sub>gs</sub>	—	1.2	—	nC	
Gate-Drain Charge	Q <sub>gd</sub>	—	3.0	—	nC	
Turn-On Delay Time	t <sub>d(on)</sub>	—	13.2	—	ns	V <sub>DD</sub> = 10V, V <sub>GS</sub> = 4.5V, R <sub>L</sub> = 10Ω, R <sub>G</sub> = 6Ω
Turn-On Rise Time	t <sub>r</sub>	—	12.6	—	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	—	64.8	—	ns	
Turn-Off Fall Time	t <sub>f</sub>	—	21.7	—	ns	

Notes: 5. Short duration pulse test used to minimize self-heating effect.



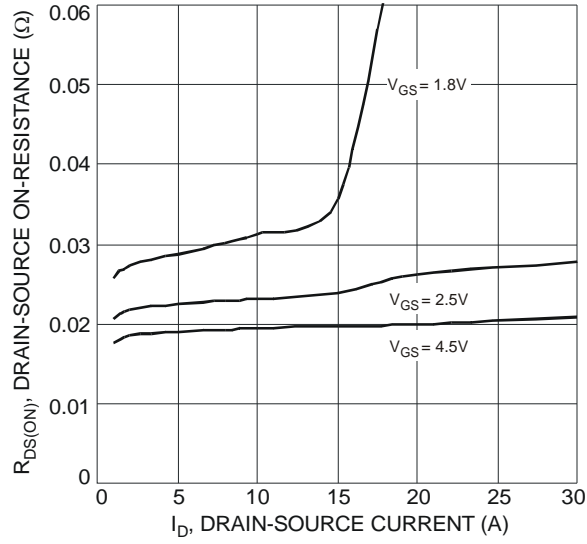


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

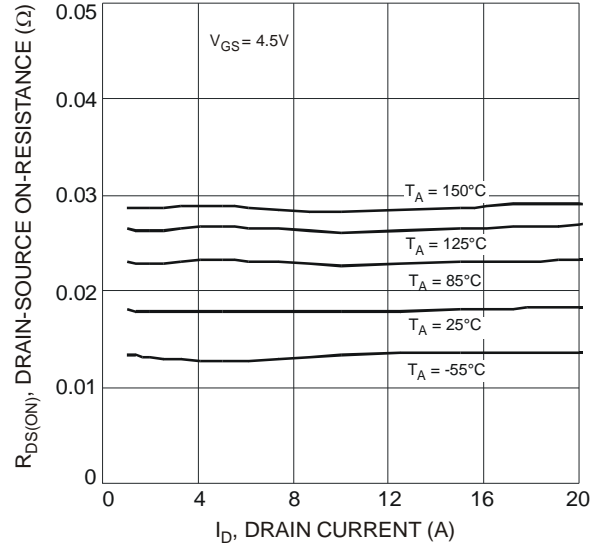


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature

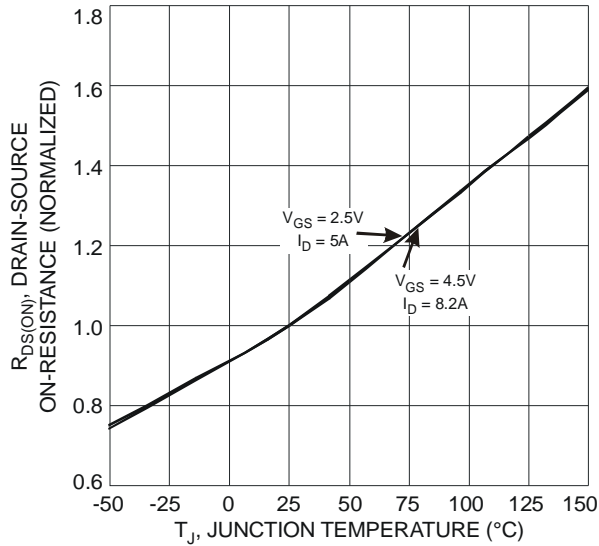


Fig. 5 On-Resistance Variation with Temperature

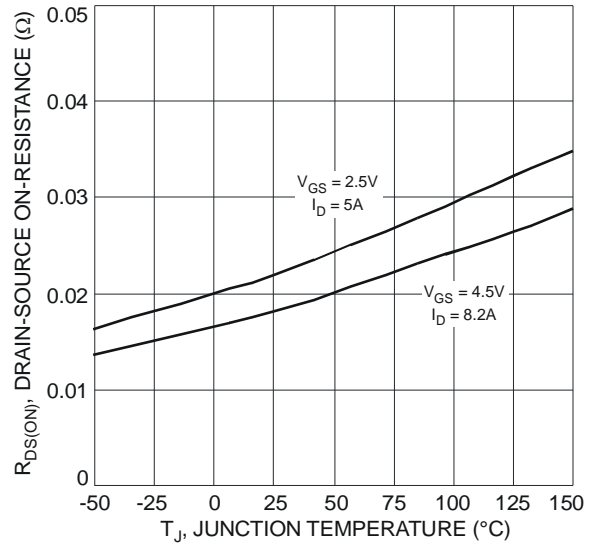


Fig. 6 On-Resistance Variation with Temperature

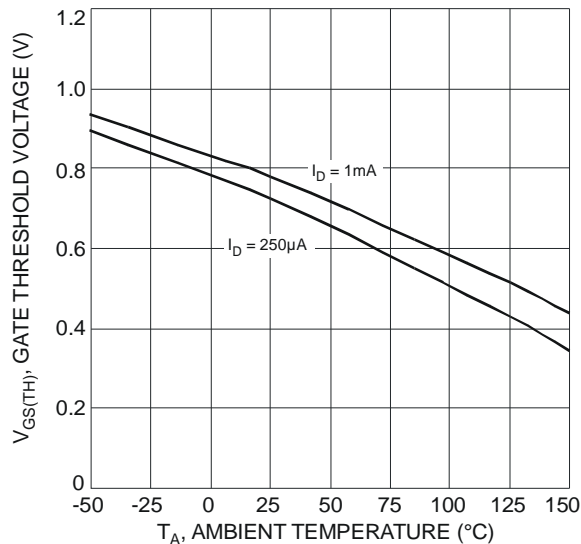


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

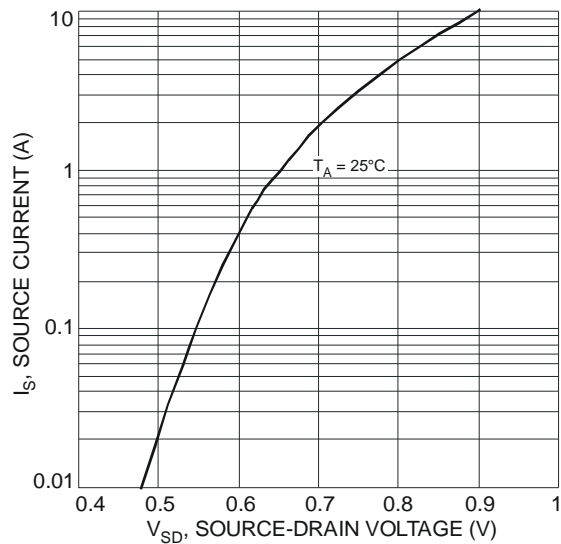


Fig. 8 Diode Forward Voltage vs. Current

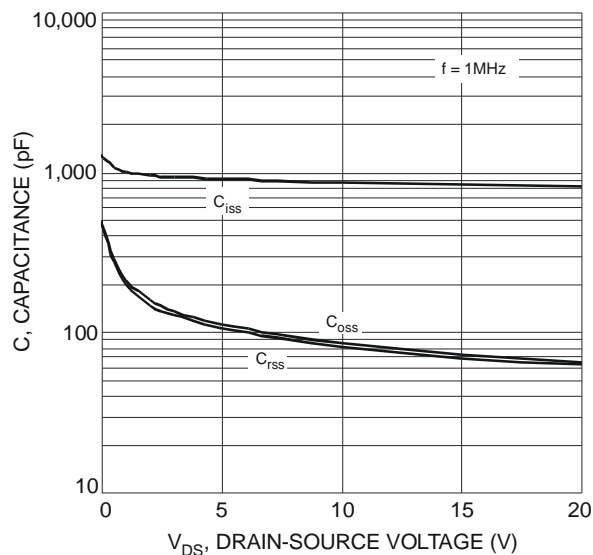


Fig. 9 Typical Capacitance

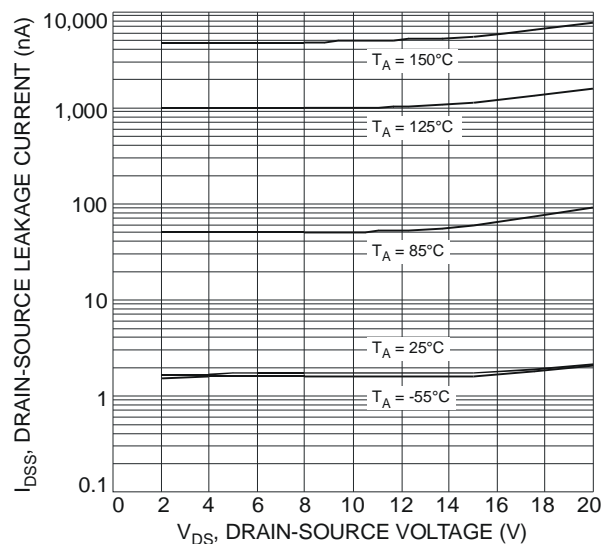


Fig. 10 Typical Drain-Source Leakage Current vs. Drain-Source Voltage

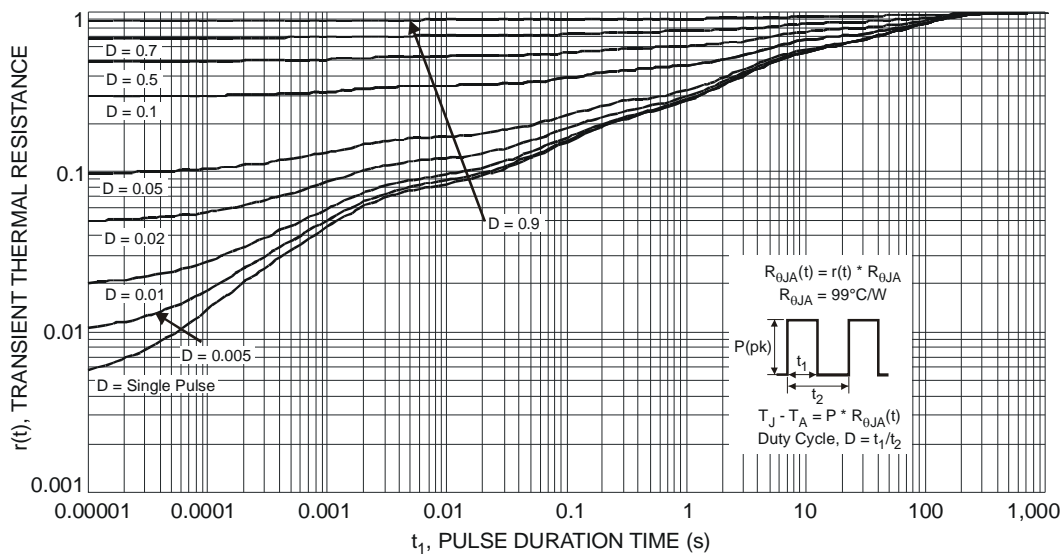


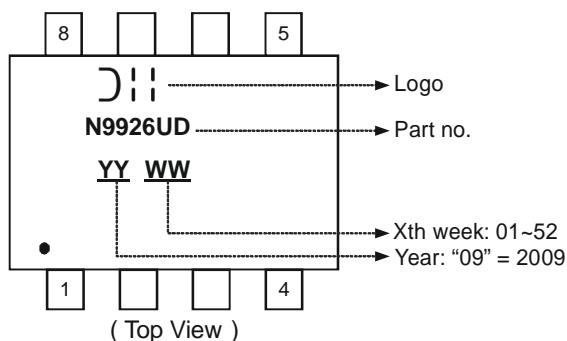
Fig. 11 Transient Thermal Response

## Ordering Information (Note 6)

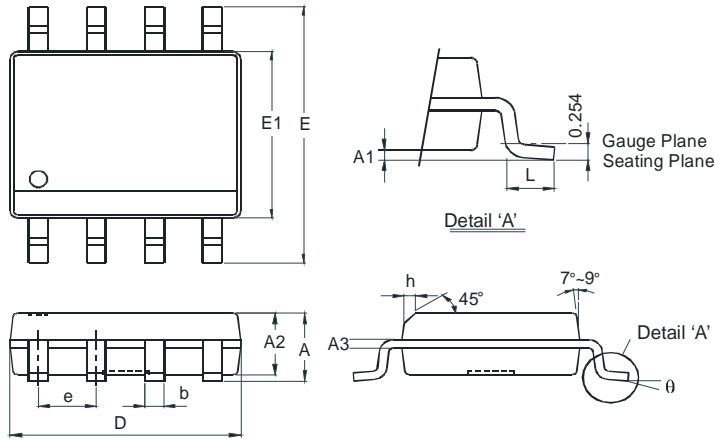
Part Number	Case	Packaging
DMG9926USD-13	SOP-8L	2500/Tape & Reel

Notes: 6. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

## Marking Information

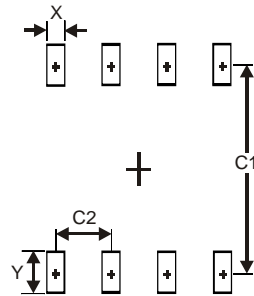


## Package Outline Dimensions



SOP-8L		
Dim	Min	Max
A	-	1.75
A1	0.10	0.20
A2	1.30	1.50
A3	0.15	0.25
b	0.3	0.5
D	4.85	4.95
E	5.90	6.10
E1	3.85	3.95
e	1.27 Typ	
h	-	0.35
L	0.62	0.82
θ	0°	8°
All Dimensions in mm		

## Suggested Pad Layout



Dimensions	Value (in mm)
X	0.60
Y	1.55
C1	5.4
C2	1.27

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