





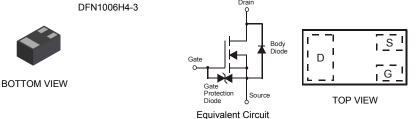
#### N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

### **Features**

- N-Channel MOSFET
- Low On-Resistance
- Very Low Gate Threshold Voltage, 1.2V max
- Low Input Capacitance
- · Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- ESD Protected Gate
- Lead Free By Design/RoHS Compliant (Note 2)
- "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability



- Case: DFN1006H4-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.001 grams (approximate)



## **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Drain Source Voltage	$V_{DSS}$	30	V
Gate-Source Voltage	$V_{GSS}$	±10	V
Drain Current (Note 1)	I <sub>D</sub>	300	mA

## Thermal Characteristics @TA = 25°C unless otherwise specified

Total Power Dissipation (Note 1) @T <sub>A</sub> = 25°C	$P_{D}$	350	mW
Thermal Resistance, Junction to Ambient (Note 1)	$R_{ hetaJA}$	357	°C/W
Operating and Storage Temperature Range	T <sub>j</sub> , T <sub>STG</sub>	-55 to +150	°C

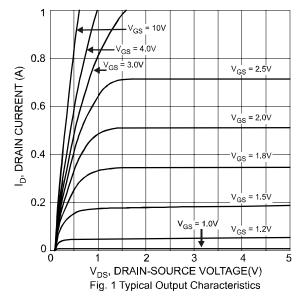
### **Electrical Characteristics** @TA = 25°C unless otherwise specified

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)							
Drain-Source Breakdown Voltage		$BV_{DSS}$	30	_	_	V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current	@ T <sub>C</sub> = 25°C	I <sub>DSS</sub>	_	_	1	μА	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Body Leakage		I <sub>GSS</sub>	_	_	±10 ±500	μA nA	$V_{GS} = \pm 10V, V_{DS} = 0V$ $V_{GS} = \pm 5V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 4)							
Gate Threshold Voltage		$V_{GS(th)}$	0.6	_	1.2	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance		R <sub>DS (ON)</sub>	_		2.2 1.5 1.2	Ω	$V_{GS} = 1.8V$ , $I_D = 20mA$ $V_{GS} = 2.5V$ , $I_D = 20mA$ $V_{GS} = 4.0V$ , $I_D = 100mA$
Forward Transconductance		Y <sub>fs</sub>	100	_	_	mS	V <sub>DS</sub> =10V, I <sub>D</sub> = 0.1A
Source-Drain Diode Forward Voltage		$V_{SD}$	0.5		1.4	V	$V_{GS} = 0V, I_{S} = 115mA$
DYNAMIC CHARACTERISTICS							
Input Capacitance		C <sub>iss</sub>	_	39		pF	.,
Output Capacitance		Coss	_	10	_	pF	$V_{DS} = 3V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance		C <sub>rss</sub>	_	3.6		pF	1 - 1.0WII IZ
Switching Time	Turn-on Time	t <sub>on</sub>		11	_	nS	$V_{DD}$ = 5V, $I_{D}$ = 10 mA,
Ownering Time	Turn-off Time	t <sub>off</sub>	_	51		nS	$V_{GS}$ = 0-5 $V$

Notes: 1. Device mounted on FR-4 PCB, pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

- 2. No purposefully added lead.
- 3. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.
- 4. Short duration pulse test used to minimize self-heating effect.





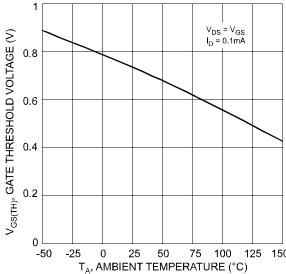


Fig. 3 Gate Threshold Voltage vs. Ambient Temperature

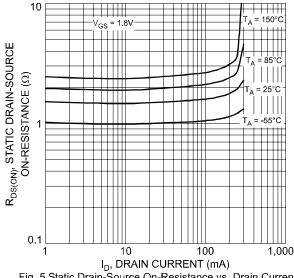
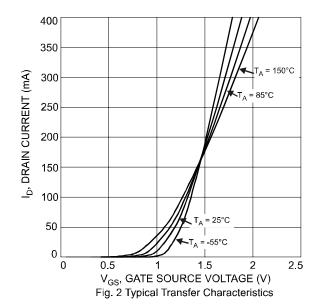


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current



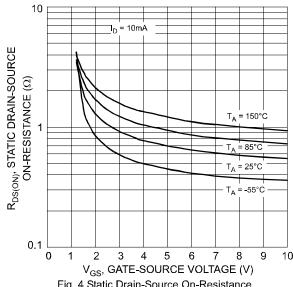


Fig. 4 Static Drain-Source On-Resistance vs. Gate-Source Voltage

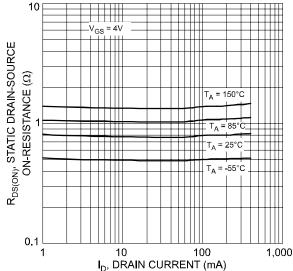


Fig. 6 Static Drain-Source On-Resistance vs. Drain Current



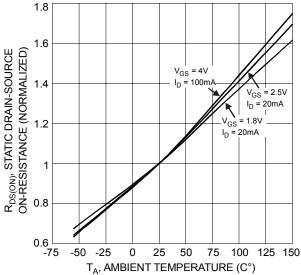


Fig. 7 Normalized Static Drain-Source On-Resistance vs. Ambient Temperature

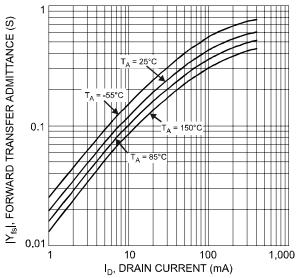


Fig. 9 Forward Transfer Admittance vs. Drain Current

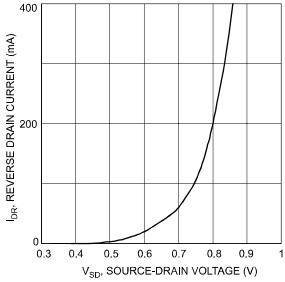
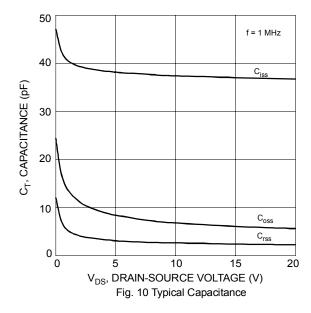


Fig. 8 Reverse Drain Current vs. Source-Drain Voltage



## Ordering Information (Note 5)

DMN32D2LFB4-7 DFN1006H4-3	3 3000/Tape & Reel

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

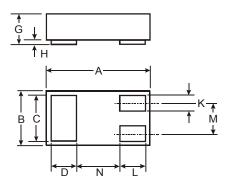
# **Marking Information**

• DV

DV = Product Type Marking Code

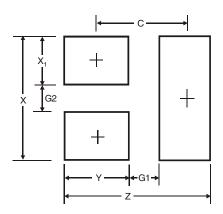


# **Package Outline Dimensions**



DFN1006-3				
Dim	Min	Max	Тур	
Α	0.95	1.075	1.00	
В	0.55	0.675	0.60	
С	0.45	0.55	0.50	
D	0.20	0.30	0.25	
G	0.47	0.53	0.50	
Н	0	0.05	0.03	
K	0.10	0.20	0.15	
L	0.20	0.30	0.25	
M	_	_	0.35	
N		_	0.40	
All Dimensions in mm				

## **Suggested Pad Layout**



Dimensions	Value (in mm)
Z	1.1
G1	0.3
G2	0.2
X	0.7
X1	0.25
Y	0.4
С	0.7

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