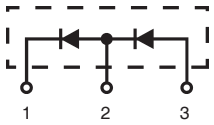
IXYS

DSEE 8-08CC

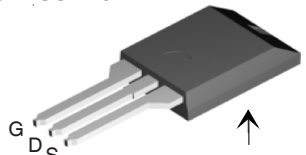
HiPerDynFRED™ Epitaxial Diode
ISOPLUS220™
Electrically Isolated Back Surface

I_{FAV} = 10 A
V_{RRM} = 800 V
t_{rr} = 30 ns

V_{RRM} ① V	V_{RRM} V	Type
800	400	DSEE 8-08CC



ISOPLUS 220LV™



Isolated back surface

Preliminary Data Sheet

Symbol	Conditions	Maximum Ratings	
I_{FRMS}		20	A
I_{FAVM}	T_C = 130°C; rectangular, d = 0.5	10	A
I_{FSM}	T_{VJ} = 45°C; t_p = 10 ms (50 Hz), sine	60	A
E_{AS}	T_{VJ} = 25°C; non-repetitive I_{AS} = 2 A; L = 180 µH	0.5	mJ
I_{AR}	$V_A = 1.5 \cdot V_R$ typ.; f = 10 kHz; repetitive	0.2	A
T_{VJ}		-40...+175	°C
T_{VJM}		175	°C
T_{stg}		-40...+150	°C
P_{tot}	T_C = 25°C	55	W
V_{ISOL}	50/60 Hz RMS; $I_{ISOL} \leq$ 1 mA	2500	V~
F_C	Mounting force	11...65 / 2.4...11	N / lb
Weight	typical	2	g

Symbol	Conditions	Characteristic Values typ.	max.	
I_R ②	T_{VJ} = 25°C $V_R = V_{RRM}$		60	µA
	T_{VJ} = 150°C $V_R = V_{RRM}$		0.25	mA
V_F ③	I_F = 10 A; T_{VJ} = 125°C		1.12	V
	T_{VJ} = 25°C		1.53	V
R_{thJC}			2.75	K/W
R_{thCH}		0.6		K/W
t_{rr}	I_F = 1 A; -di/dt = 50 A/µs; V_R = 30 V; T_{VJ} = 25°C	30		ns
I_{RM}	V_R = 100 V; I_F = 12 A; $-di_F/dt$ = 100 A/µs T_{VJ} = 100°C	2	2.4	A

Features
- Silicon chip on Direct-Copper-Bond substrate
  - High power dissipation
  - Isolated mounting surface
  - 2500V electrical isolation
- Low cathode to tab capacitance (<15pF)
- Planar passivated chips
- Very short recovery time
- Extremely low switching losses
- Low I_{RM}-values
- Soft recovery behaviour
- Epoxy meets UL 94V-0

Applications
- Antiparallel diode for high frequency switching devices
- Antisaturation diode
- Snubber diode
- Free wheeling diode in converters and motor control circuits
- Rectifiers in switch mode power supplies (SMPS)
- Inductive heating
- Uninterruptible power supplies (UPS)
- Ultrasonic cleaners and welders

Advantages
- Avalanche voltage rated for reliable operation
- Soft reverse recovery for low EMI/RFI
- Low I_{RM} reduces:
  - Power dissipation within the diode
  - Turn-on loss in the commutating switch

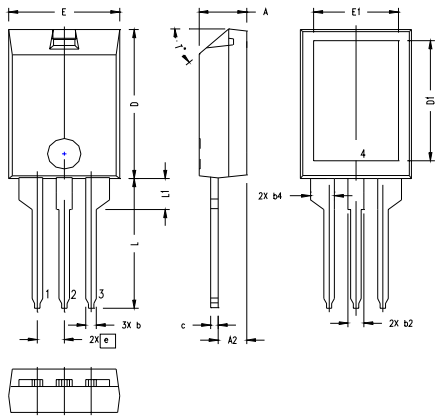
Notes: Data given for T_{VJ} = 25°C and per diode unless otherwise specified
① Diodes connected in series
② Pulse test: pulse Width = 5 ms, Duty Cycle < 2.0 %
③ Pulse test: pulse Width = 300 µs, Duty Cycle < 2.0 %

Recommended replacement:
DPG 10P400PJ

IXYS reserves the right to change limits, test conditions and dimensions.

DS99053(05/03)

ISOPLUS220LV Outline



SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.157	.197	4.00	5.00
A2	.098	.118	2.50	3.00
b	.035	.051	0.90	1.30
b2	.049	.065	1.25	1.65
b4	.093	.100	2.35	2.55
c	.028	.039	0.70	1.00
D	.591	.630	15.00	16.00
D1	.472	.512	12.00	13.00
E	.394	.433	10.00	11.00
E1	.295	.335	7.50	8.50
e	.100 BASIC		2.55 BASIC	
L	.512	.571	13.00	14.50
L1	.118	.138	3.00	3.50
T°			42.5°	47.5°

NOTE:
1. Bottom heatsink (Pin 4) is electrically isolated from Pin 1, 2, or 3.
2. This drawing will meet dimensional requirement of JEDEC SS Product Outline TO-273 except D and D1 dimension.