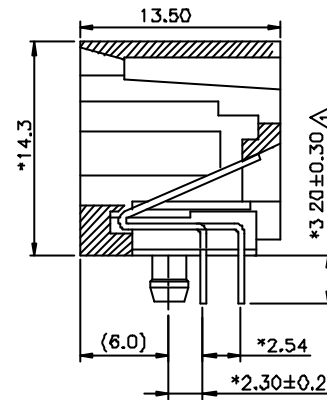
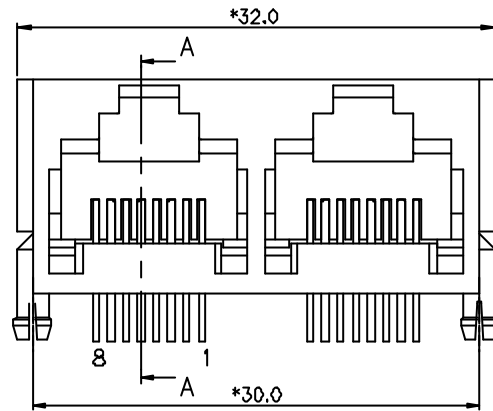
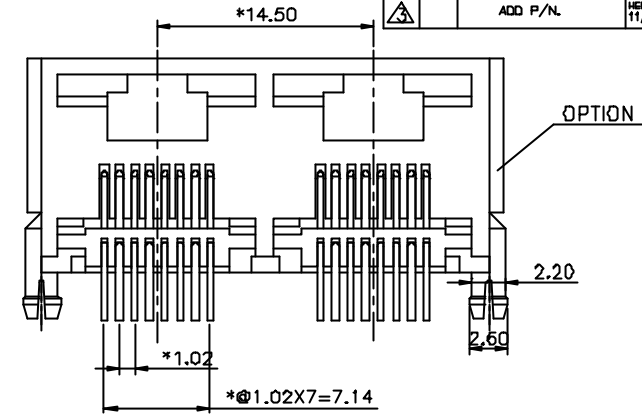




SECTION A-A



REVISION RECORD				
REV	ECO	DESCRIPTION	DRFT	CHKD
△		ADD P/N.	05/08/05	
△		ADD P/N.	10/18/05	
△		ADD P/N.	11/3/05	

NOTES:**ELECTRICAL:**

1. VOLTAGE RATING : 125 VAC RMS.
2. CURRENT RATING : 1.5 AMP.
3. CONTACT RESISTANCE : 30 MILLIOHMS MAX.
4. INSULATION RESISTANCE: 1000 MEGOHMS MIN @ 500 VDC.
5. DIELECTRIC STRENGTH : 1000 VAC RMS 60HZ, 1MIN.

MECHANICAL:

1. HOUSING MATERIAL : PBT+30%GF UL94V-0
2. INSERT MATERIAL : PBT+30%GF UL94V-0
3. CONTACT MATERIAL : PHOSPHOR BRONZE Ø0.46mm.
4. PLATING : GOLD PLATING OVER NICKEL.
5. OPERATING LIFE : 750 CYCLES MIN.
6. PCB RETENTION PRE-SOLDER : 1 LB MIN.
7. PCB RETENTION POST-SOLDER. 10 LBS MIN.

ENVIRONMENTAL:

1. STORAGE : -40°C TO +85°C.
2. OPERATION: 0°C TO 70°C.
3. WAVE SOLDERING TEMPERATURE: 230~250° (3~5 SECONDS)

MATES WITH MODULAR PLUG CONFORMING TO

FCC PART 68, SUBPART F.

CUL FILE NO E163191

FREE LEAD PART NUMBER: E558X-20AXXX-L

NO. OF CONTACT:

2- 2C

4- 4C

6- 6C

8- 8C

1- 3u" 2- 6u"

3-15u" 4-30u"

5-50u" 6-Pd 20u"

1- WITH PANEL STOP

2- W/O PANEL STOP

4- W/ THREE MERELY PANEL STOP △

5- W/O THREE MERELY PANEL STOP △

5-A06-0080

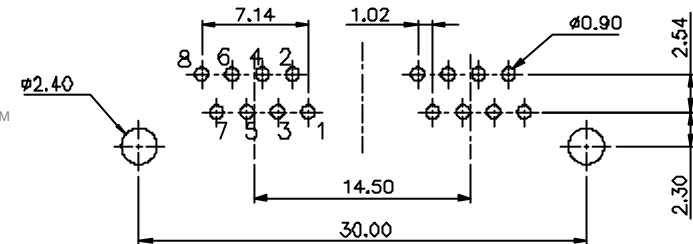
△ D-A06-2380

△ K-A05-0220

昵夕厂ノ

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PC Board Layout Component Side Shown

DETACHED LIST MIN 0 ± 0.2 .00 ± 0.10 .000 ± 0.075 ANGLES ± 0.5 THIRD ANGLE PROJECTION	DFTO YAN CHKD INFO APPLD 0808 DATE 11/05/05	DATE 03/28/05 DATE DATE 11/05/05	FULL RISE ELECTRONIC CO., LTD	
	TITLE MINI L/P DUAL PORT 8P		DRAWING NO GE553D37-LC	SIZE A3
	MATERIAL : QTY : FINISH		/PART NO SEE NOTE	REV 3
	SCALE : 3 : 1		DO NOT SCALE DRAWING SHEET OF	