



# EFC6602R — N-Channel Silicon MOSFET

## Lithium-ion battery charging and discharging switch

### Features

- 2.5V drive
- Common-drain type
- 2KV ESD HBM
- Protection diode in
- Halogen free compliance

### Specifications

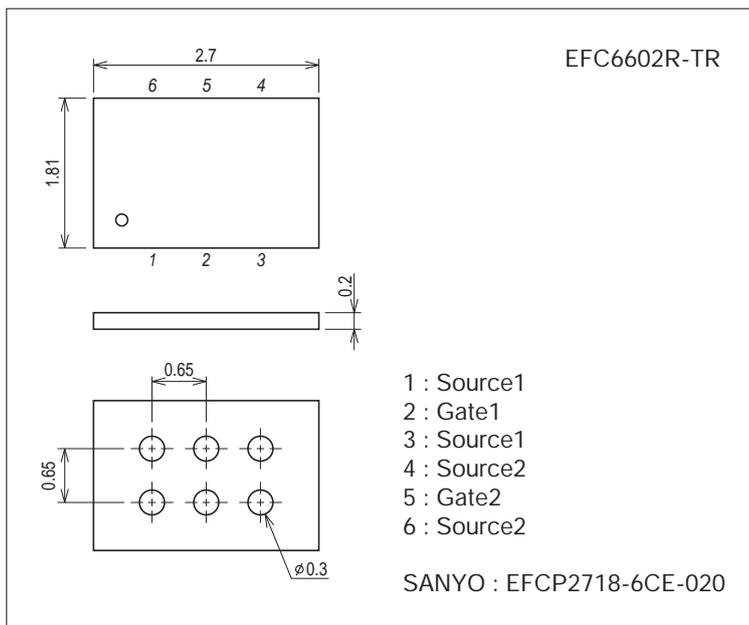
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Source-to-Source Voltage	VSSS		12	V
Gate-to-Source Voltage	VGSS		±12	V
Source Current (DC)	IS		18	A
Source Current (Pulse)	ISP	PW≤10μs, duty cycle≤1%	60	A
Total Dissipation	PT	When mounted on ceramic substrate (5000mm²×0.8mm)	2.0	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

### Package Dimensions

unit : mm (typ)

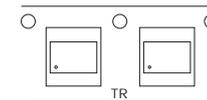
7073-001



### Product & Package Information

- Package : EFCP
- JEITA, JEDEC : -
- Minimum Packing Quantity : 5,000 pcs./reel

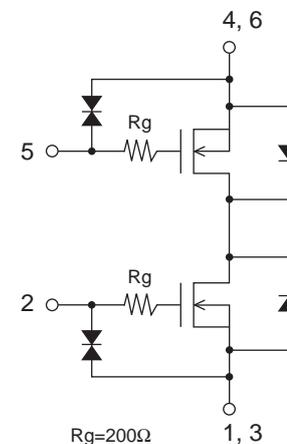
Taping Type : TR



Marking



### Electrical Connection



# EFC6602R

## Electrical Characteristics at Ta=25°C

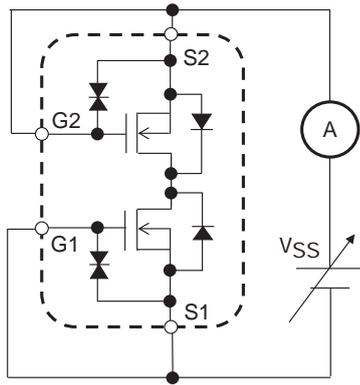
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Source-to-Source Breakdown Voltage	V(BR)SSS	IS=1mA, VGS=0V Test Circuit 1	12			V
Zero-Gate Voltage Source Current	ISSS	VSS=10V, VGS=0V Test Circuit 1			1	μA
Gate-to-Source Leakage Current	IGSS	VGS=±8V, VSS=0V Test Circuit 2			±1	μA
Cutoff Voltage	VGS(off)	VSS=6V, IS=1mA Test Circuit 3	0.5		1.3	V
Forward Transfer Admittance	yfs	VSS=6V, IS=3A Test Circuit 4		13		S
Static Source-to-Source On-State Resistance	RSS(on)1	IS=3A, VGS=4.5V Test Circuit 5	3.1	4.5	5.9	mΩ
	RSS(on)2	IS=3A, VGS=4.0V Test Circuit 5	3.3	4.8	6.3	mΩ
	RSS(on)3	IS=3A, VGS=3.8V Test Circuit 5	3.5	5	6.5	mΩ
	RSS(on)4	IS=3A, VGS=3.1V Test Circuit 5	4.0	5.8	8.2	mΩ
	RSS(on)5	IS=3A, VGS=2.5V Test Circuit 5	5.2	7.5	11	mΩ
Turn-ON Delay Time	td(on)	VDD=6V, VGS=4.5V, IS=3A Test Circuit 7		530		ns
Rise Time	tr			2100		ns
Turn-OFF Delay Time	td(off)			6200		ns
Fall Time	tf			5500		ns
Total Gate Charge	Qg		VDD=6V, VGS=4.5V, IS=18A Test Circuit 8		55	
Forward Source-to-Source Voltage	VF(S-S)	IS=3A, VGS=0V Test Circuit 6		0.76	1.2	V

## Ordering Information

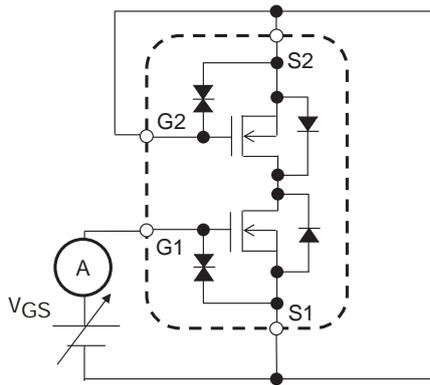
Device	Package	Shipping	memo
EFC6602R-TR	EFCP	5,000pcs./reel	Pb Free and Halogen Free

Test circuits are example of measuring FET1 side

Test Circuit 1  
I<sub>SSS</sub>

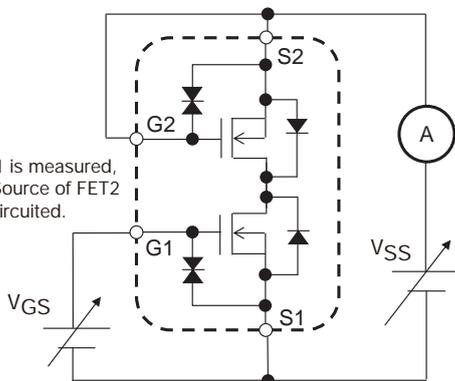


Test Circuit 2  
I<sub>GSS</sub>



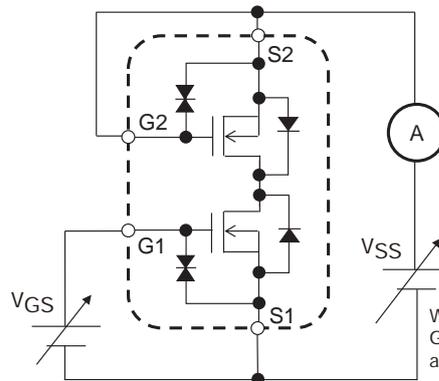
When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 3  
V<sub>GS(off)</sub>



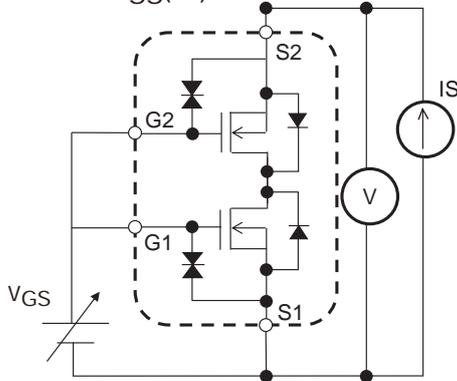
When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 4  
|y<sub>fs</sub>|

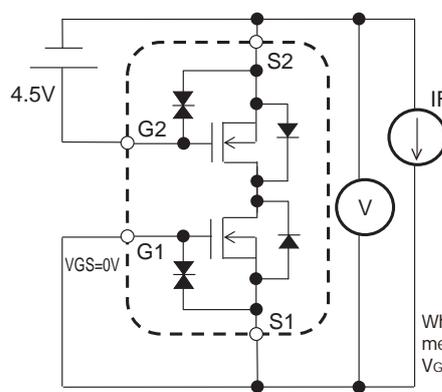


When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 5  
R<sub>SS(on)</sub>

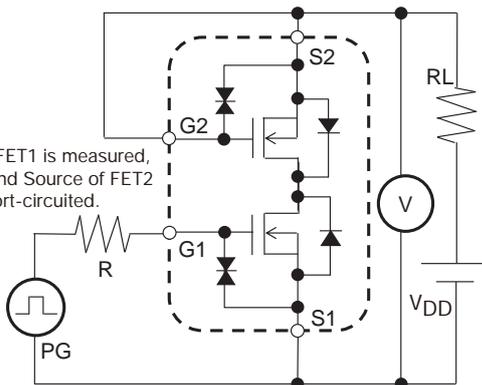


Test Circuit 6  
V<sub>F(S-S)</sub>



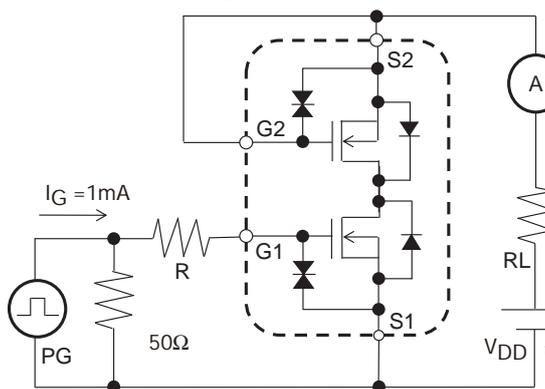
When FET1 is measured, +4.5V is added to V<sub>GS</sub> of FET2.

Test Circuit 7  
t<sub>d(on)</sub>, t<sub>r</sub>, t<sub>d(off)</sub>, t<sub>f</sub>

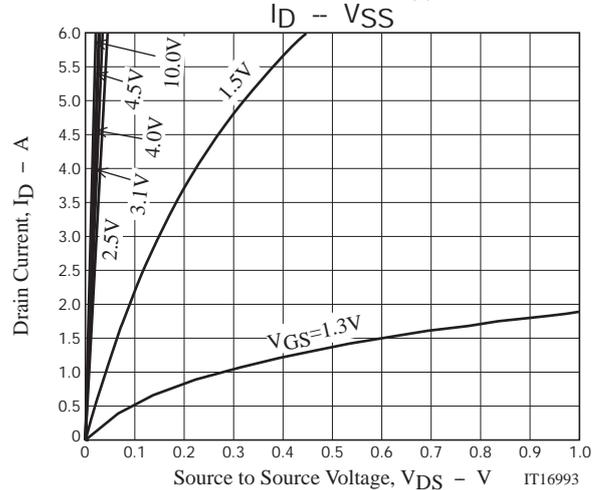
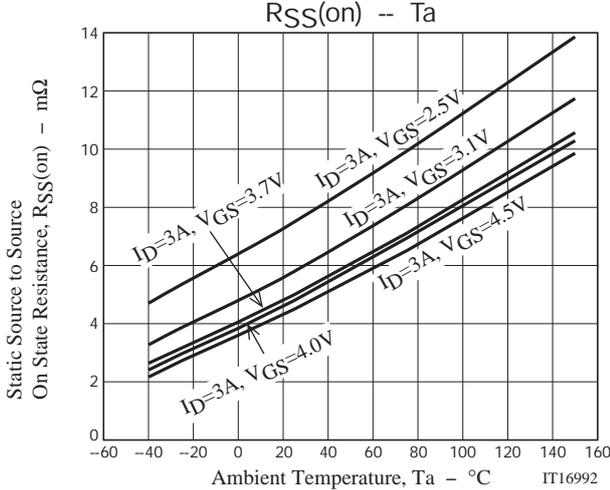
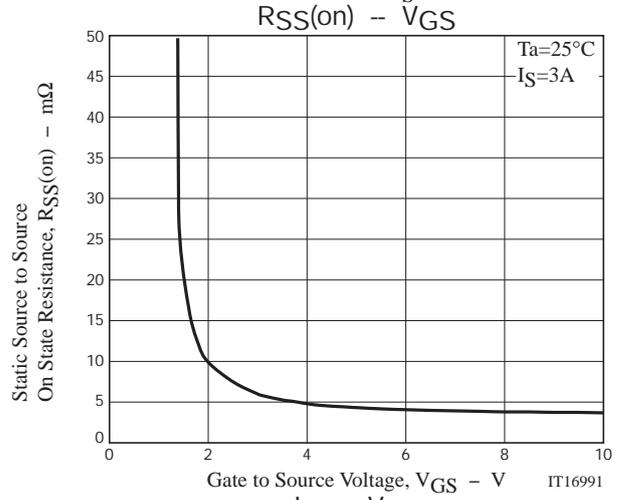
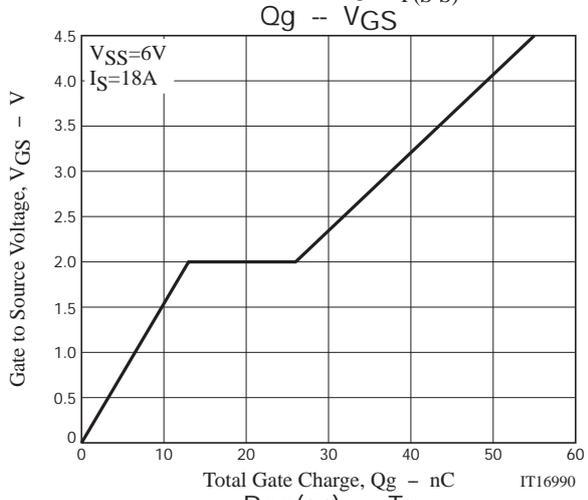
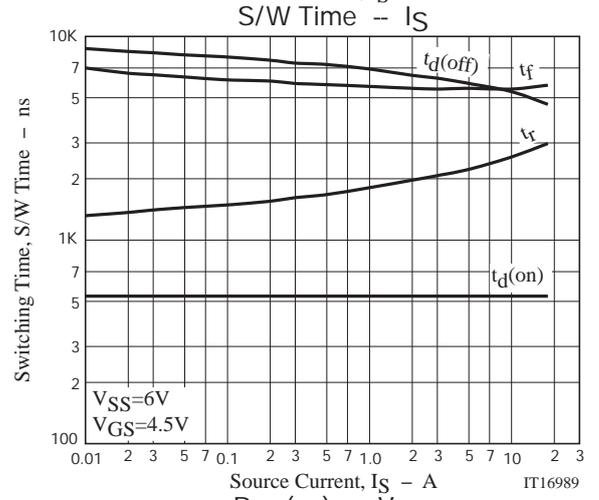
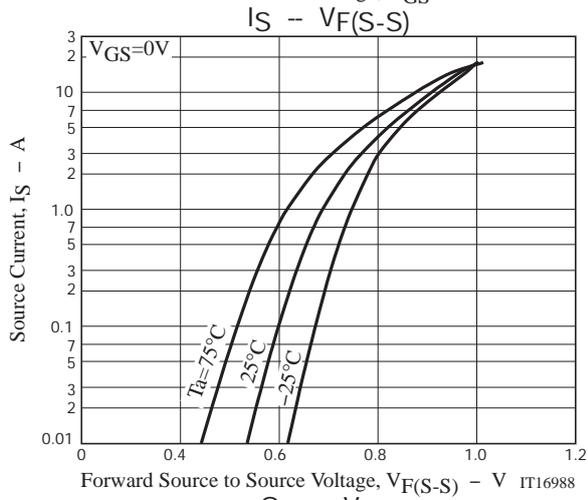
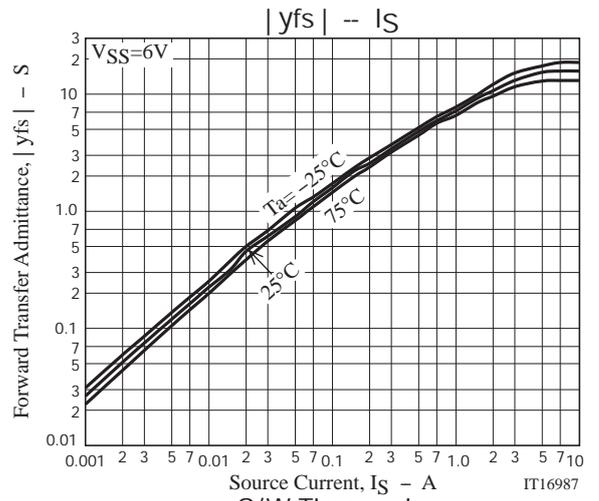
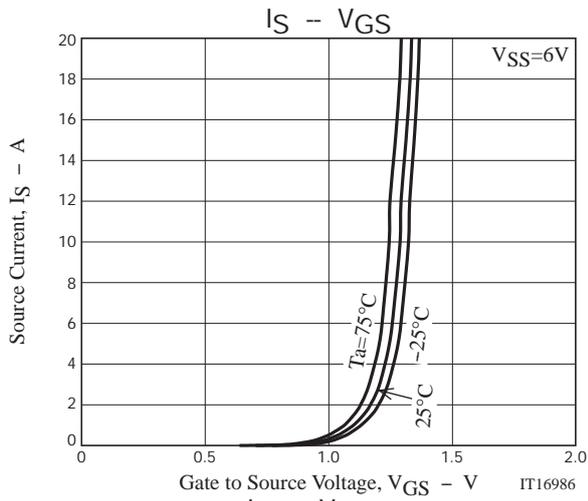


When FET1 is measured, Gate and Source of FET2 are short-circuited.

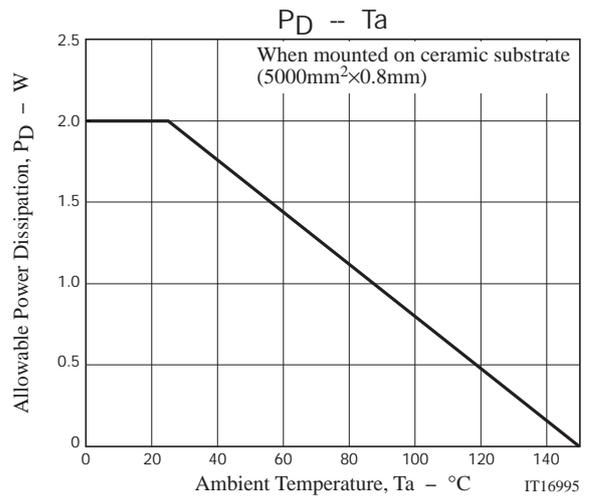
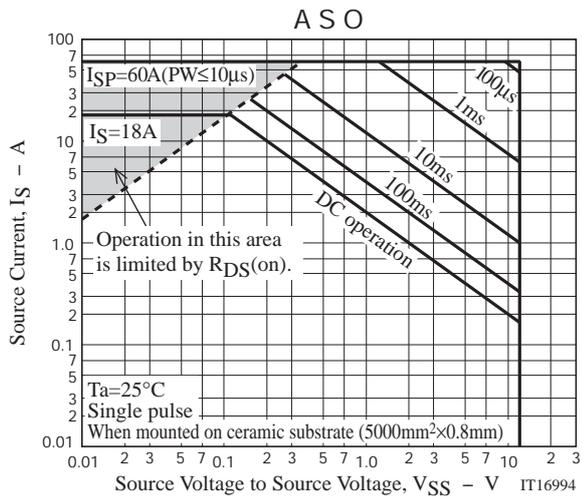
Test Circuit 8  
Q<sub>g</sub>



When FET1 is measured, Gate and Source of FET2 are short-circuited.



# EFC6602R

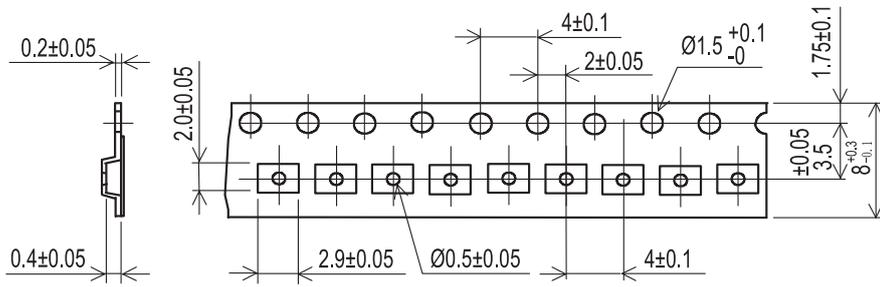


Taping Specification

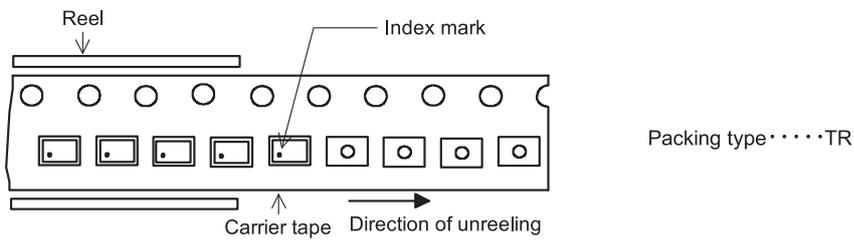
EFC6602R-TR

1. Taping Configuration

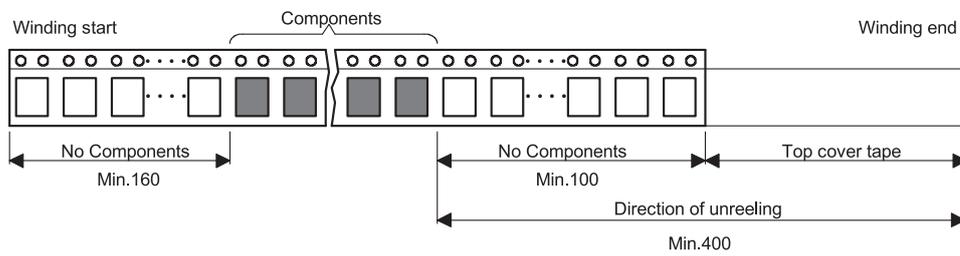
1-1 .Carrier Tape Size (unit:mm)



1-2 .Device Placement Direction



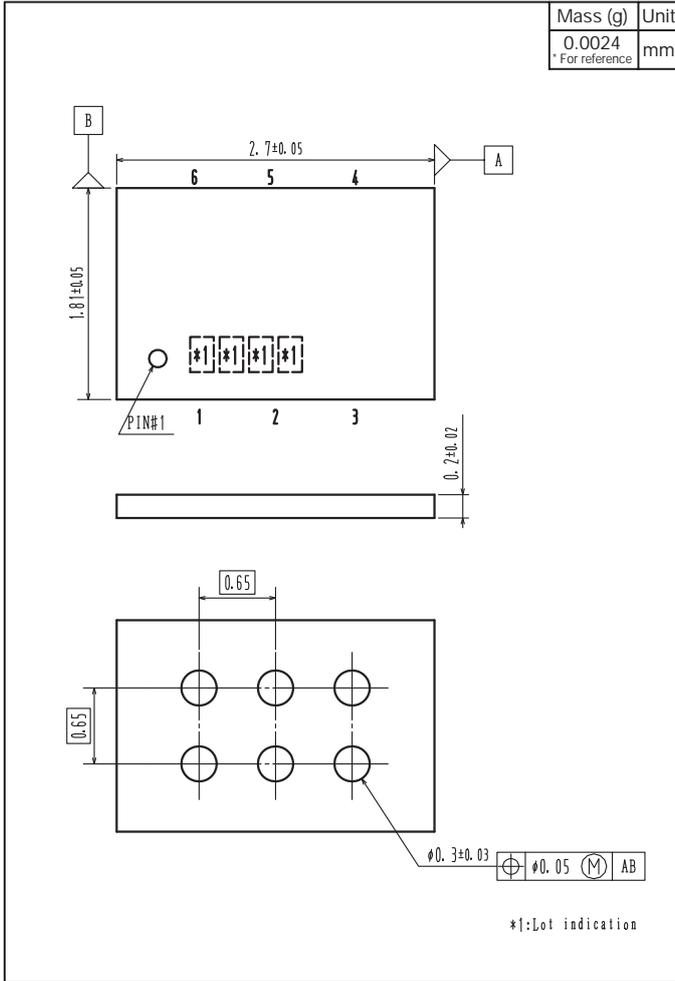
1-3 .Leader portion and Trailer portion (unit:mm)



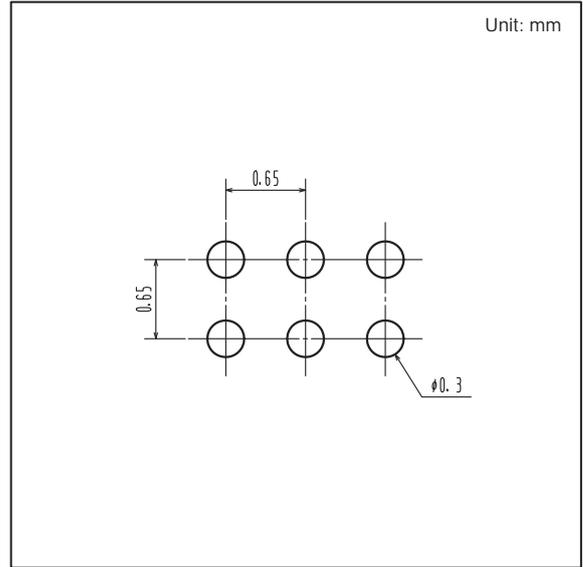
# EFC6602R

## Outline Drawing

EFC6602R-TR



## Land Pattern Example



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