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April 2007

### FDC6331L

### **Integrated Load Switch**

### **General Description**

This device is particularly suited for compact power management in portable electronic equipment where 2.5V to 8V input and 2.8A output current capability are needed. This load switch integrates a small N-Channel power MOSFET (Q1) that drives a large PChannel power MOSFET (Q2) in one tiny SuperSOT<sup>TM</sup>-6 package.

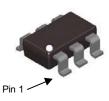
### **Applications**

- · Load switch
- Power management

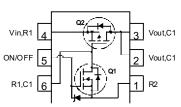


### **Features**

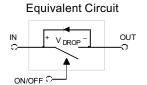
- -2.8 A, -8 V.  $R_{DS(ON)} = 55$  m $\Omega$  @  $V_{GS} = -4.5$  V  $R_{DS(ON)} = 70$  m $\Omega$  @  $V_{GS} = -2.5$  V  $R_{DS(ON)} = 100$  m $\Omega$  @  $V_{GS} = -1.8$  V
- Control MOSFET (Q1) includes Zener protection for ESD ruggedness (>6KV Human body model)
- High performance trench technology for extremely low  $R_{\mbox{\scriptsize DS(ON)}}$







See Application Circuit



### Absolute Maximum Ratings T<sub>A</sub>=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V <sub>IN</sub>	Maximum Input Voltage		± 8	V
V <sub>ON/OFF</sub>	High level ON/OFF voltage range	-0.5 to 8	V	
Load	Load Current - Continuous	(Note 1)	2.8	A
	- Pulsed		9	
P₀	Maximum Power Dissipation	(Note 1)	0.7	W
$T_J$ , $T_{STG}$	Operating and Storage Junction Temperature Range		-55 to +150	°C

### **Thermal Characteristics**

R <sub>0JA</sub>	Thermal Resistance, Junction-to-Ambient	(Note 1)	180	°C/W
R <sub>eJC</sub>	Thermal Resistance, Junction-to-Case	(Note 1)	60	°C/W

**Package Marking and Ordering Information** 

Device Marking	Device	Reel Size	Tape width	Quantity
.331	FDC6331L	7"	8mm	3000 units

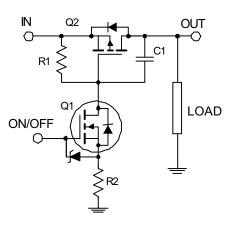
Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Chai	racteristics		•			•
BV <sub>IN</sub>	Vin Breakdown Voltage	$V_{ON/OFF} = 0 \text{ V}, I_D = -250 \mu\text{A}$	8			V
Load	Zero Gate Voltage Drain Current	V <sub>IN</sub> = 6.4 V, V <sub>ON/OFF</sub> = 0 V			-1	μΑ
I <sub>FL</sub>	Leakage Current, Forward	V <sub>ON/OFF</sub> = 0 V, V <sub>IN</sub> = 8 V			-100	nA
I <sub>RL</sub>	Leakage Current, Reverse	V <sub>ON/OFF</sub> = 0 V, V <sub>IN</sub> = -8 V			100	nA
On Chai	racteristics (Note 2)					
V <sub>ON/OFF (th)</sub>	Gate Threshold Voltage	$V_{IN} = V_{ON/OFF}$ , $I_D = -250 \mu A$	0.4	0.9	1.5	V
R <sub>DS(on)</sub>	Static Drain–Source	$V_{GS} = -4.5 \text{ V}, \qquad I_{D} = -2.8 \text{A}$		34	55	mΩ
	On–Resistance (Q2)	$V_{GS} = -2.5 \text{ V}, \qquad I_{D} = -2.5 \text{ A}$		45	70	
		$V_{GS} = -1.8 \text{ V}, \qquad I_{D} = -2.0 \text{ A}$		64	100	
R <sub>DS(on)</sub>	Static Drain–Source	$V_{GS} = 4.5 \text{ V}, \qquad I_{D} = 0.4 \text{A}$		3.1	4	Ω
	On–Resistance (Q1)	$V_{GS} = 2.7 \text{ V}, \qquad I_D = 0.2 \text{ A}$		3.8	5	

**Drain-Source Diode Characteristics and Maximum Ratings** 

ls	Maximum Continuous Drain–Source Diode Forward Current			-0.6	Α
V <sub>SD</sub>	Drain-Source Diode Forward	$V_{ON/OFF} = 0 \text{ V}, I_S = -0.6 \text{ A} \text{ (Note 2)}$		-1.2	V
	Voltage				

- Notes:
  1. R <sub>0.JA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R <sub>0.JC</sub> is guar anteed by design while R <sub>0.JA</sub> is determined by the user's board design.
- 2. Pulse Test: Pulse Width < 300 $\mu$ s, Duty Cycle < 2.0%.

### FDC6331L Load Switch Application Circuit



External Component Recommendation:

For additional in-rush current control, R2 and C1 can be added. For more information, see application note AN1030.

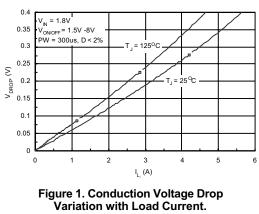
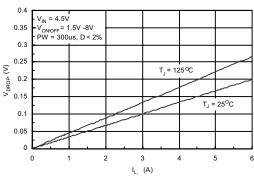


Figure 2. Conduction Voltage Drop Variation with Load Current.



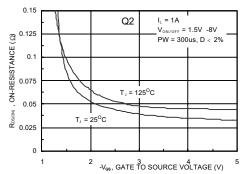


Figure 3. Conduction Voltage Drop Variation with Load Current.

Figure 4. On-Resistance Variation With Input Voltage

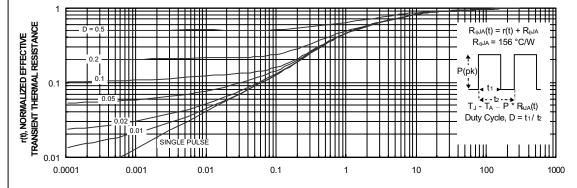


Figure 5. Transient Thermal Response Curve.

Thermal characterization performed on the conditions described in Note 2. Transient thermal response will change depends on the circuit board design.





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