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# FQD6N40C

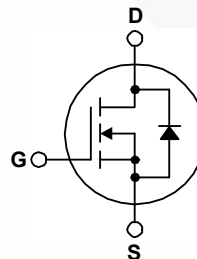
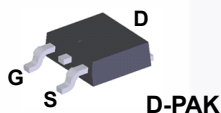
## N-Channel QFET® MOSFET 400 V, 4.5 A, 1.0 Ω

### Features

- 4.5 A, 400 V,  $R_{DS(on)} = 1.0 \Omega$  (Max.) @  $V_{GS} = 10 \text{ V}$ ,  $I_D = 2.25 \text{ A}$
- Low Gate Charge (Typ. 16 nC)
- Low  $C_{rss}$  (Typ. 15 pF)
- 100% Avalanche Tested

### Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.



### Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted.

| Symbol         | Parameter                                                                     | FQD6N40CTM  | Unit                |
|----------------|-------------------------------------------------------------------------------|-------------|---------------------|
| $V_{DSS}$      | Drain-Source Voltage                                                          | 400         | V                   |
| $I_D$          | Drain Current - Continuous ( $T_C = 25^\circ\text{C}$ )                       | 4.5         | A                   |
|                | - Continuous ( $T_C = 100^\circ\text{C}$ )                                    | 2.7         | A                   |
| $I_{DM}$       | Drain Current - Pulsed (Note 1)                                               | 18          | A                   |
| $V_{GSS}$      | Gate-Source Voltage                                                           | $\pm 30$    | V                   |
| $E_{AS}$       | Single Pulsed Avalanche Energy (Note 2)                                       | 270         | mJ                  |
| $I_{AR}$       | Avalanche Current (Note 1)                                                    | 4.5         | A                   |
| $E_{AR}$       | Repetitive Avalanche Energy (Note 1)                                          | 4.8         | mJ                  |
| $dv/dt$        | Peak Diode Recovery $dv/dt$ (Note 3)                                          | 4.5         | V/ns                |
| $P_D$          | Power Dissipation ( $T_A = 25^\circ\text{C}$ )*                               | 2.5         | W                   |
|                | Power Dissipation ( $T_C = 25^\circ\text{C}$ )                                | 48          | W                   |
|                | - Derate above $25^\circ\text{C}$                                             | 0.38        | W/ $^\circ\text{C}$ |
| $T_J, T_{STG}$ | Operating and Storage Temperature Range                                       | -55 to +150 | $^\circ\text{C}$    |
| $T_L$          | Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds | 300         | $^\circ\text{C}$    |

### Thermal Characteristics

| Symbol          | Parameter                                                                              | FQD6N40CTM | Unit               |
|-----------------|----------------------------------------------------------------------------------------|------------|--------------------|
| $R_{\theta JC}$ | Thermal Resistance, Junction-to-Case, Max.                                             | 2.6        | $^\circ\text{C/W}$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient (minimum pad of 2 oz copper), Max.             | 110        |                    |
|                 | Thermal Resistance, Junction-to-Ambient (* 1 in <sup>2</sup> pad of 2 oz copper), Max. | 50         |                    |

## Package Marking and Ordering Information

| Device Marking | Device     | Package | Reel Size | Tape Width | Quantity   |
|----------------|------------|---------|-----------|------------|------------|
| FQD6N40C       | FQD6N40CTM | D-PAK   | 330 mm    | 16 mm      | 2500 units |

## Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted.

| Symbol                                                 | Parameter                                             | Test Conditions                                                      | Min      | Typ  | Max  | Unit |
|--------------------------------------------------------|-------------------------------------------------------|----------------------------------------------------------------------|----------|------|------|------|
| Off Characteristics                                    |                                                       |                                                                      |          |      |      |      |
| BV <sub>DSS</sub>                                      | Drain-Source Breakdown Voltage                        | V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA                       | 400      | --   | --   | V    |
| ΔBV <sub>DSS</sub> / ΔT <sub>J</sub>                   | Breakdown Voltage Temperature Coefficient             | I <sub>D</sub> = 250 μA, Referenced to 25°C                          | --       | 0.54 | --   | V/°C |
| I <sub>DSS</sub>                                       | Zero Gate Voltage Drain Current                       | V <sub>DS</sub> = 400 V, V <sub>GS</sub> = 0 V                       | --       | --   | 1    | μA   |
|                                                        |                                                       | V <sub>DS</sub> = 320 V, T <sub>C</sub> = 125°C                      | --       | --   | 10   | μA   |
| I <sub>GSSF</sub>                                      | Gate-Body Leakage Current, Forward                    | V <sub>GS</sub> = 30 V, V <sub>DS</sub> = 0 V                        | --       | --   | 100  | nA   |
| I <sub>GSSR</sub>                                      | Gate-Body Leakage Current, Reverse                    | V <sub>GS</sub> = -30 V, V <sub>DS</sub> = 0 V                       | --       | --   | -100 | nA   |
| On Characteristics                                     |                                                       |                                                                      |          |      |      |      |
| V <sub>GS(th)</sub>                                    | Gate Threshold Voltage                                | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA          | 2.0      | --   | 4.0  | V    |
| R <sub>DS(on)</sub>                                    | Static Drain-Source On-Resistance                     | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2.25A                       | --       | 0.83 | 1    | Ω    |
| g <sub>FS</sub>                                        | Forward Transconductance                              | V <sub>DS</sub> = 40 V, I <sub>D</sub> = 2.25A                       | --       | 4.7  | --   | S    |
| Dynamic Characteristics                                |                                                       |                                                                      |          |      |      |      |
| C <sub>iss</sub>                                       | Input Capacitance                                     | V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1.0 MHz           | --       | 480  | 625  | pF   |
| C <sub>oss</sub>                                       | Output Capacitance                                    |                                                                      | --       | 80   | 105  | pF   |
| C <sub>rss</sub>                                       | Reverse Transfer Capacitance                          |                                                                      | --       | 15   | 20   | pF   |
| Switching Characteristics                              |                                                       |                                                                      |          |      |      |      |
| t <sub>d(on)</sub>                                     | Turn-On Delay Time                                    | V <sub>DD</sub> = 200 V, I <sub>D</sub> = 6A, R <sub>G</sub> = 25 Ω  | --       | 13   | 35   | ns   |
| t <sub>r</sub>                                         | Turn-On Rise Time                                     |                                                                      | --       | 65   | 140  | ns   |
| t <sub>d(off)</sub>                                    | Turn-Off Delay Time                                   |                                                                      | --       | 21   | 55   | ns   |
| t <sub>f</sub>                                         | Turn-Off Fall Time                                    |                                                                      | (Note 4) | --   | 38   | 85   |
| Q <sub>g</sub>                                         | Total Gate Charge                                     | V <sub>DS</sub> = 320 V, I <sub>D</sub> = 6A, V <sub>GS</sub> = 10 V | --       | 16   | 20   | nC   |
| Q <sub>gs</sub>                                        | Gate-Source Charge                                    |                                                                      | --       | 2.3  | --   | nC   |
| Q <sub>gd</sub>                                        | Gate-Drain Charge                                     |                                                                      | (Note 4) | --   | 8.2  | --   |
| Drain-Source Diode Characteristics and Maximum Ratings |                                                       |                                                                      |          |      |      |      |
| I <sub>S</sub>                                         | Maximum Continuous Drain-Source Diode Forward Current |                                                                      | --       | --   | 4.5  | A    |
| I <sub>SM</sub>                                        | Maximum Pulsed Drain-Source Diode Forward Current     |                                                                      | --       | --   | 18   | A    |
| V <sub>SD</sub>                                        | Drain-Source Diode Forward Voltage                    | V <sub>GS</sub> = 0 V, I <sub>S</sub> = 4.5 A                        | --       | --   | 1.4  | V    |
| t <sub>rr</sub>                                        | Reverse Recovery Time                                 | V <sub>GS</sub> = 0 V, I <sub>S</sub> = 6 A,                         | --       | 230  | --   | ns   |
| Q <sub>rr</sub>                                        | Reverse Recovery Charge                               | di <sub>F</sub> / dt = 100 A/μs                                      | --       | 1.7  | --   | μC   |

### NOTES:

1. Repetitive Rating : Pulse width limited by maximum junction temperature.
2.  $L = 13.7\text{ mH}$ ,  $I_{AS} = 6\text{ A}$ ,  $V_{DD} = 50\text{ V}$ ,  $R_G = 25\text{ }\Omega$ , starting  $T_J = 25^\circ\text{C}$ .
3.  $I_{SD} \leq 6\text{ A}$ ,  $di/dt \leq 200\text{ A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , starting  $T_J = 25^\circ\text{C}$ .
4. Essentially independent of operating temperature.

## Typical Characteristics

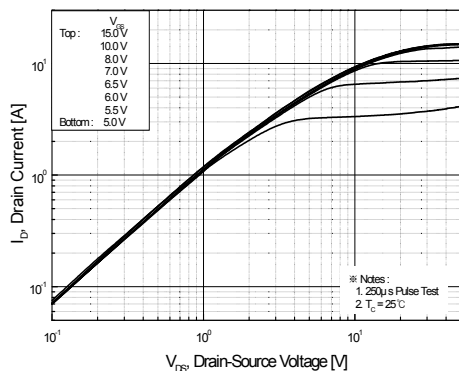


Figure 1. On-Region Characteristics

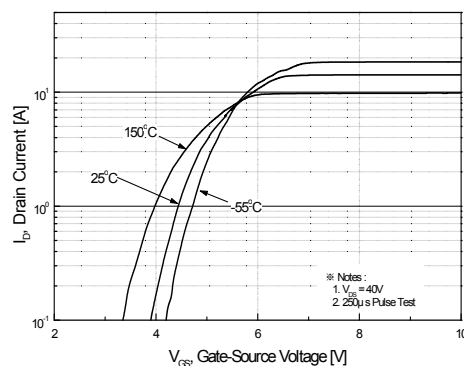


Figure 2. Transfer Characteristics

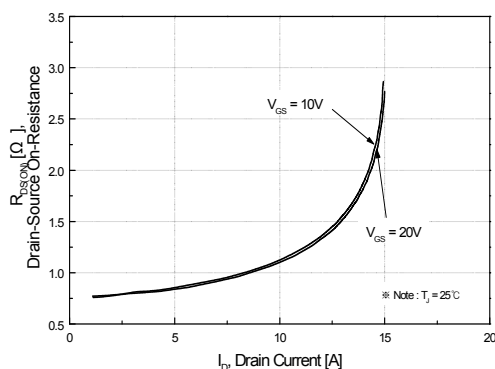


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

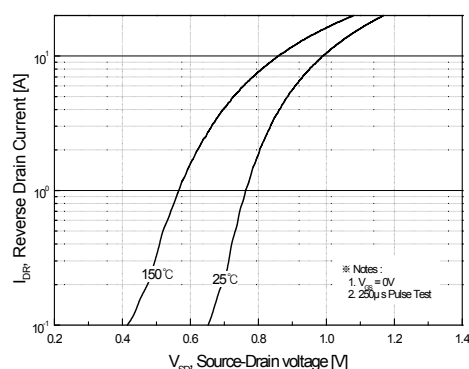


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

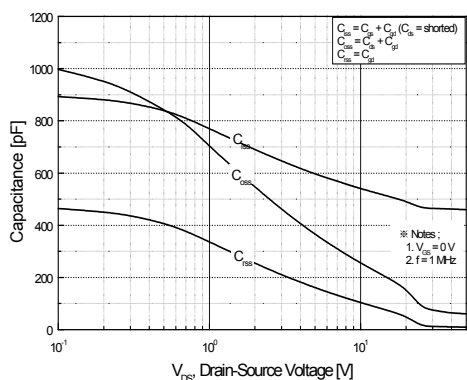


Figure 5. Capacitance Characteristics

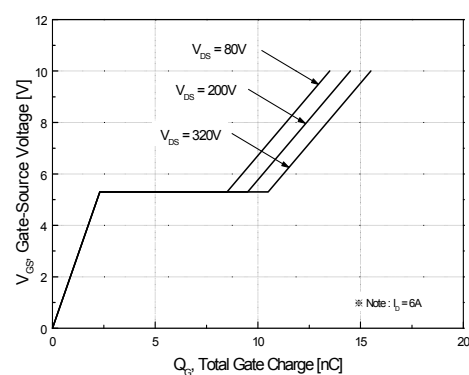


Figure 6. Gate Charge Characteristics

## Typical Characteristics (Continued)

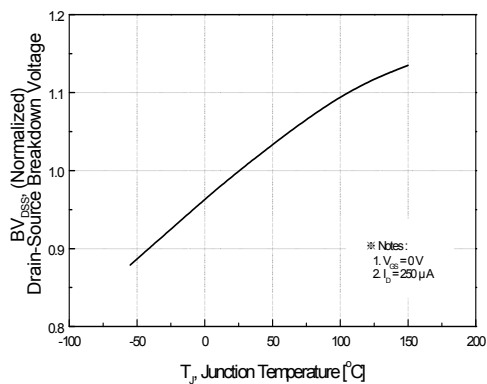


Figure 7. Breakdown Voltage Variation vs Temperature

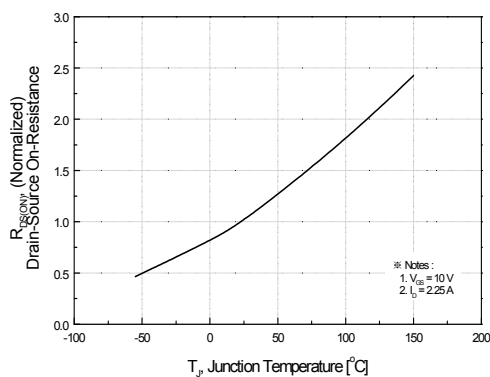


Figure 8. On-Resistance Variation vs Temperature

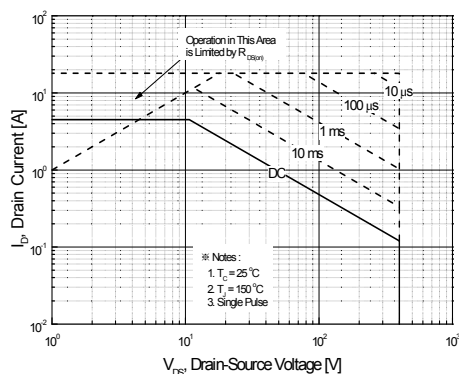


Figure 9. Maximum Safe Operating Area

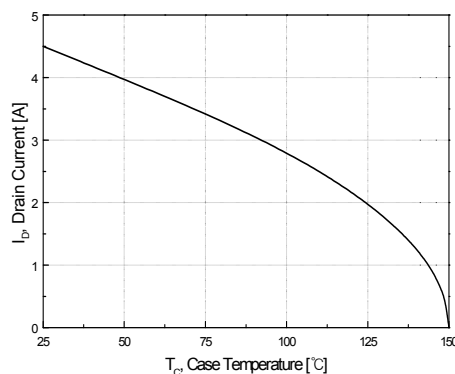


Figure 10. Maximum Drain Current vs Case Temperature

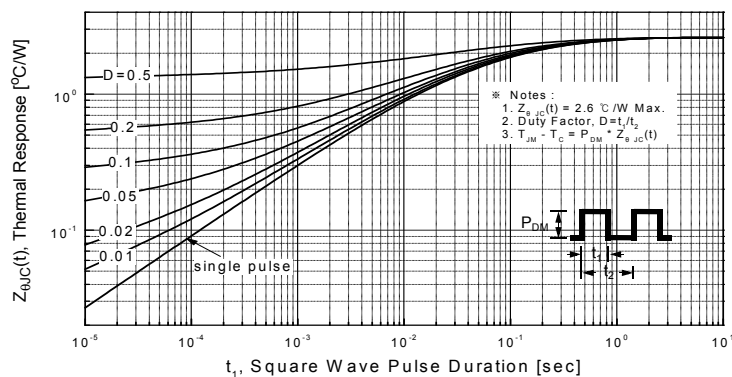


Figure 11. Transient Thermal Response Curve

Figure 12. Gate Charge Test Circuit & Waveform

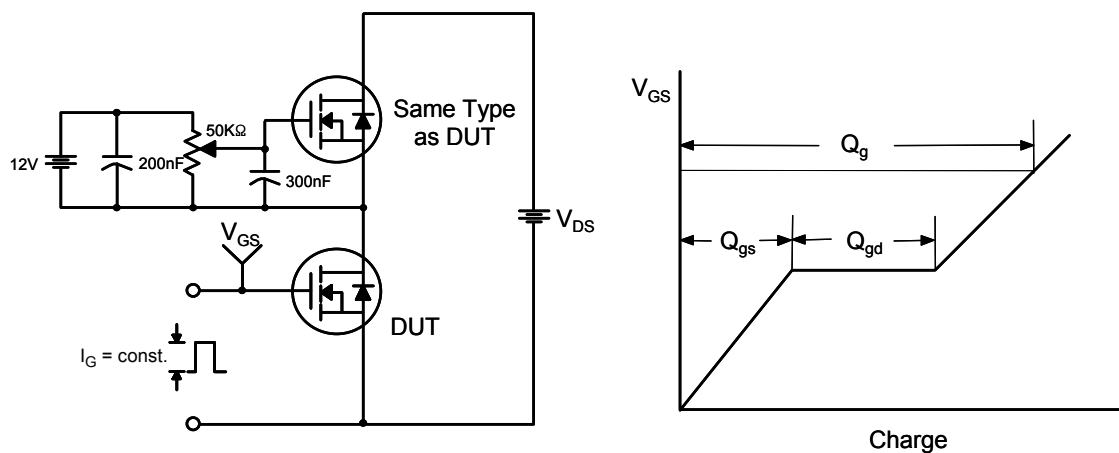


Figure 13. Resistive Switching Test Circuit & Waveforms

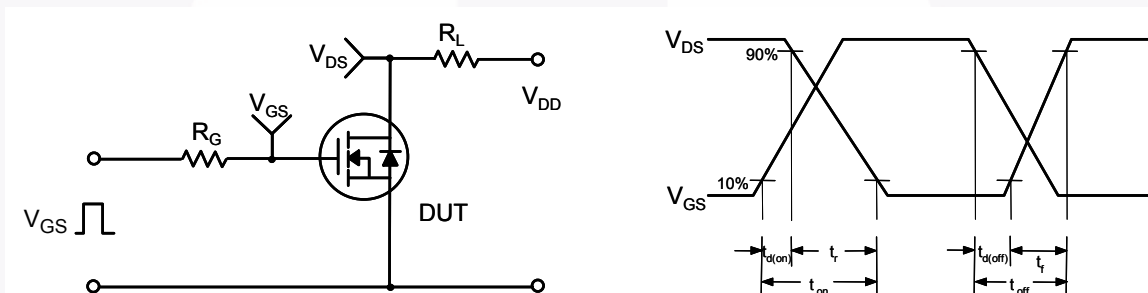


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

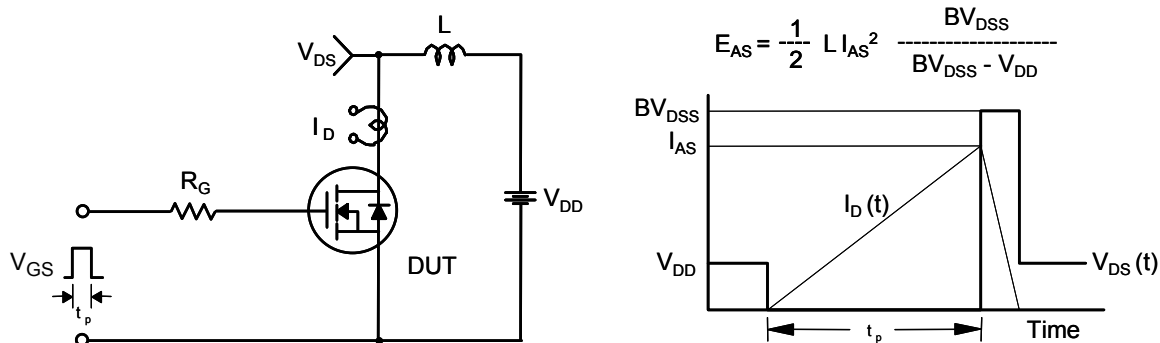
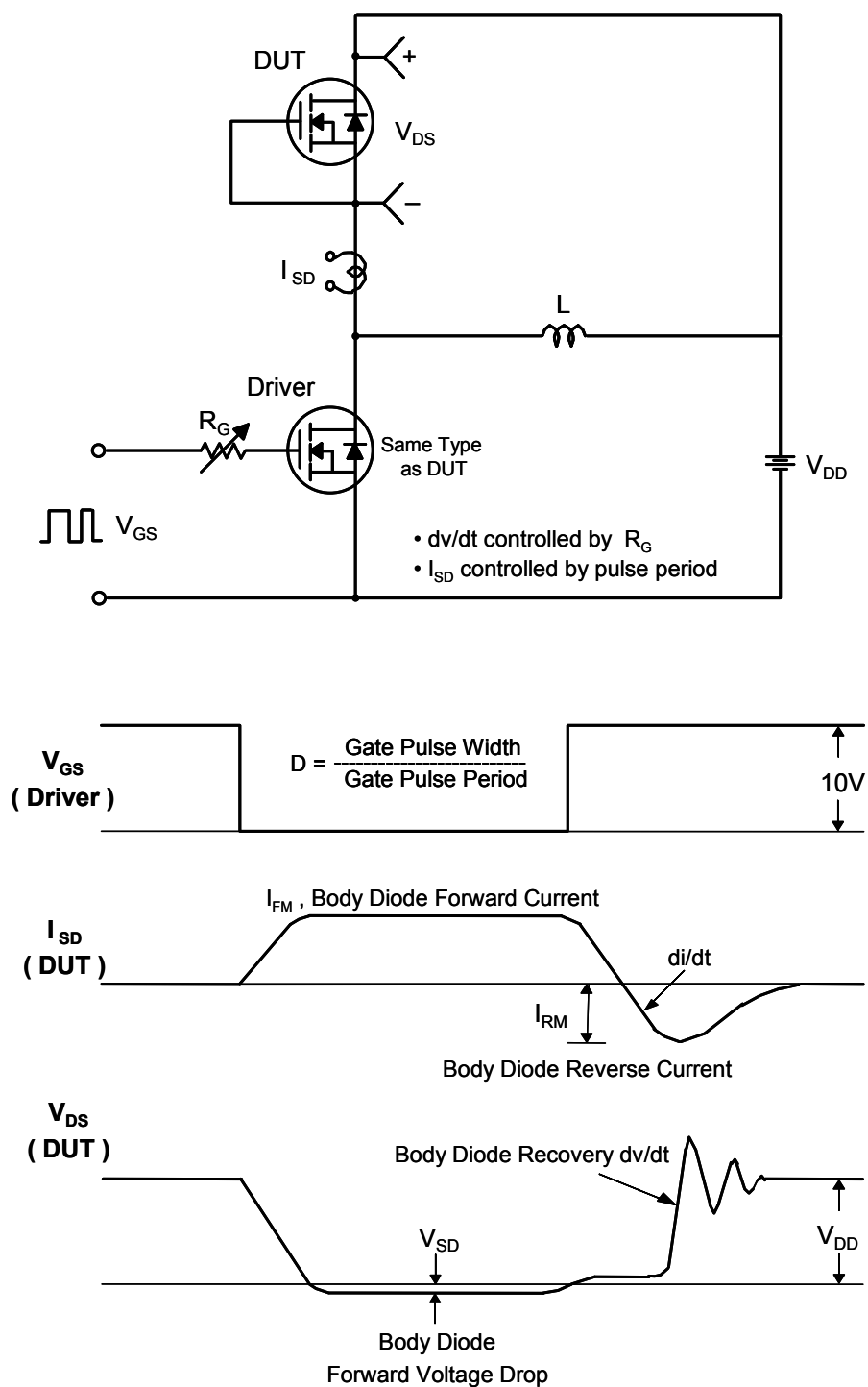
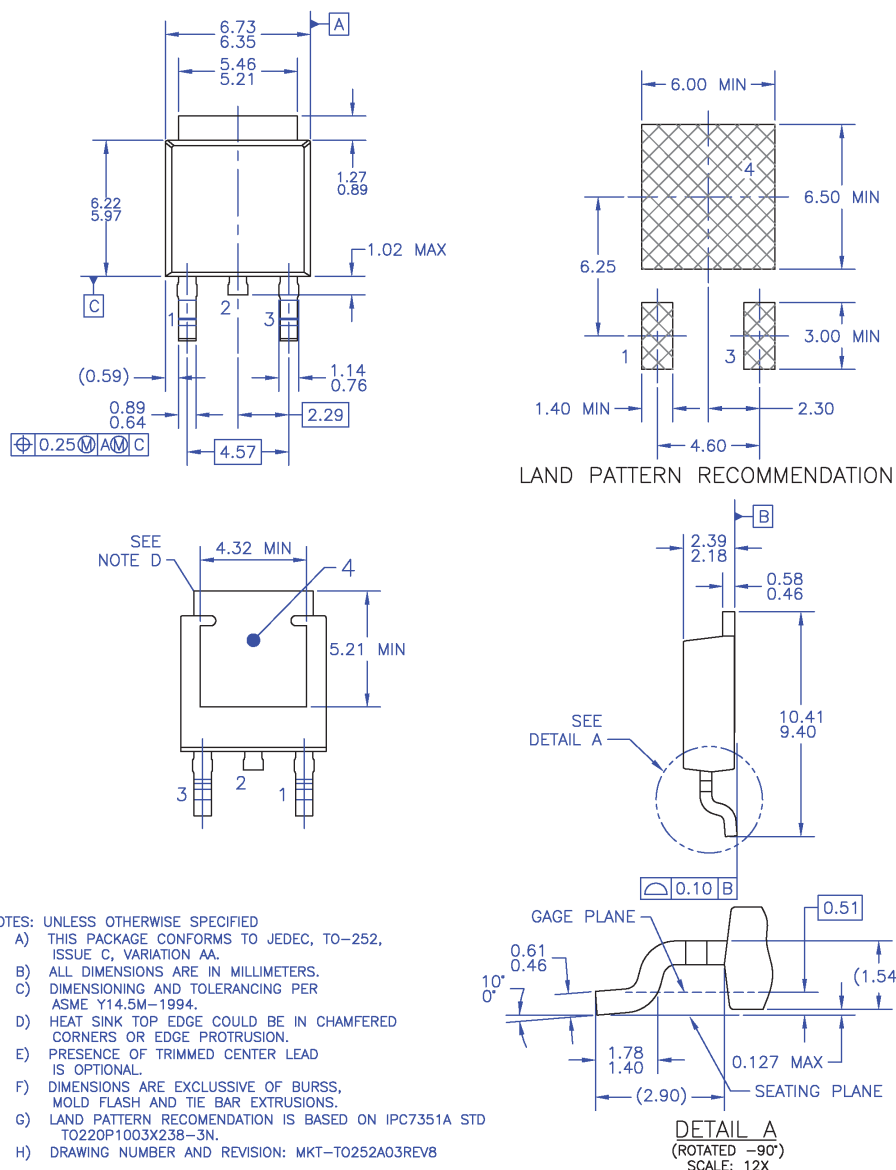


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms



## Mechanical Dimensions

## TO-252 3L (DPAK)



**Figure 16. TO252 (D-PAK), Molded, 3 Lead, Option AA&AB**

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Dimension in Millimeters





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| BitSiC™                                                                           | Global Power ResourceSM                         | Programable Active Droop™ | TinyBoost®                                                                          |
| Build it Now™                                                                     | GreenBridge™                                    | QFET®                     | TinyBuck®                                                                           |
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