

GA01PNS80-220

Silicon Carbide PiN Diode

V_{RRM}	=	8.0 kV
I _{F (Tc=25°C)}	=	2 A

Features

- 8 kV blocking
- 175 °C operating temperature
- Fast turn off characteristics
- Soft reverse recovery characteristics
- Ultra-Fast high temperature switching

Package

• RoHS Compliant



Advantages

- · Reduced stacking
- · Reduced system complexity/Increased reliability

Applications

- Voltage Multiplier
- Ignition/Trigger Circuits
- Oil/Downhole
- Lighting
- Defense

Maximum Ratings at T_i = 175 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values	Unit
Repetitive peak reverse voltage	V_{RRM}		8	kV
Continuous forward current	I _F		2	А
RMS forward current	I _{F(RMS)}		1	Α
Operating and storage temperature	T_j , T_stg		-55 to 175	°C

Electrical Characteristics at T_j = 175 °C, unless otherwise specified

Parameter	Symbol	Conditions		Values		Unit
		Conditions	min.	typ.	max.	Unit
Diode forward voltage	V _F	$I_F = 2 \text{ A}, T_j = 25 \text{ °C}$		6.1		V
Diode loi ward voltage	VF	I _F = 2 A, T _j = 175 °C		4.7		V
Reverse current	I _R	$V_R = 8 \text{ kV}, T_j = 25 ^{\circ}\text{C}$		4		^
Reverse current		$V_R = 8 \text{ kV}, T_j = 175 ^{\circ}\text{C}$		4		μΑ
Total reverse recovery charge	Q _{rr}	$I_F \le I_{F,MAX}$ $V_R = 1000$	/	558		nC
		-II /-I4 70 A/ IF - 1.0 /\	,			
Switching time	ts	$T_j = 175 \text{ °C}$ $V_R = 1000 \text{ °C}$ $V_R = 1000 \text{ °C}$	/	< 236		ns
		V _R = 1 V, f = 1 MHz, T _j = 25 °C		26		
Total capacitance	С	$V_R = 400 \text{ V}, f = 1 \text{ MHz}, T_j = 25 \text{ °C}$		5		pF
		$V_R = 1000 \text{ V}, f = 1 \text{ MHz}, T_j = 25 ^{\circ}\text{C}$;	4		
Total capacitive charge	Q_{C}	$V_R = 1000 \text{ V}, f = 1 \text{ MHz}, T_j = 25 ^{\circ}\text{C}$	}	5.4		nC

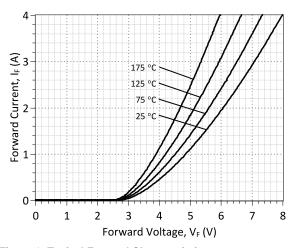


Figure 1: Typical Forward Characteristics

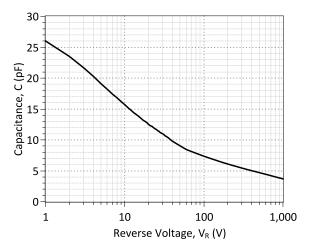


Figure 3: Typical Junction Capacitance vs Reverse Voltage Characteristics

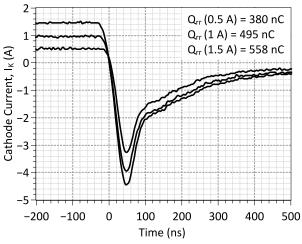


Figure 5: Typical Turn Off Characteristics at $T_{\rm j}$ = 175°C and $V_{\rm R}$ = 1000 V

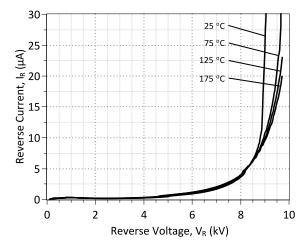


Figure 2: Typical Reverse Characteristics at 25°C

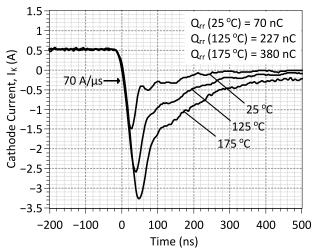


Figure 4: Typical Turn Off Characteristics at I_{k} = 0.5 A and V_{R} = 1000 V

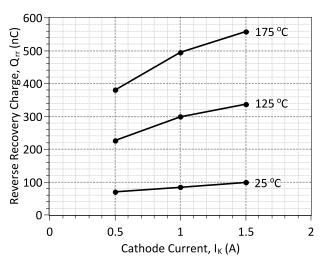


Figure 6: Reverse Recovery Charge vs Cathode Current

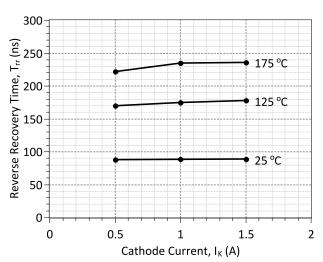
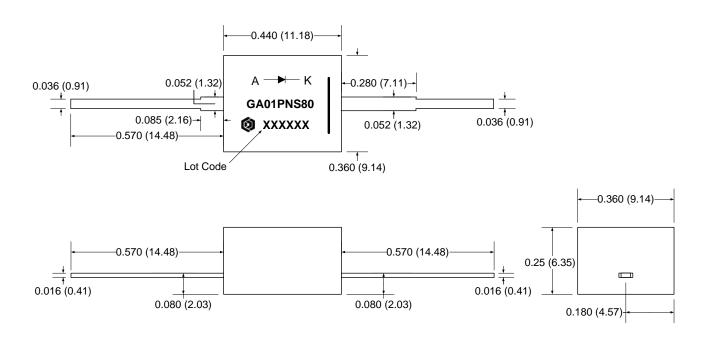


Figure 7: Reverse Recovery Time vs Cathode Current

Package Dimensions:

PACKAGE OUTLINE



NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS



GA01PNS80-220

Revision History			
Date	Revision	Comments	Supersedes
2015/04/30	1	Updated Electrical Characteristics	
2014/11/07	0	Initial release	

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SPICE Model Parameters

This is a secure document. Please copy this code from the SPICE model PDF file on our website (http://www.genesicsemi.com/images/products_sic/thyristor/GA01PNS80-220_SPICE.pdf) into LTSPICE (version 4) software for simulation of the GA01PNS80-220.

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MODEL OF GeneSiC Semiconductor Inc.
     $Revision: 1.1
                                $
     $Date: 30-APR-2015
     GeneSiC Semiconductor Inc.
     43670 Trade Center Place Ste. 155
     Dulles, VA 20166
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* These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY
* OF ANY KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED
* TO ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A
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* Models accurate up to 2 times rated drain current.
 Start of GA01PNS80-220 SPICE Model
. MODEL GA01PNS80 D
+ IS
     9.2491e-015
         1.02512
+ RS
          3.3373
+ N
+ IKF
         0.00011784
          3.23
+ EG
         25
+ XTI
+ TRS1
         -0.0024
+ CJO
          2.7E-11
+ VJ
          2.304
          0.376
+ M
+ FC
          0.5
         8000
+ BV
+ IBV
         1.00E-03
+ VPK
          8000
+ IAVE
+ TYPE
          SiC PiN
+ MFG
         GeneSiC Semi
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* End of GA01PNS80-220 SPICE Model

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