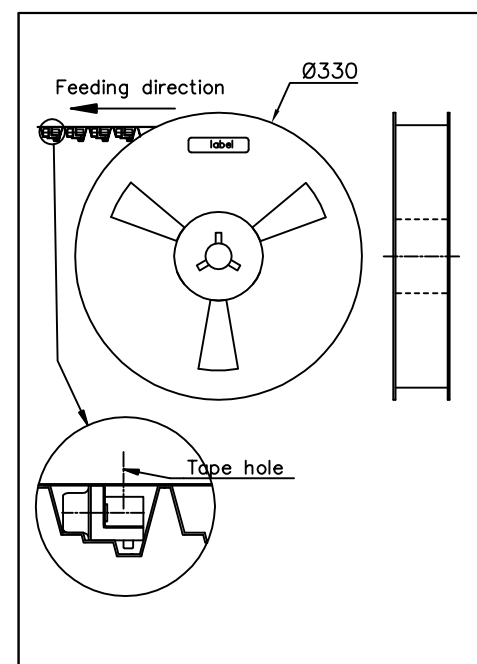


The image contains two technical drawings of PCB drilling details. The left drawing is for 14 & 15 POSN configurations, showing a rectangular board with two circular mounting holes. Dimensions include overall width B, distance E from the left hole to the start of the component holes, distance F from the end of the component holes to the right hole, and distance G between individual component holes. The component holes are arranged in three rows. The top and bottom rows have a diameter of $\phi 3.20$, and the middle row has a diameter of $\phi 0.90$ MIN. The board thickness is indicated as 2.54. The right drawing is for 26 & 44 POSN configurations, showing a similar board but with a break symbol (two parallel diagonal lines) in the middle of the component hole array, indicating a longer board. Dimensions B, E, F, and G are also shown for this configuration.

[illegible]

mat'l. code					surface ISO 1302		tolerance ISO 406 ISO 1101	
ltr	ecn no	dr	date		tolerances unless	otherwise specified		
D	I06-0074	GVJ	2006-06-08		angles 0°±1°	linear		
E	I09-0202	NG	2009-09-24				.00±0.1	
							.000±.00X	
					dr	GEORGE.V.JOSEPH	2004-10-12	
					engr	NIRMAL.GEORGE	2009-09-24	
					chr	NIRMAL.GEORGE	2009-09-24	
					appd	SUDHIR.VARMA	2009-09-24	
sheet index	revision	D	E					
	sheet	1	1					

Packaging Details
Tape according to EIA-481-B
Anti static Property-
As per EIA-541
Reel ø 330mm
Qty – 75 nos/ reel
Empty Pockets
3 pockets min.and 300mm
extra cover tape at begining
and 5 pockets min. at the end

A horizontal number line is shown, ranging from 3 to 6. Major tick marks are labeled 4, 5, and 6. Between each major tick, there are 10 smaller tick marks, representing increments of 0.1. A point is marked with a dot at the 5th small tick mark after 4, which represents the value 4.5.

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[FCI / Amphenol:](#)

[ICD26S13E6GV12LF](#)