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# Low Profile, High Current IHLP® Inductors





Manufactured under one or more of the following: **US Patents**; **6,198,375/6,204,744/6,449,829/6,460,244.** Several foreign patents, and other patents pending.

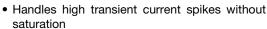
STANDARD ELECTRICAL SPECIFICATIONS					
L <sub>0</sub> INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (μH)	DCR TYP. 25 °C (mΩ)	DCR MAX. 25 °C (mΩ)	HEAT RATING CURRENT DC TYP. (A) <sup>(3)</sup>	SATURATION CURRENT DC TYP. (A) (4)	SRF TYP. (MHz)
0.10	3.6	3.9	17.0	45.0	239
0.22	4.9	5.2	15.0	22.0	145
0.33	7.6	8.2	12.0	25.0	125
0.47	8.9	9.4	11.5	21.0	98
0.68	11.2	12.4	10.0	15.0	77
1.0	18.9	20.0	7.0	16.0	62
2.2	45.6	50.1	4.2	9.5	39
3.3	79.2	85.5	3.3	8.5	30
4.7	108.0	116.6	2.8	5.0	28
5.6	113.0	122.0	2.5	4.5	24
6.8	139.0	150.0	2.4	4.3	21
10	184.0	199.0	2.3	4.0	20

### Notes

- (1) All test data is referenced to 25 °C ambient
- (2) Operating temperature range -55 °C to +125 °C
- (3) DC current (A) that will cause an approximate ΔT of 40 °C
- $^{(4)}\,$  DC current (A) that will cause  $L_0$  to drop approximately 20 %
- (5) The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

#### **FEATURES**

- Shielded construction
- Lowest DCR/µH, in this package size

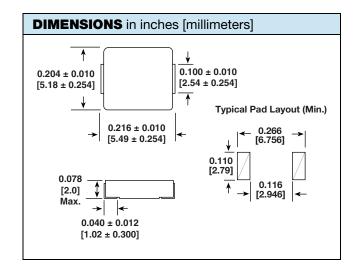


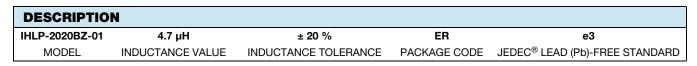


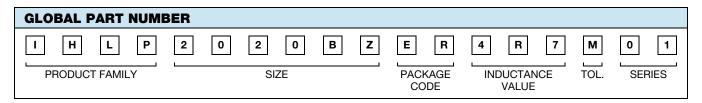
- Ultra low buzz noise, due to composite construction
- Excellent DC/DC energy storage up to 5 MHz.
  Filter inductor applications up to SRF (see "Standard Electrical Specifications" table)
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

### **APPLICATIONS**

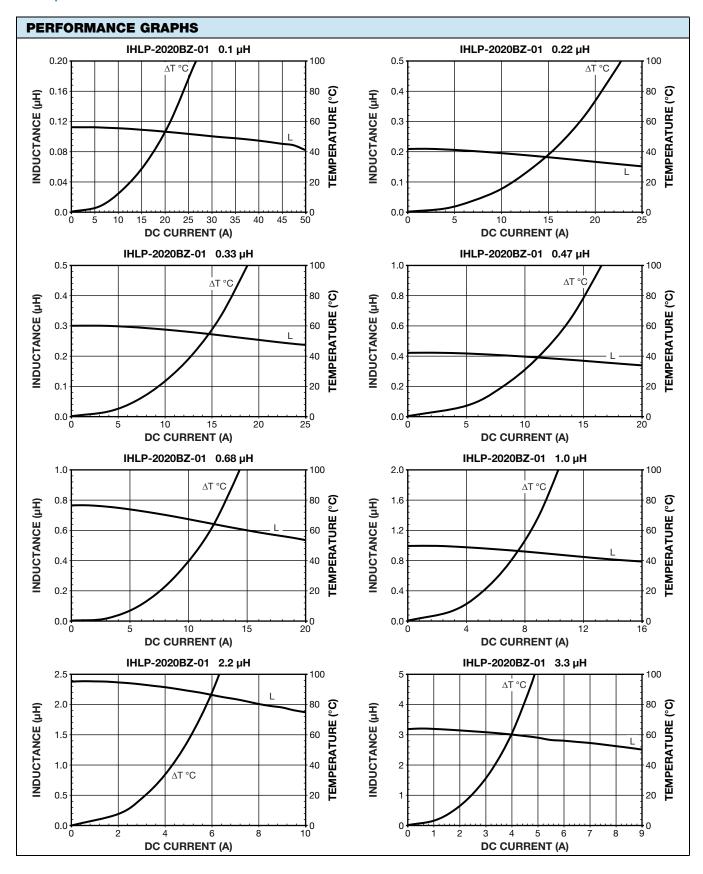
- PDA / notebook / desktop / server applications
- · High current POL converters
- · Low profile, high current power supplies
- · Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for Field Programmable Gate Array (FPGA)







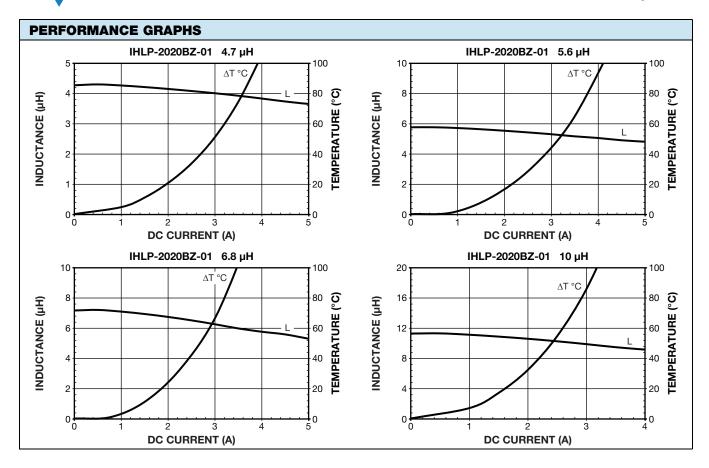




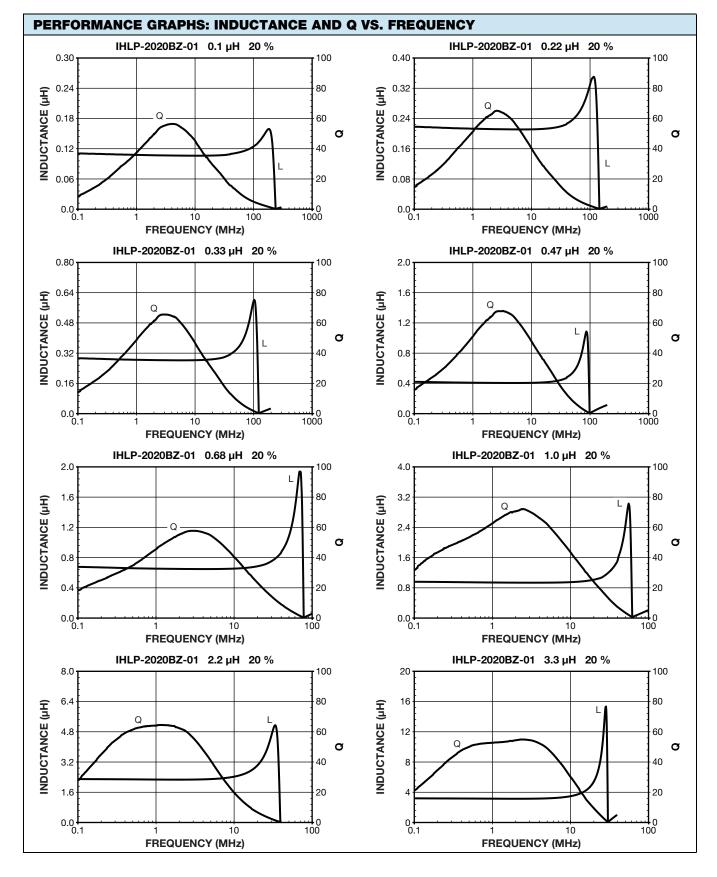


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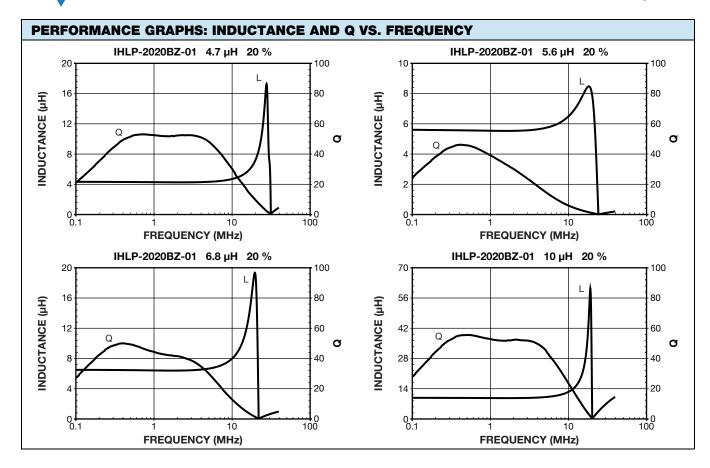






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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

Revision: 02-Oct-12 Document Number: 91000