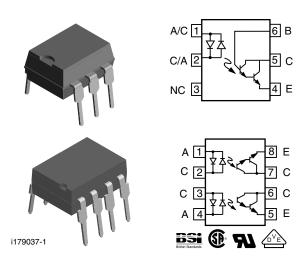


# Optocoupler, Photodarlington Output, AC Input, High Gain (Single, Dual Channel)



## **DESCRIPTION**

The IL755, ILD755 are bidirectional input optically coupled isolators. They consist of two gallium arsenide infrared emitting diodes coupled to a silicon NPN photodarlington per channel.

The IL755 is single channel Darlington optocoupler. The ILD755 has two isolated channels in a single DIP package.

## **FEATURES**

- · AC or polarity insensitive inputs
- · Built-in reverse polarity input protection
- · Industry standard DIP package
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC





## **APPLICATIONS**

 Designed for applications requiring detection or monitoring of AC signals

#### AGENCY APPROVALS

- UL1577, file no. E52744 system code H, double protection
- CSA 93751
- BSI IEC 60950; IEC 60065
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 (pending), available with option 1

ILD755-1X017

CQC

ORDERING INFORMATION							
I L X 7 5 5 - # X 0 # # T DIP-# Option 7  PART NUMBER CTR BIN PACKAGE OPTION TAPE AND REEL 7.62 mm > 0.7 mm							
	CTR (%)						
AGENCY CERTIFIED/PACKAGE	SINGLE CHANNEL, 6 PIN		DUAL CHANNEL, 8 PIN				
	± 2 mA	± 1 mA	± 2 mA	± 1 mA			
UL, CSA, BSI, CQC	≥ 750	≥ 1000	≥ 750	≥ 1000			
DIP-#	IL755-1	IL755-2	ILD755-1	ILD755-2			
SMD-#, option 7	IL755-1X007T (1)	IL755-2X007T	-	ILD755-2X007T			
VDE, UL, CSA, BSI, CQC	≥ 750	≥ 1000	≥ 750	≥ 1000			

#### Notes

DIP-#

SMD-#, option 7

· Additional options may be possible, please contact sales office.

IL755-1X001

(1) Also available in tubes; do not add "T" to end.



PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
INPUT					
Forward continuous current			I <sub>F</sub>	60	mA
Power dissipation			P <sub>diss</sub>	100	mW
Derate linearly from 25°C				1.33	mW/°C
OUTPUT					
Collector emitter breakdown voltage			BV <sub>CEO</sub>	60	V
Collector base breakdown voltage			BV <sub>CBO</sub>	60	V
		IL755-1		200	mW
Power dissipation		IL755-2	P <sub>diss</sub>	200	mW
rowei dissipation		ILD755-1	diss	150	mW
		ILD755-2		150	mW
		IL755-1		2.6	mW/°C
Derate linearly from 25°C		IL755-2		2.6	mW/°C
Derate linearly from 25 C		ILD755-1		2.0	mW/°C
		ILD755-2		2.0	mW/°C
COUPLER					
Isolation test voltage between emitter and detector	t = 1 s		V <sub>ISO</sub>	7500/5300	V <sub>AC peak</sub> /V <sub>RMS</sub>
Creepage distance				≥ 7	mm
Clearance distance				≥ 7	mm
		IL755-1		250	mW
Total power dissipation		IL755-2	P <sub>tot</sub>	250	mW
Total power dissipation		ILD755-1	Ftot	400	mW
		ILD755-2		400	mW
		IL755-1		3.0	mW/°C
Derate linearly from 25 °C		IL755-2		3.0	mW/°C
Derate illeany Ironi 25 C		ILD755-1		3.0	mW/°C
		ILD755-2		3.0	mW/°C
Storage temperature			T <sub>stg</sub>	- 55 to + 150	°C
Operating temperature			T <sub>amb</sub>	- 55 to + 100	°C
Lead soldering time at 260 °C				10	S

## Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
Forward voltage	$I_F = \pm 10 \text{ mA}$	V <sub>F</sub>		1.2	1.5	V	
OUTPUT							
Collector emitter breakdown voltage	$I_C = 1.0 \text{ mA}$	BV <sub>CEO</sub>	60	75		V	
Collector base breakdown voltage	I <sub>C</sub> = 10 μA	BV <sub>CBO</sub>	60	90		V	
Collector emitter leakage current	$V_{CE} = 10 \text{ V}, I_F = 0 \text{ A}$	I <sub>CEO</sub>		10	100	nA	
COUPLER							
Collector emitter saturation voltage	$I_C = 10 \text{ mA}, I_F = \pm 10 \text{ mA}$	V <sub>CEsat</sub>			1	V	

## Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Current transfer ratio	$I_F = \pm 2 \text{ mA}, V_{CE} = 5.0 \text{ V}$	IL755-1	CTR	750			%
	$I_F = \pm 2$ mA, $V_{CE} = 5.0$ V	ILD755-1	CTR	750			%
	$I_F = \pm 1 \text{ mA}, V_{CE} = 5.0 \text{ V}$	IL755-2	CTR	1000			%
	$I_F = \pm 1 \text{ mA}, V_{CE} = 5.0 \text{ V}$	ILD755-2	CTR	1000			%

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Rise time	$V_{CC}$ = 10 V, $I_F$ = ± 2 mA, $R_L$ = 100 $\Omega$	IL755-1	t <sub>r</sub>		50		μs	
		ILD755-1	t <sub>r</sub>		50		μs	
Fall time	$V_{CC}$ = 10 V, $I_F$ = ± 2 mA, $R_L$ = 100 $\Omega$	IL755-1	t <sub>f</sub>		50		μs	
		ILD755-1	t <sub>f</sub>		50		μs	
Rise time	$V_{CC}$ = 10 V, $I_F$ = ± 1 mA, $R_L$ = 100 $\Omega$	IL755-2	t <sub>r</sub>		70		μs	
		ILD755-2	t <sub>r</sub>		70		μs	
Fall time	$V_{CC}$ = 10 V, $I_F$ = ± 1 mA, $R_L$ = 100 $\Omega$	IL755-2	t <sub>f</sub>		70		μs	
		ILD755-2	t <sub>f</sub>		70		μs	

## TYPICAL CHARACTERSITICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

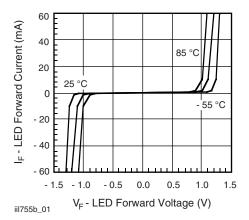


Fig. 1 - LED Forward Current vs. Forward Voltage

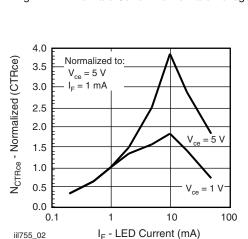


Fig. 2 - Normalized Non-Saturated and Saturated CTR<sub>CE</sub> vs. LED Current

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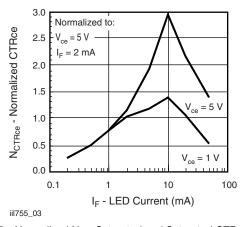


Fig. 3 - Normalized Non-Saturated and Saturated CTR $_{\mbox{\scriptsize CE}}$  vs. LED Current

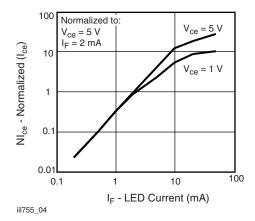


Fig. 4 - Normalized Non-Saturated and Saturated I<sub>CE</sub> vs. LED Current

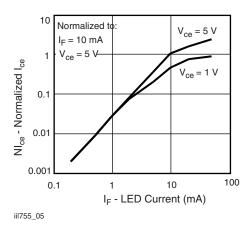


Fig. 5 - Normalized Non-Saturated and Saturated Collector-Emitter Current vs. LED Current

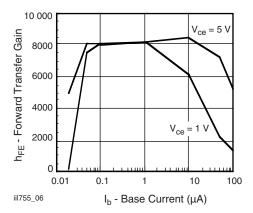


Fig. 6 - Non-Saturated and Saturated hFE vs. Base Current

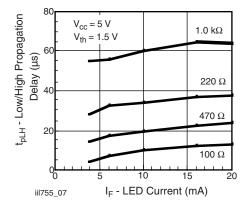


Fig. 7 - Low to High Propagation Delay vs. Collector Load Resistance and LED Current

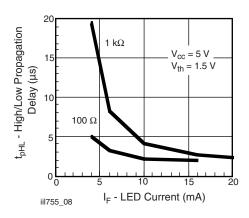


Fig. 8 - High to Low Propagation Delay vs. Collector Load Resistance and LED Current

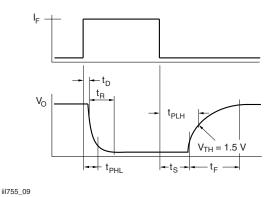


Fig. 9 - Switching Waveform

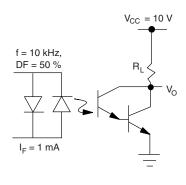
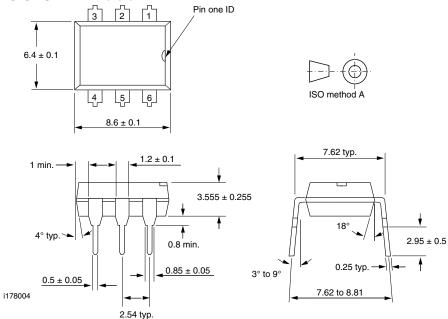


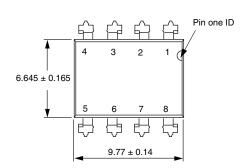
Fig. 10 - Test Circuit, Saturated and Non-Saturated Operation

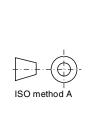
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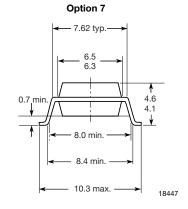


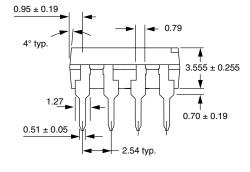
## **PACKAGE DIMENSIONS** in millimeters

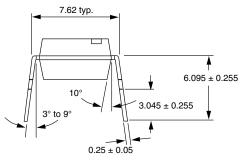






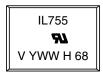






#### i178006

## **PACKAGE MARKING** (example)



### **Notes**

- The VDE logo is only marked on option 1 parts.
- Tape and reel suffix (T) is not part of the package marking.



# **Legal Disclaimer Notice**

Vishay

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