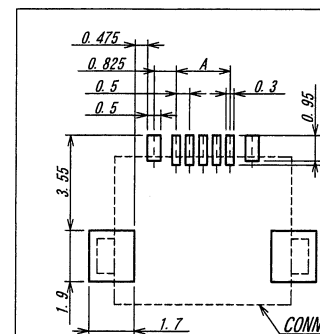
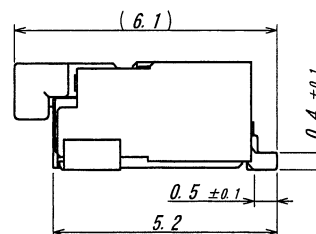
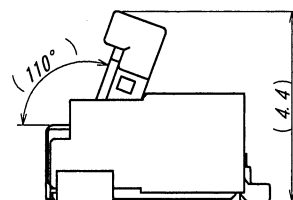
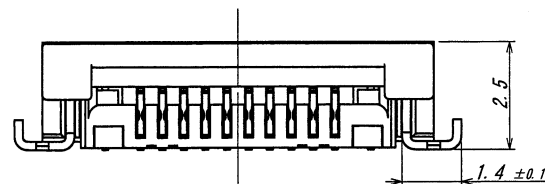
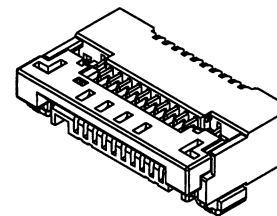




RECOMMENDED PCB SIZE
(TOLERANCES ± 0.05)



NOTE
1. コプラナリティ/COPLANARITY: 0.1MAX.

△	509A/	RELEASED	J.O	7.T '15-12-10
TIR 記号	ECN No	REVISION RECORD	DR	CHK DATE 年月日

③ニッケル下地めっき/OVER Ni PLATE:2.0 μ mMIN.
金めっき/Au PLATE:0.1 μ mMIN.

④ニッケル下地めっき/OVER Ni PLATE:2.0 μ mMIN.
金めっき/Au PLATE:0.03 μ mMIN. (テール部/TAIL AREA)

⑤⑥ニッケル下地めっき/OVER Ni PLATE:0.5~2 μ m.
銀半光沢めっき/TIN MATT PLATE:3.0 μ mMIN.

The drawing shows a tapered shaft with a central hole. The front view on the left is a circle with a smaller concentric circle inside, representing the hole. A horizontal centerline passes through the center of both circles. The side view on the right shows the tapered profile of the shaft, with a horizontal centerline extending from the front view. The two views are connected by a dashed line, indicating they represent the same object from different perspectives.

IRISO ELECTRONICS CO., LTD
イリソ電子工業株式会社

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工業株式会社

110-412001-774

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