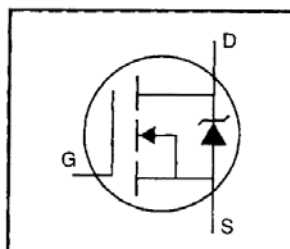


IRFBG20PbF

- Dynamic dv/dt Rating
- Repetitive Avalanche Rated
- Fast Switching
- Ease of Paralleling
- Simple Drive Requirements
- Lead-Free



$$V_{DSS} = 1000V$$

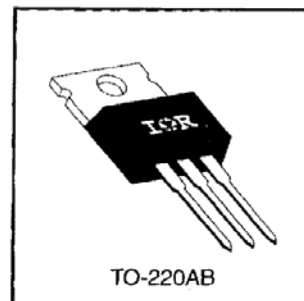
$$R_{DS(on)} = 11\Omega$$

$$I_D = 1.4A$$

Description

Third Generation HEXFETs from International Rectifier provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220 package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 watts. The low thermal resistance and low package cost of the TO-220 contribute to its wide acceptance throughout the industry.



Absolute Maximum Ratings

	Parameter	Max.	Units
I_D @ $T_C = 25^\circ C$	Continuous Drain Current, V_{GS} @ 10 V	1.4	A
I_D @ $T_C = 100^\circ C$	Continuous Drain Current, V_{GS} @ 10 V	0.86	
I_{DM}	Pulsed Drain Current ①	5.6	
P_D @ $T_C = 25^\circ C$	Power Dissipation	54	W
	Linear Derating Factor	0.43	W/°C
V_{GS}	Gate-to-Source Voltage	± 20	V
E_{AS}	Single Pulse Avalanche Energy ②	200	mJ
I_{AR}	Avalanche Current ①	1.4	A
E_{AR}	Repetitive Avalanche Energy ①	5.4	mJ
dv/dt	Peak Diode Recovery dv/dt ③	1.0	V/ns
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C
	Soldering Temperature, for 10 seconds	300 (1.6mm from case)	
	Mounting Torque, 6-32 or M3 screw	10 lbf•in (1.1 N•m)	

Thermal Resistance

	Parameter	Min.	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case	—	—	2.3	°C/W
$R_{\theta CS}$	Case-to-Sink, Flat, Greased Surface	—	0.50	—	
$R_{\theta JA}$	Junction-to-Ambient	—	—	62	

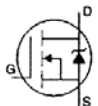
IRFBG20PbF

International
IR Rectifier

Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Test Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	1000	—	—	V	$V_{GS}=0V, I_D=250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	1.2	—	V/ $^\circ\text{C}$	Reference to 25°C , $I_D=1\text{mA}$
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	—	—	11	Ω	$V_{GS}=10V, I_D=0.84A$ ④
$V_{GS(th)}$	Gate Threshold Voltage	2.0	—	4.0	V	$V_{DS}=V_{GS}, I_D=250\mu A$
g_{fs}	Forward Transconductance	1.0	—	—	S	$V_{DS}=50V, I_D=0.84A$ ④
I_{DSS}	Drain-to-Source Leakage Current	—	—	100	μA	$V_{DS}=1000V, V_{GS}=0V$
		—	—	500		$V_{DS}=800V, V_{GS}=0V, T_J=125^\circ\text{C}$
I_{GSS}	Gate-to-Source Forward Leakage	—	—	100	nA	$V_{GS}=20V$
	Gate-to-Source Reverse Leakage	—	—	-100		$V_{GS}=-20V$
Q_g	Total Gate Charge	—	—	38	nC	$I_D=1.4A$
Q_{gs}	Gate-to-Source Charge	—	—	4.9		$V_{DS}=400V$
Q_{gd}	Gate-to-Drain ("Miller") Charge	—	—	22		$V_{GS}=10V$ See Fig. 6 and 13 ④
$t_{d(on)}$	Turn-On Delay Time	—	9.4	—	ns	$V_{DD}=500V$
t_r	Rise Time	—	17	—		$I_D=1.4A$
$t_{d(off)}$	Turn-Off Delay Time	—	58	—		$R_G=18\Omega$
t_f	Fall Time	—	31	—		$R_D=370\Omega$ See Figure 10 ④
L_D	Internal Drain Inductance	—	4.5	—	nH	Between lead, 6 mm (0.25in.) from package and center of die contact
L_S	Internal Source Inductance	—	7.5	—		
C_{iss}	Input Capacitance	—	500	—	pF	$V_{GS}=0V$
C_{oss}	Output Capacitance	—	52	—		$V_{DS}=25V$
C_{rss}	Reverse Transfer Capacitance	—	17	—		$f=1.0\text{MHz}$ See Figure 5

Source-Drain Ratings and Characteristics

	Parameter	Min.	Typ.	Max.	Units	Test Conditions
I_S	Continuous Source Current (Body Diode)	—	—	1.4	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I_{SM}	Pulsed Source Current (Body Diode) ①	—	—	5.6		
V_{SD}	Diode Forward Voltage	—	—	1.5	V	$T_J=25^\circ\text{C}, I_S=1.4A, V_{GS}=0V$ ④
t_{rr}	Reverse Recovery Time	—	130	190	ns	$T_J=25^\circ\text{C}, I_F=1.4A$
Q_{rr}	Reverse Recovery Charge	—	0.46	0.69	μC	$di/dt=100A/\mu s$ ④
t_{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by L_S+L_D)				

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature (See Figure 11)
- ② $V_{DD}=50V$, starting $T_J=25^\circ\text{C}$, $L=193\text{mH}$, $R_G=25\Omega$, $I_{AS}=1.4A$ (See Figure 12)
- ③ $I_{SD}\leq 1.4A$, $di/dt\leq 60A/\mu s$, $V_{DD}\leq 600$, $T_J\leq 150^\circ\text{C}$
- ④ Pulse width $\leq 300\mu s$; duty cycle $\leq 2\%$.

International
IOR Rectifier



Diagram of an IRF1010 MOSFET package with labels:

- INTERNATIONAL RECTIFIER LOGO
- PART NUMBER
- DATE CODE
- YEAR 7 = 1997
- WEEK 19
- LINE C
- ASSEMBLY LOT CODE